

### 1A/60V Digital Transistor (with built-in resistor and zener diode)

Parameter	Value		
V <sub>CC</sub>	60±10V		
I <sub>C</sub>	1A		
R <sub>1</sub>	2.2kΩ		
$R_2$	10kΩ		

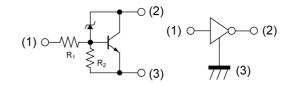
# Features

- 1)High DC current gain. (Min.300 at Vo/Io=2V/0.5A)
- 2)Low Vo(<sub>ON</sub>). (Typ.400mV at Io/I<sub>I</sub>=500mA/5mA)
- 3)Built-in zener diode gives strong protection against reverse surge by L-load. (an inductive load)

### Outline



### •Inner circuit



- (1) IN (BASE)
- (2) OUT (COLLECTOR)
- (3) GND (EMITTER)

### Application

INVERTER, INTERFACE, DRIVER

# Packaging specifications

Part No.	Package	Package size	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit.(pcs)	Marking
DTDG23YP	MPT3	4540	T100	180	12	1000	E02

# ● Absolute maximum ratings (T<sub>a</sub> = 25°C)

Parameter	Symbol	Values	Unit
Supply voltage	V <sub>CC</sub>	60±10	V
Input voltage	V <sub>IN</sub>	-6 to 40	V
Oallantan arment	I <sub>C</sub>	1	Α
Collector current	I <sub>CP</sub> *1	2	Α
Power dissipation	P <sub>D</sub> *2	1.5	W
Junction temperature	T <sub>j</sub>	150	°C
Range of storage temperature	T <sub>stg</sub>	-55 to +150	°C

# ● Electrical characteristics (T<sub>a</sub> = 25°C)

Davamatav	Cymah ol	Conditions	Values			Lloit	
Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit	
langut valtaga	$V_{I(off)}$	$V_{CC} = 5V, I_{O} = 100 \mu A$	-	-	0.3	V	
Input voltage	V <sub>I(on)</sub>	V <sub>O</sub> = 0.4V, I <sub>O</sub> = 100mA	2.0	-	-		
Output voltage	V <sub>O(on)</sub>	$I_{O}/I_{I} = 500 \text{mA} / 5 \text{mA}$	1	-	400	mV	
Input current	l <sub>l</sub>	V <sub>I</sub> = 5V	1	-	3.6	mA	
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> = 40V, V <sub>I</sub> = 0V	1	-	500	nA	
DC current gain	G <sub>I</sub>	V <sub>O</sub> = 2V, I <sub>O</sub> = 500mA	300	-	-	-	
Input resistance	R <sub>1</sub>	-	1.54	2.2	2.86	kΩ	
Emitter-base resistance	R <sub>2</sub>	-	7	10	13	kΩ	
Transition frequency	f <sub>T</sub> *3	V <sub>CE</sub> = 5V, I <sub>E</sub> = -100mA, f = 30MHz	-	80	-	MHz	

<sup>\*1</sup> Pw≦10msec. duty≦1/2

<sup>\*2</sup> Mounted on a ceramic board.(40×40×0.7mm)

<sup>\*3</sup> Characteristics of built-in transistor

### ● Electrical characteristic curves (T<sub>a</sub> =25°C)

Fig.1 Input Voltage vs. Output Current (ON Characteristics)

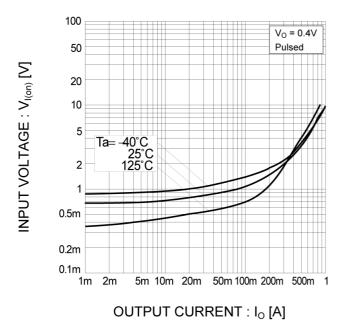


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

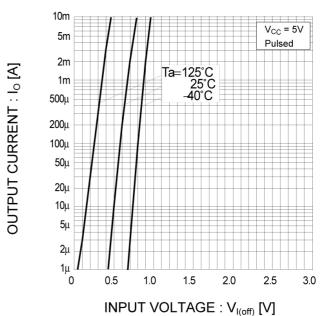


Fig.4 DC Current Gain vs. Output Current

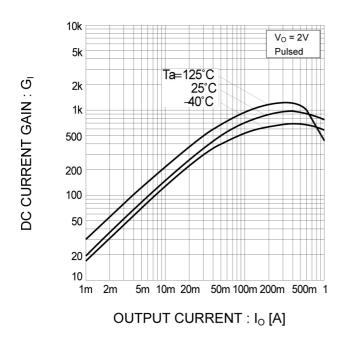
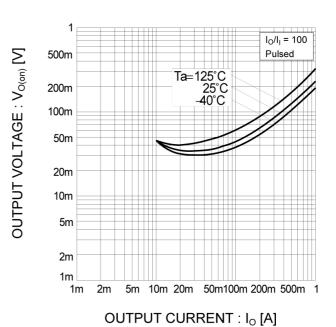
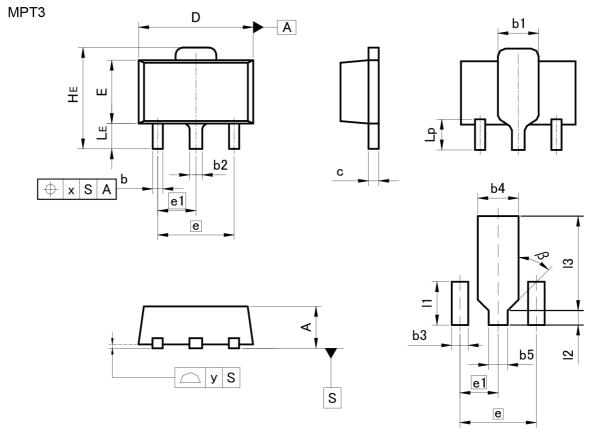


Fig.5 Output Voltage vs. Output Current



### Dimensions



Pattern of terminal position areas [Not a recommended pattern of soldering pads]

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	1.40	1.50	0.055	0.059	
b	0.30	0.50	0.012	0.020	
b1	1.50	1.70	0.059	0.067	
b2	0.40	0.60	0.016	0.024	
С	0.35	0.50	0.014	0.020	
D	4.40	4.70	0.173	0.185	
E	2.40	2.70	0.094	0.106	
е	3.0	00	0.118		
e1	1.50		0.059		
HE	3.70	4.30	0.146	0.169	
LE	0.80	1.20	0.031	0.047	
Lp	1.01	1.41	0.040	0.056	
x	-	0.15	- 0.006		
У	_	0.10	- 0.004		

DIM	MILIM	ETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
b3	_	0.65	ı	0.026	
b4	_	1.70	ı	0.067	
b5	_	0.75	ı	0.030	
l1	_	1.71	ı	0.067	
12	_	0.58	ı	0.023	
13	-	3.72	-	0.146	
β	45	0	45	0	

Dimension in mm/inches



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