

SILICON TRANSISTOR 2SB1657

AUDIO FREQUENCY AMPLIFIER, SWITCHING PNP SILICON EPITAXIAL TRANSISTORS

FEATURES

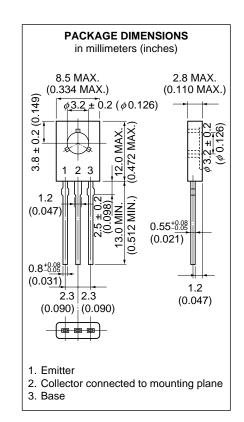
Low Vce(sat)

 $V_{CE(sat)} = -0.15 \text{ V Max } (@Ic/IB = 0.5 \text{ A}/25 \text{ mA})$

High DC Current Gain
 hFE = 150 to 600 (@VcE = -2.0 V, Ic = -0.5 A)

ABSOLUTE MAXIMUM RATINGS

Maximum Voltage and Current (T_A = 25 °C) Collector to Base Voltage V_{CB0} -30 V Collector to Emitter Volteage VCE0 -30 V Emitter to Base Voltage V_{EB0} -6.0 V Collector Current (DC) -5.0 A Ic(DC) Collector Current (Pulse)* -8.0 A IC(Pulse) Base Current (DC) -1.0 AI_{B(DC)} * PW ≤ 10ms, Duty Cycle ≤ 10 % Maximum Power Dissipation Total Power Dissipation (Tc = 25 °C) Рτ 10 W Total Power Dissipation (T_A = 25 °C) 1.0 W Maximum Temperature 150 °C Junction Temperature Τį Storage Temperature -55 to 150 °C

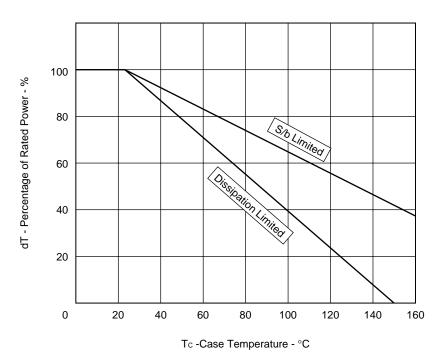


ELECTRICAL CHARACTERISTICS (TA = 25 °C)

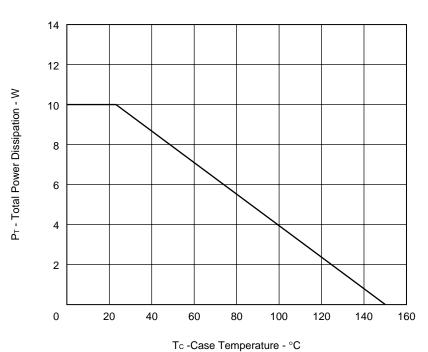
characteristics	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Collector Cutoff Currnet	Ісво	$V_{CB} = -30 \text{ V}, I_E = 0$			-100	nA
Emitter Cutoff Current	I _{EB0}	$V_{EB} = -6.0 \text{ V}, \text{ Ic} = 0$			-100	nA
DC Current Gain	h _{FE1}	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -0.5 \text{ A}$	150		600	_
DC Current Gain	h _{FE2}	$V_{CE} = -2.0 \text{ V}, \text{ Ic} = -3.0 \text{ A}$	70			_
Collector Saturation Voltage	VCE(sat)1	$I_C = -0.5 \text{ A}, I_B = -25 \text{ mA}$		-0.08	-0.15	V
Collector Saturation Voltage	VCE(sat)2	$I_C = -1.0 \text{ A}, I_B = -50 \text{ mA}$		-0.13	-0.25	V
Collector Saturation Voltage	VCE(sat)3	$I_C = -2.0 \text{ A}, I_B = -100 \text{ mA}$		-0.24	-0.40	V
Collector Saturation Voltage	VCE(sat)4	$I_C = -3.0 \text{ V}, I_B = -75 \text{ mA}$		-0.46	-1.0	V
Base Saturation Voltage	V _{BE(sat)}	$I_C = -1.0 \text{ A}, I_B = -50 \text{ mA}$		-0.83	-1.50	V
Gain Bandwidth Product	f⊤	$V_{CE} = -10 \text{ V}, I_{E} = -50 \text{ mA}$		75		MHz
Output Capacitance	Cob	VcB = −10 V, IE = 0, f = 1 MHz		60		pF

The information in this document is subject to change without notice.

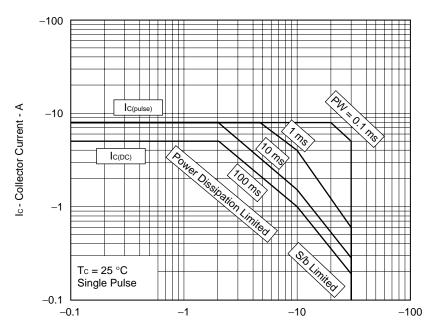
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



TOTAL POWER DISSIPATION vs. CASE TEMPERATURE

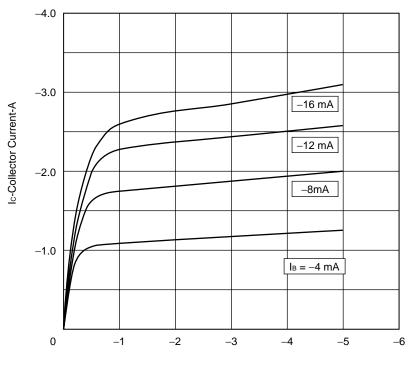


FORWARD BIAS SAFE OPERATING AREA



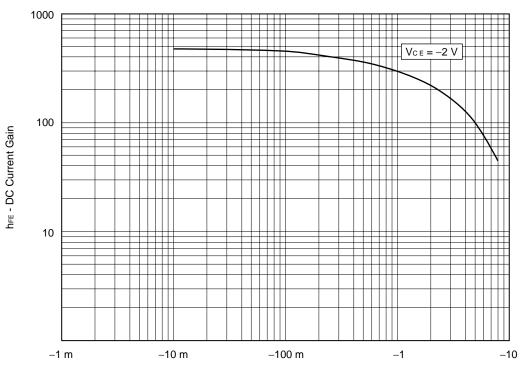
Vce - Collector to Emitter Voltage - V

COLLECTOR TO EMITTER VOLTAGE vs COLLECTOR CURRENT



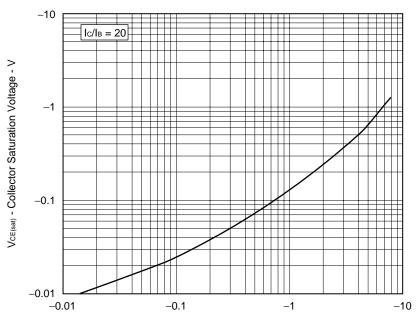
 $\ensuremath{\mathsf{V}}\xspace_{\mathsf{CE}}$ - Collector to Emitter Voltage - $\ensuremath{\mathsf{V}}\xspace$

DC CURRENT GAIN vs COLLECTOR CURRENT



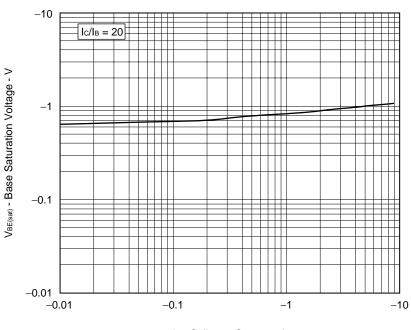
Ic - Collector Current - A

COLLECTOR SATURATION VOLTAGE vs COLLECTOR CURRENT



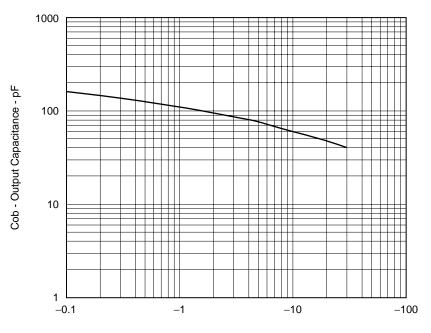
Ic - Collector Current - A

BASE SATURATION VOLTAGE vs COLLECTOR CURRENT



Ic - Collector Current - A

OUTPUT CAPACITANCE vs COLLECTOR TO BASE VOLTAGE



 V_{CB} - Collector to Base Voltage - V



REFERENCE

Document Name	Document No.	
NEC semiconductor device reliability/quality control system	TEI-1202	
Quality grade on NEC semiconductor devices	IEI-1209	
Semiconductor device mounting technology manual	C10535E	
Semiconductor device package manual	C10943X	
Guide to quality assurance for semiconductor devices	MEI-1202	
Semiconductor selection guide	X10679E	

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Anti-radioactive design is not implemented in this product.