20A SCHOTTKY BARRIER RECTIFIER

Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- High Surge Capability
- High Current Capability and Low Forward Voltage Drop
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Application
- Lead Free Finish, RoHS Compliant (Note 3)

Mechanical Data

Case: TO-3P

Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0

Moisture Sensitivity: Level 1 per J-STD-020C

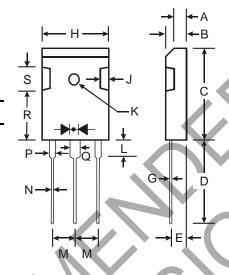
Terminals: Finish — Bright Tin. Plated Leads Solderable per MIL-STD-202, Method 208

Polarity: As Marked on Body

Ordering Information: See Last Page

Marking: Type Number

Weight: 5.6 grams (approximate)



TO-3P					
Dim	Min	Max			
Α	1.88	2.08			
В	4.68	5.36			
С	20.63	22.38			
D	18.5	21.5			
E	2.1	2.4			
G	0.51	0.76			
Н	15.38	16.25			
J	1.90	2.70			
K	2.9Ø	3.65∅			
L	3.78	4.50			
M	5.2	5.7			
N	0.89	1.53			
P	1.82	2.46			
ø	2.92	3.23			
R	11.70	12.84			
S	_	6.10			
All Dimensions in mm					

Maximum Ratings and Electrical Characteristics @TA = 25°C unless otherwise specified

	90 V00 V	000		A0000-00000				
Characteristic	Symbol	SBL 2030PT	SBL 2035PT	SBL 2040PT	SBL 2045PT	SBL 2050PT	SBL 2060PT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	30	35	40	45	50	60	٧
RMS Reverse Voltage	V _{R(RMS)}	21	24.5	28	31.5	35	42	V
Average Rectified Output Current (Note 1) $^{\circ}$	lo			2	0			Α
Non-Repetitive Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load	I _{FSM}			25	50			Α
Forward Voltage Drop $@ I_F = 10A, T_C = 25^{\circ}C$	V_{FM}		0.	55		0.	75	V
Peak Reverse Current					mA			
Typical Total Capacitance (Note 2)	Ст	1100					pF	
Typical Thermal Resistance Junction to Case (Note 1)	$R_{ heta JC}$	2.5				°C/W		
Operating and Storage Temperature Range	T _{i,} T _{STG}	-65 to +150					°C	

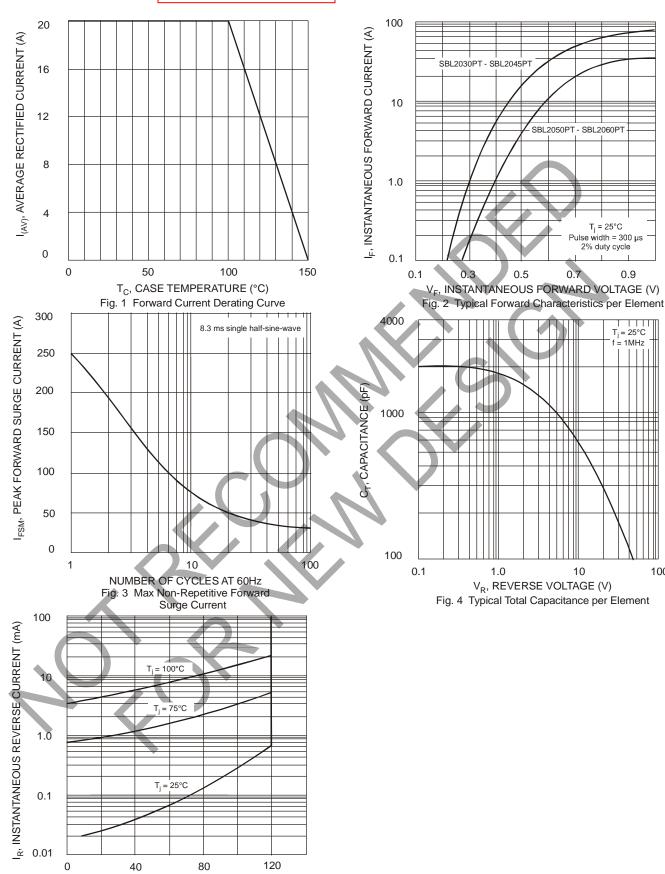
Notes:

- 1. Thermal resistance junction to case mounted on heatsink.
- Measured at 1.0MHz and applied reverse voltage of 4.0V DC.
 RoHS revision 13.2.2003. Glass and high temperature solder exemptions applied, see EU Directive Annex Notes 5 and 7.

SBL2030PT - SBL2060PT DS23047 Rev. 5 - 3 1 of 3 © Diodes Incorporated www.diodes.com



NOT RECOMMENDED FOR NEW DESIGN



T_j = 25°C Pulse width = 300 µs 2% duty cycle

= 1MHz

100

0.7

10

PERCENT OF RATED PEAK REVERSE VOLTAGE (%) Fig. 5 Typical Reverse Characteristics per Element



NOT RECOMMENDED FOR NEW DESIGN

Ordering Information (Note 4)

Device	Packaging	Shipping
SBL2030PT	TO-3P	30/Tube
SBL2035PT	TO-3P	30/Tube
SBL2040PT	TO-3P	30/Tube
SBL2045PT	TO-3P	30/Tube
SBL2050PT	TO-3P	30/Tube
SBL2060PT	TO-3P	30/Tube

Notes: 4. For packaging details, visit our website at http://www.diodes.com/datasheets/ap02008.pdf.

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DS23047 Rev. 5 - 3 3 of 3 SBL2030PT - SBL2060PT