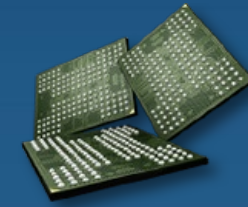


NAND Flash

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Our ongoing innovations in process and design continue to drive NAND development, evidenced by the introduction of industry-leading 20nm NAND. We offer a diverse portfolio of products with distinct features, functionality, and performance to accommodate your device-specific needs.

Finding the Right NAND Solution

We've been developing leading-edge memory technology for more than 30 years. Our long tradition of innovation and technology leadership has helped shape our broad NAND Flash offering. With high-performance NAND products that cover everything from mobile, to embedded, to enterprise storage applications, we can help you find the best solution for your design.

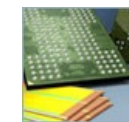
Overview	Benefits	Densities	Configurations	Supply Voltages
» SLC NAND	- Up to 100,000 P/E cycle endurance - Fastest throughput of the various NAND technologies (MLC, TLC) - Compatibility with the ONFI synchronous interface	128Mb–512Gb	x1, x8, x16	1.8V, 3.0V, 3.3V
» MLC NAND	- Solid performance and endurance - Twice the density of SLC NAND at a lower cost-per-bit - Compatibility with the ONFI synchronous interface	16Gb–1Tb	x8	3.3V
» TLC NAND	Higher density in the same space, at a lower cost than SLC or MLC NAND	64Gb–128Gb	x8	3.3V
» Serial NAND	- Enables large memory subsystems - Increased power efficiency - High performance, low power - Increased bandwidth	1Gb–4Gb	x1	3.3V

Selecting a Flash Storage Solution

Our new [NOR/NAND Flash Guide: Selecting a Flash Storage Solution](#) tool can help you choose the right NOR Flash device for your design.

» [Download the Guide](#)

Choosing the Right NAND



Flash memory manufacturers are offering a variety of NAND Flash memory products—including some specialized NAND solutions—with significantly different performance capabilities and features across a number of process nodes.

» [More](#)

ONFI Advantages

The Open NAND Flash Interface (ONFI) is a collaboration among Micron and many other NAND Flash suppliers, controller manufacturers, and designers with the goal of developing open standards for the interface used to communicate with NAND Flash memory.

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