

10 9 8 7 6 5 4 3 2 1

NOTES:

1. MATERIALS

HOUSING - HIGH TEMP. NYLON GLASS FILLED, UL 94V-0

COLOR: BLACK

EJECTOR - HIGH TEMP. NYLON GLASS FILLED, UL 94V-0

COLOR: SEE TABLE

TERMINAL - COPPER ALLOY

2. PLATING - SEE TABLE IN SHEET 5 & 6.

3. CARD SLOT ACCEPTS 1.27±0.10 MM MODULE THICKNESS.

(MEASURED OVER P.C. PADS)

4. RECOMMENDED MODULE LAYOUT SHALL BE AS PER JEDEC MO-237.

5. REFER TO PRODUCT SPEC. PS-87705-002 FOR PERFORMANCE SPECIFICATIONS.

6. PRODUCT SHALL BE PACKED IN TRAY.

7. PRODUCT SHALL HAVE DATE CODE STAMPED ON SIDE OF HOUSING.

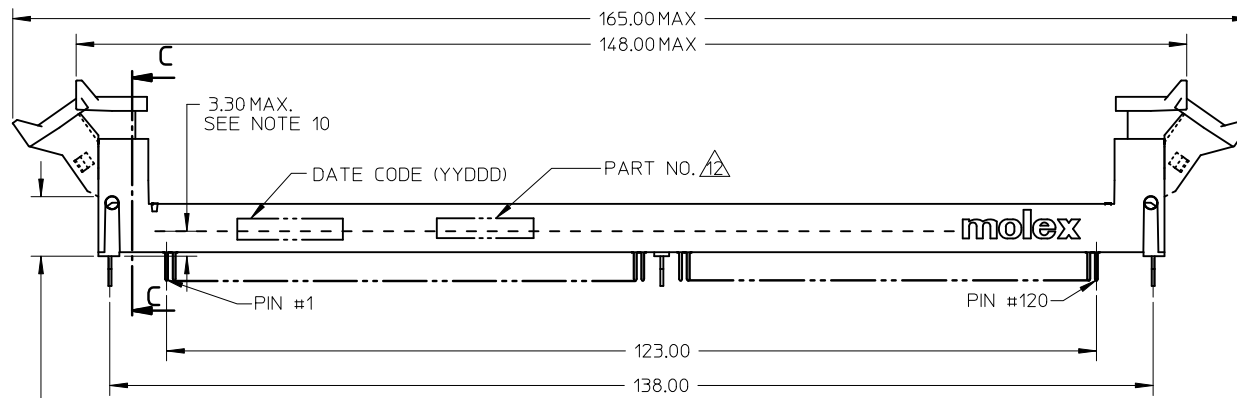
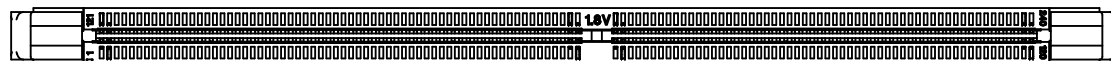
8. KEEP OUT AREA IS THE AREA WHERE THE CONNECTOR IS MOUNTED ONTO THE PCB WHICH DOES NOT INCLUDE THE LATCH.

9. REFER TO CRITICAL JEDEC INFORMATION FOR SOCKET OUTLINE PER SO-001.

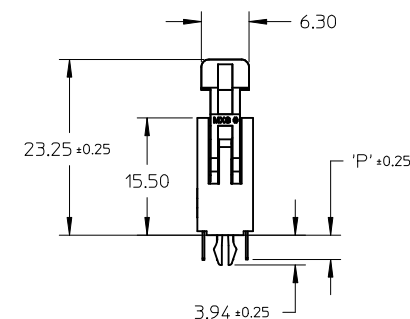
10. DIMM MODULE SEATING PLANE FROM TOP OF PCB.

11. CONTACT MOLEX FOR AVAILABILITY OF PRODUCT OPTIONS.

△ PART NUMBER SHALL BE MARKED LEGIBLY AS 87705-****

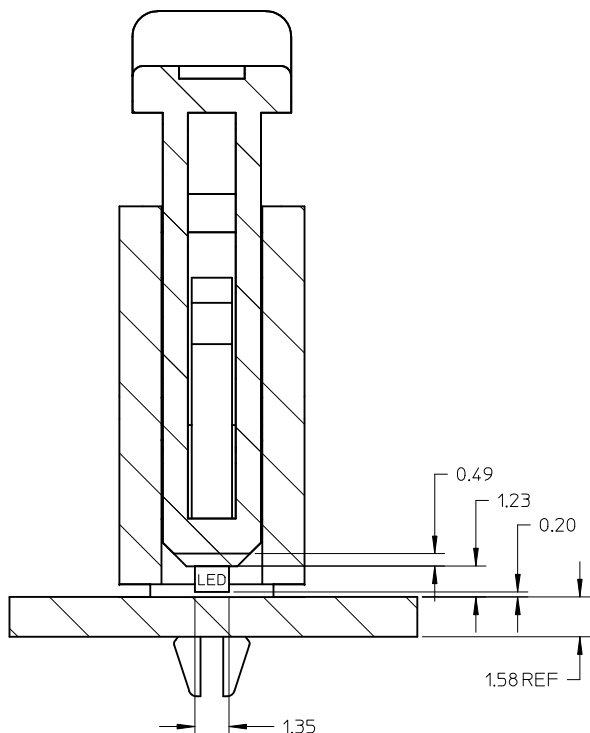
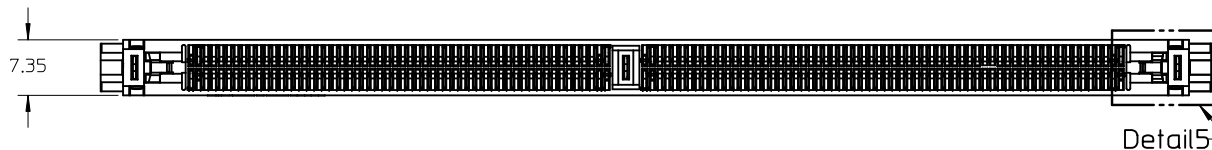


5.00 MIN
LATCH KEEP
OUT AREA

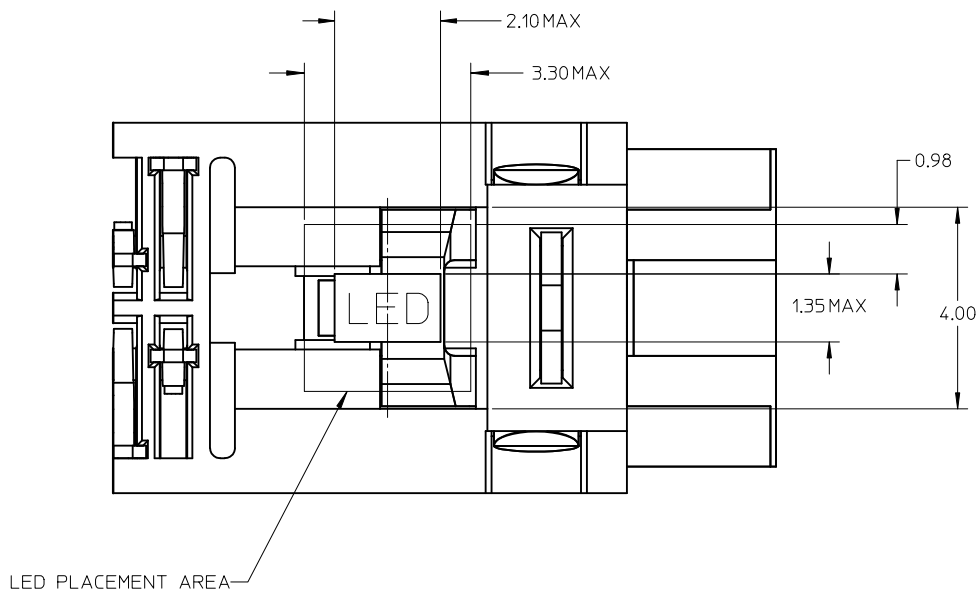


ADDED OPTION EC NO: S2012-0025 2009/11/16 DRWN:CCTEH CHKD:CGTAN 2011/07/14 APPR:SHENI 2011/07/14	QUALITY SYMBOLS	DESCRIPTION	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
				mm	INCH	DRAWN BY CGOH	DATE 2002/05/09	TITLE DDR2 DIMM, 1.00MM PITCH 240 CKTS (FORKLOCK VERSION)			
			4 PLACES	± ---	± ---	CHECKED BY DSOH	DATE 2002/07/22				
			3 PLACES	± ---	± ---	APPROVED BY SKTOH	DATE 2002/07/23				
			2 PLACES	± 0.20	± ---	MATERIAL NO. SEE TABLE		DOCUMENT NO. SD-87705-001		SHEET NO. 1 OF 6	
D2	REV		1 PLACE		± ---	± ---	MOLEX INCORPORATED				
			ANGULAR ± 5 °								
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				

10 9 8 7 6 5 4 3 2 1



SECTION C-C

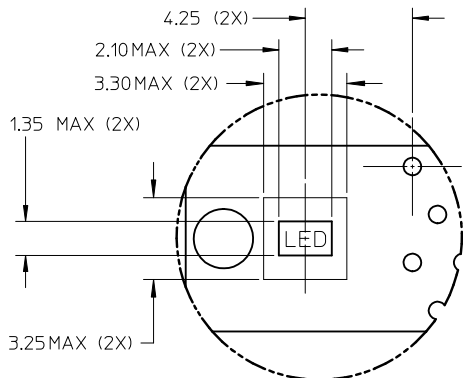


Detail 5

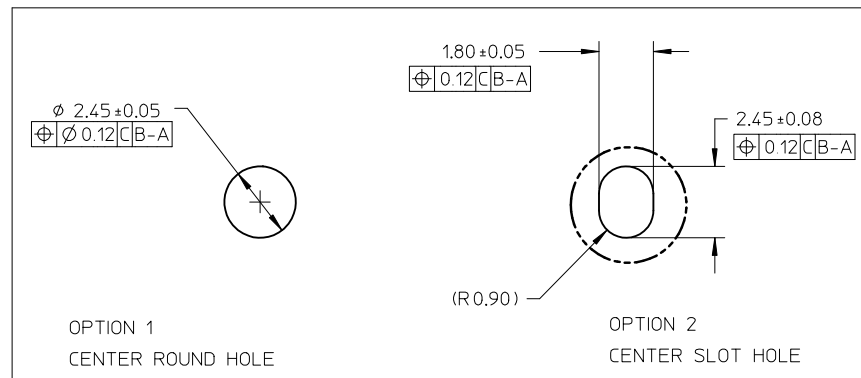
ADDED OPTION EC NO: S2012-0025 DRWN: CTEH CHKD: CGTAN APPR: SLENI 2009/11/16 2011/07/14 2011/07/14	DESCRIPTION REV	QUALITY SYMBOLS 	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION			
					DRAWN BY CGOH		DATE 2002/05/09		TITLE DDR2 DIMM, 100MM PITCH 240 CKTS (FORKLOCK VERSION)			
					CHECKED BY DSOH		DATE 2002/07/22					
					APPROVED BY SKTOH		DATE 2002/07/23					
					mm		INCH		MOLEX INCORPORATED			
			4 PLACES		± ---		± ---					
			3 PLACES		± ---		± ---		DOCUMENT NO. SD-87705-001			
			2 PLACES		± 0.20		± ---					
			1 PLACE		± ---		± ---		SHEET NO. 2 OF 6			
					ANGULAR ± 5 °							
DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS				SEE TABLE								
D2				SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION						

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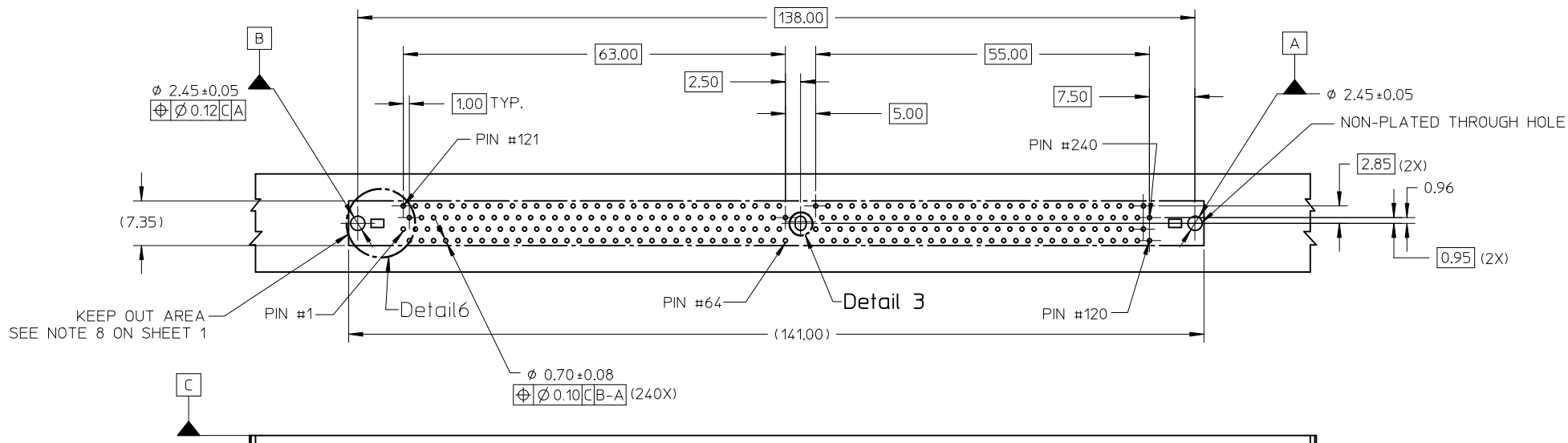


Detail 6



Detail 3

(SEE RECOMMENDED THICKNESS IN TABLE)
RECOMMENDED PCB LAYOUT
CONNECTOR SIDE



ADDED OPTION EC NO: S2012-0025 2009/11/16 DRWN:CCTEH CHKD:GTAN 2011/07/14 APPR:SHENI	QUALITY SYMBOLS	DESCRIPTION	GENERAL TOLERANCES (UNLESS SPECIFIED)		DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION		
				mm	INCH	DRAWN BY CGOH	DATE 2002/05/09	TITLE DDR2 DIMM, 100MM PITCH 240 CKTS (FORKLOCK VERSION)			
			4 PLACES	± ---	± ---	CHECKED BY DSOH	DATE 2002/07/22				
			3 PLACES	± ---	± ---	APPROVED BY SKTOH	DATE 2002/07/23				
			2 PLACES	± 0.20	± ---	MATERIAL NO.		DOCUMENT NO.		SHEET NO.	
D2	REV		ANGULAR ± 5 °		SEE TABLE		SD-87705-001		4 OF 6		
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS		SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION				

9 8 7 6 5 4 3 2 1

F

E

D

C

B

A

F

E

D

C

B

A

PART NO.	KEY POS.	DIM 'P'	RECOMMENDED PCB THICKNESS	PLATING OPTION	LATCH COLOR	LUBRICATION
87705-0021	CENTER (1.8V)	2.67	1.57	0.38uM / 15 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE	NATURAL	NO
87705-1021		3.18	2.36			
87705-1053		3.66				
87705-0031		2.67	1.57	0.03uM / 1 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE		
87705-1031		3.18	2.36			
87705-0051		2.67	1.57	0.76uM / 30 uIN MIN. GOLD ON CONTACT, 2.54uM/ 100uIN MIN. TIN ON SOLDER TAILS, 1.27uM/ 50uIN MIN. NICKEL UNDERPLATE		
87705-1051		3.18	2.36			
87705-3061						YES

ADDED OPTION EC NO: S2012-0025 DRWN:CTEH CHKD:CGTAN APPR:SHLENI	DESCRIPTION REV	QUALITY SYMBOLS F_A=0 F_G=0 F_P=0	GENERAL TOLERANCES (UNLESS SPECIFIED)			DIMENSION STYLE MM ONLY		SCALE NTS	DESIGN UNITS METRIC	THIRD ANGLE PROJECTION	
				mm	INCH	DRAWN BY CGOH	DATE 2002/05/09	TITLE DDR2 DIMM, 1.00MM PITCH 240 CKTS (FORKLOCK VERSION)			
			4 PLACES	± ---	± ---	CHECKED BY DSOH	DATE 2002/07/22				
			3 PLACES	± ---	± ---	APPROVED BY SKTOH		DATE 2002/07/23	molex MOLEX INCORPORATED		
			2 PLACES	± 0.20	± ---	MATERIAL NO. SEE TABLE		DOCUMENT NO. SD-87705-001			SHEET NO. 5 OF 6
			1 PLACE	± ---	± ---	SIZE A3		THIS DRAWING CONTAINS INFORMATION THAT IS PROPRIETARY TO MOLEX INCORPORATED AND SHOULD NOT BE USED WITHOUT WRITTEN PERMISSION			
			ANGULAR ± 5 °								
			DRAFT WHERE APPLICABLE MUST REMAIN WITHIN DIMENSIONS								

E

A