



AOS Semiconductor

Product Reliability Report

AON6702/AON6702L, rev A

Plastic Encapsulated Device

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This AOS product reliability report summarizes the qualification result for AON6702. Accelerated environmental tests are performed on a specific sample size, and then followed by electrical test at end point. Review of final electrical test result confirms that AON6702 passes AOS quality and reliability requirements. The released product will be categorized by the process family and be monitored on a quarterly basis for continuously improving the product quality.

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I. Product Description:

SRFET™ AON6702/L uses advanced trench technology with a monolithically integrated Schottky diode to provide excellent $R_{DS(ON)}$ and low gate charge. This device is suitable for use as a low side switch in SMPS, load switching and general purpose applications.

- RoHS compliant
- AON6702L is Halogen free

Absolute Maximum Ratings $T_A=25^{\circ}\text{C}$ unless otherwise noted				
Parameter		Symbol	Maximum	Units
Drain-Source Voltage		V_{DS}	30	V
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current ^G	$T_C=25^{\circ}\text{C}$	I_D	85	A
	$T_C=100^{\circ}\text{C}$		67	
Pulsed Drain Current ^C		I_{DM}	260	
Continuous Drain Current	$T_A=25^{\circ}\text{C}$	I_{DSM}	26	A
	$T_A=70^{\circ}\text{C}$		20	
Avalanche Current ^C		I_{AR}	72	A
Repetitive avalanche energy $L=0.1\text{mH}$ ^C		E_{AR}	259	mJ
Power Dissipation ^B	$T_C=25^{\circ}\text{C}$	P_D	83	W
	$T_C=100^{\circ}\text{C}$		33	
Power Dissipation ^A	$T_A=25^{\circ}\text{C}$	P_{DSM}	2.3	W
	$T_A=70^{\circ}\text{C}$		1.4	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to 150	$^{\circ}\text{C}$

Thermal Characteristics					
Parameter		Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^A	$t \leq 10\text{s}$	$R_{\theta JA}$	14	18	$^{\circ}\text{C/W}$
Maximum Junction-to-Ambient ^{A,D}	Steady-State		40	55	$^{\circ}\text{C/W}$
Maximum Junction-to-Case	Steady-State	$R_{\theta JC}$	1	1.5	$^{\circ}\text{C/W}$

II. Die / Package Information:

	AON6702	AON6702L (Green Compound)
Process	Standard sub-micron Low voltage N channel process	Standard sub-micron Low voltage N channel process
Package Type	DFN 5x6_8L	DFN 5x6_8L
Lead Frame	A194 (FH)	A194 (FH)
Die Attach	Solder Paste	Solder Paste
Bond wire	Plate-1 copper 6 mils	Plate-1 copper 6 mils
Mold Material	Epoxy resin with silica filler	Epoxy resin with silica filler
Filler % (Spherical/Flake)	90/10	100/0
Flammability Rating	UL-94 V-0	UL-94 V-0
Backside Metallization	Ti / Ni / Ag	Ti / Ni / Ag
Moisture Level	Up to Level 1 *	Up to Level 1 *

Note * based on info provided by assembler and mold compound supplier

III. Result of Reliability Stress for AON6702 (Standard) & AON6702L (Green)

Test Item	Test Condition	Time Point	Lot Attribution	Total Sample size	Number of Failures
Solder Reflow Precondition	Standard: 1hr PCT+3 cycle reflow @260°C Green: 168hr 85°C /85%RH +3 cycle reflow @260°C	0hr	Standard: 24 lots (Note B**)	4022 pcs	0
HTGB	Temp = 150°C , Vgs=100% of Vgsmax	168 / 500 hrs 1000 hrs	2 lot (Note A*)	82 pcs 77+5 pcs / lot	0
HTRB	Temp = 150°C , Vds=80% of Vdsmax	168 / 500 hrs 1000 hrs	2 lot (Note A*)	82 pcs 77+5 pcs / lot	0
HAST	130 +/- 2°C , 85%RH, 33.3 psi, Vgs = 80% of Vgs max	100 hrs	Standard: 12 lots (Note B**)	660 pcs 50+5 pcs / lot	0
Pressure Pot	121°C , 29.7psi, RH=100%	96 hrs	Standard: 17 lots (Note B**)	1394 pcs 77+5 pcs / lot	0
Temperature Cycle	-65°C to 150°C , air to air	250 / 500 cycles	Standard: 24 lots (Note B**)	1968 pcs 77+5 pcs / lot	0

III. Result of Reliability Stress for AON6702 (Standard) & AON6702L (Green)

Continues

DPA	Internal Vision Cross-section X-ray	NA	5 5 5	5 5 5	0
CSAM		NA	5	5	0
Bond Integrity	Room Temp 150°C bake 150°C bake	0hr 250hr 500hr	40 40 40	40 wires 40 wires 40 wires	0
Solderability	245°C	5 sec	15	15 leads	0

Note A: The HTGB and HTRB reliability data presents total of available AON6702 and AON6702L burn-in data up to the published date.

Note B: The pressure pot, temperature cycle and HAST reliability data for AON6702 and AON6702L comes from the AOS generic package qualification data.

IV. Reliability Evaluation

FIT rate (per billion): 21.63

MTTF = 5279years

In general, 500 hrs of HTGB, 150 deg C accelerated stress testing is equivalent to 15 years of lifetime at 55 deg C operating conditions (by applying the Arrhenius equation with an activation energy of 0.7eV and 60% of upper confidence level on the failure rate calculation). AOS reliability group also routinely monitors the product reliability up to 1000 hr at and performs the necessary failure analysis on the units failed for reliability test(s).

The presentation of FIT rate for the individual product reliability is restricted by the actual burn-in sample size of the selected product (AON6702). Failure Rate Determination is based on JEDEC Standard JESD 85. FIT means one failure per billion hours.

$$\text{Failure Rate} = \text{Chi}^2 \times 10^9 / [2 (N) (H) (Af)] = 1.83 \times 10^9 / [2 (2X164) (500) (258)] = 21.63$$

$$\text{MTTF} = 10^9 / \text{FIT} = 4.62 \times 10^7 \text{hrs} = 5279 \text{ years}$$

Chi² = Chi Squared Distribution, determined by the number of failures and confidence interval

N = Total Number of units from HTRB and HTGB tests

H = Duration of HTRB/HTGB testing

Af = Acceleration Factor from Test to Use Conditions (Ea = 0.7eV and Tuse = 55°C)

Acceleration Factor **[Af]** = $\text{Exp} [Ea / k (1/Tj - 1/Tj s)]$

Acceleration Factor ratio list:

	55 deg C	70 deg C	85 deg C	100 deg C	115 deg C	130 deg C	150 deg C
Af	258	87	32	13	5.64	2.59	1

Tjs = Stressed junction temperature in degree (Kelvin), K = C+273.16

Tju = The use junction temperature in degree (Kelvin), K = C+273.16

k = Boltzmann's constant, $8.617164 \times 10^{-5} \text{eV/K}$



V. Quality Assurance Information

Acceptable Quality Level for outgoing inspection: **0.1%** for electrical and visual.

Guaranteed Outgoing Defect Rate: **< 25 ppm**

Quality Sample Plan: conform to **Mil-Std-105D**