

8Gb NAND/Mobile DDR SDRAM: MT29C4G96MAYAPCJA-5 IT



Specifications

Part Status Code: Production
NAND Density: 4Gb
Width: x8
LPDDR Density: 4Gb
LPSDR Density:
Secondary Width: x32
RoHS: Yes
Voltage: 1.7V-1.9V
Package: TFBGA
Pin Count: 137-ball
Clock Rate: 200 MHz
Op Temp: -40C to +85C
Pinout: JEDEC
Platform:

Data Sheets

 [Data sheet: 137-ball, 10.5 x 13.0mm, NAND + LPDDR MCP, JEDEC, J42P, J43P, J4Z2, J4Z3](#) 
Rev. Date: 07/2011, File Size: 4.13 MB

Certifications

-  [RoHS Certificate of Compliance.pdf](#)
-  [China RoHS Certificate.pdf](#)

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Orderable Part	Part Status	Buy Online
MT29C4G96MAYAPCJA-5 IT	Production	Contact Rep

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