

GaAs IC SPDT Low Loss Switch Reflective DC–6 GHz



AS006L2-00

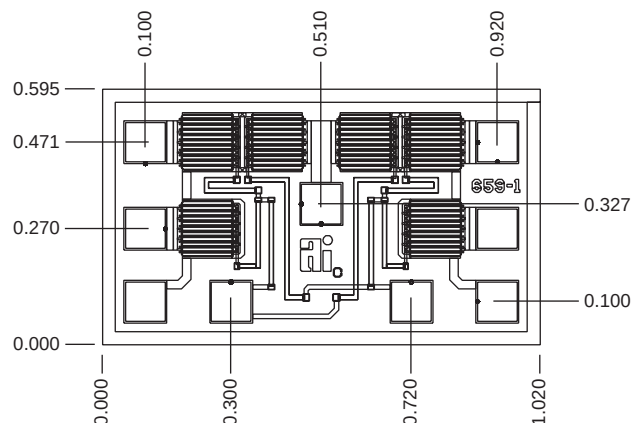
Features

- Fully Passivated
- Low Loss, Reflective
- Excellent Intermodulation and Temperature Stability
- Fast Switching, Low Transients

Description

The AS006L2-00 SPDT switch is designed for applications up to 6 GHz which require low loss and medium isolation. The chip is a rugged .008" thick, and has a fully passivated surface allowing for ease of handling for MCM assemblies. Ti/W/Au gate metal makes this product ideal for commercial, SatCom and military applications. All devices are 100% tested on-wafer and inspected to MIL-STD-883 MT 2010. Element evaluation can be performed to MIL-PRF-38534 Class H or K on request.

Chip Outline



Dimensions indicated in mm.
All bonding pads are 0.1 x 0.1 mm.
Chip thickness = 0.2 mm.

Electrical Specifications at 25°C

Parameter ¹	Frequency ⁵	Min.	Typ.	Max.	Unit
Insertion Loss ²	DC–1.0 GHz		0.5	0.6	dB
	DC–2.0 GHz		0.6	0.7	dB
	DC–4.0 GHz		0.8	0.9	dB
	DC–6.0 GHz		1.0	1.2	dB
Isolation	DC–1.0 GHz	50	52		dB
	DC–2.0 GHz	40	42		dB
	DC–4.0 GHz	30	34		dB
	DC–6.0 GHz	28	30		dB
VSWR ³	DC–1.0 GHz		1.3:1	1.4:1	
	DC–4.0 GHz		1.5:1	1.6:1	
	DC–6.0 GHz		1.5:1	1.6:1	

Operating Characteristics at 25°C

Parameter	Condition	Frequency	Min.	Typ.	Max.	Unit
Switching Characteristics	Rise, Fall (10/90% or 90/10% RF)			3		ns
	On, Off (50% CTL to 90/10% RF)			6		ns
	Video Feedthru ⁴			20		mV
Input Power for 1 dB Compression Control Voltages (V _C)	0/-5 V (0/-8 V)	0.5–6 GHz		24 (30)		dBm
		0.001 GHz		16 (20)		dBm
Intermodulation Intercept Point (IP3)	For Two-tone Input Power 13 dBm	0.5–6 GHz 0.001 GHz		46 35		dBm dBm
Control Voltages	V _{Low} = 0 to -0.2 V @ 20 µA Max. V _{High} = -5 V @ 50 µA to -9 V @ 200 µA Max.					

1. All measurements made in a 50 Ω system, unless otherwise specified.

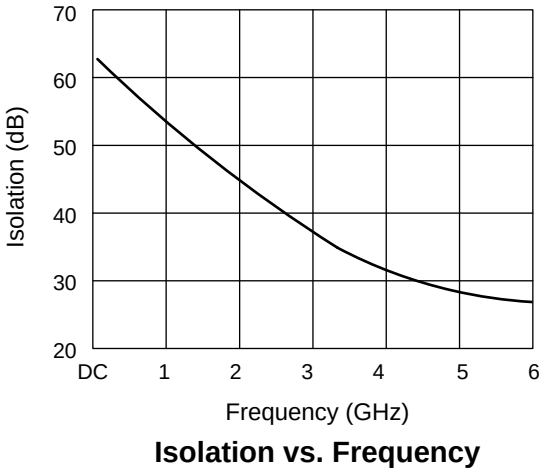
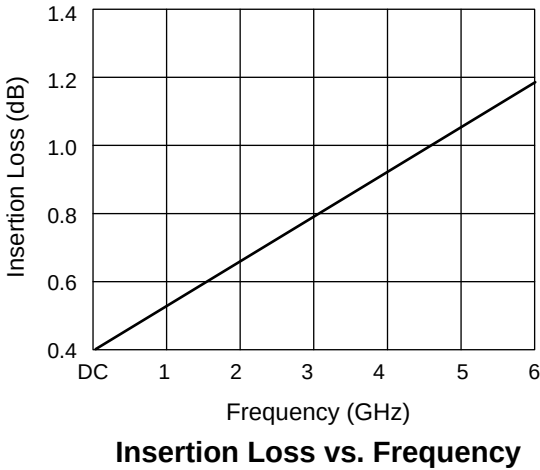
2. Insertion loss changes by 0.003 dB/°C.

3. Insertion loss state.

4. Video feedthru measured with 1 ns risetime pulse and 500 MHz bandwidth.

5. DC = 300 kHz.

Typical Performance Data



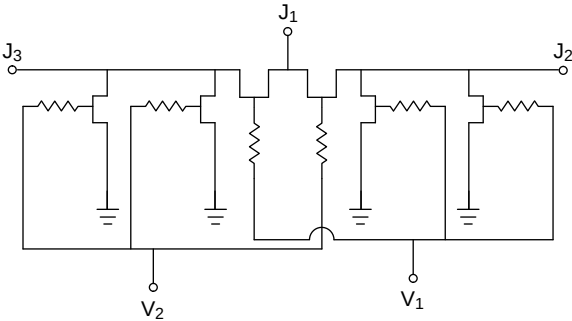
Truth Table

V ₁	V ₂	J ₁ –J ₂	J ₁ –J ₃
0	-5	Isolation	Insertion Loss
-5	0	Insertion Loss	Isolation

Absolute Maximum Ratings

Characteristic	Value
RF Input Power (RF In)	2 W > 500 MHz 0/-8 V 0.5 W @ 50 MHz 0/-8 V
Control Voltage (V _C)	+0.2 V, -10 V
Operating Temperature (T _{OP})	-55°C to +125°C
Storage Temperature (T _{ST})	-65°C to +150°C
Thermal Resistance (Θ _{JC})	25°C/W

Switch Schematic



Chip Layout

