

TLP285-4

Programmable Controllers

Power Supplies

Hybrid ICs

The Toshiba TLP285-4 consists of photo transistor, optically coupled to a gallium arsenide infrared emitting diode. TLP285-4 is housed in the SOP16 package, very small and thin coupler.

Since TLP285-4 are guaranteed wide operating temperature ($T_a = -55$ to 110°C) and high isolation voltage (3750Vrms), it's suitable for high-density surface mounting applications such as programmable controllers and hybrid ICs.

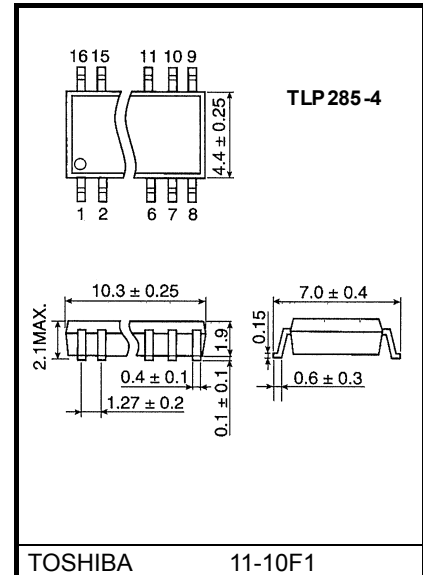
- Collector-Emitter Voltage : 80 V (min)
- Current Transfer Ratio : 50% (min)
Rank GB : 100% (min)
- Isolation Voltage : 3750 Vrms (min)
- Guaranteed performance over -55 to 110°C
- UL Recognized : UL1577 , File No. E67349
cUL Recognized : CSA Component Acceptance Service No.5A
- BSI (under application) : BS EN 60065: 2002,
: BS EN 60950-1: 2006
- Option (V4)
VDE approved : EN60747-5-2
Maximum operating insulation voltage : 707 Vpk
Highest permissible over voltage : 6000 Vpk

(Note) When a EN60747-5-2 approved type is needed, please designate the "Option(V4)"

Construction Mechanical Rating

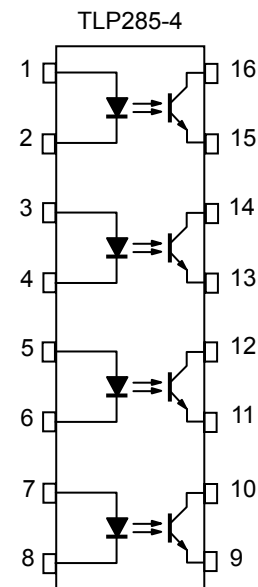
Creepage Distance	5.0 mm (min)
Clearance	5.0 mm (min)
Insulation Thickness	0.4 mm (min)

Unit in mm



Weight: 0.19 g (typ.)

Pin Configuration



1,3,5,7 :ANODE
2,4,6,8 :CATHODE
9,11,13,15 :EMITTER
10,12,14,16 :COLLECTOR

Current Transfer Ratio

TYPE	Classification (Note1)	Current Transfer Ratio (%) (I _C / I _F)		Marking of Classification
		I _F = 5 mA, V _{CE} = 5 V, T _a = 25°C		
		Min	Max	
TLP285-4	Blank	50	600	Blank , GB
	Rank GB	100	600	GB

Note1: ex. Rank GB: TLP285 (GB)

Application type name for certification test, please use standard product type name, i.e.
TLP285-4 (GB): TLP285-4

Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current	$I_{F(RMS)}$	50	mA
	Forward Current Derating	$\Delta I_F / ^\circ C$	-0.67 (Ta $\geq$ 50°C)	mA / °C
	Pulse Forward Current (Note2)	I_{FP}	1	A
	Reverse Voltage	V_R	5	V
	Junction Temperature	T_j	125	°C
DETECTOR	Collector-Emitter Voltage	V_{CEO}	80	V
	Emitter-Collector Voltage	V_{ECO}	7	V
	Collector Current	I_C	50	mA
	Collector Power Dissipation (1 Circuit)	P_C	100	mW
	Collector Power Dissipation Derating (Ta $\geq$ 25°C) (1 Circuit)	$\Delta P_C / ^\circ C$	-1.0	mW / °C
	Junction Temperature	T_j	125	°C
Operating Temperature Range		T_{opr}	-55 to 110	°C
Storage Temperature Range		T_{stg}	-55 to 125	°C
Lead Soldering Temperature		T_{sol}	260 (10s)	°C
Total Package Power Dissipation (1 Circuit)		P_T	170	mW
Total Package Power Dissipation Derating (Ta $\geq$ 25°C) (1 Circuit)		$\Delta P_T / ^\circ C$	-1.7	mW / °C
Isolation Voltage (Note3)		BV_S	3750	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width $\leq$ 100 μ s, frequency 100Hz

Note3: AC, 1 minute, R.H. $\leq$ 60%, Device considered a two terminal device : LED side pins shorted together and DETECTOR side pins shorted together.

Individual Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	V_F	$I_F = 10$ mA	1.0	1.15	1.3	V
	Reverse Current	I_R	$V_R = 5$ V	—	—	10	μ A
	Capacitance	C_T	$V = 0$, $f = 1$ MHz	—	30	—	pF
DETECTOR	Collector-Emitter Breakdown Voltage	$V_{(BR) CEO}$	$I_C = 0.5$ mA	80	—	—	V
	Emitter-Collector Breakdown Voltage	$V_{(BR) ECO}$	$I_E = 0.1$ mA	7	—	—	V
	Collector Dark Current (Note5)	I_{CEO}	$V_{CE} = 48$ V, Ambient Light Below (100 lx) (Note4)	—	0.01 (2)	0.1 (10)	μ A
			$V_{CE} = 48$ V, Ta = 85°C Ambient Light Below (100 lx) (Note4)	—	2 (4)	50 (50)	μ A
	Capacitance (Collector to Emitter)	C_{CE}	$V = 0$, $f = 1$ MHz	—	10	—	pF

Note.4 : Irradiation to marking side using standard light bulb.

Note 5: Because of the construction,leak current might be increased by ambient light.

Please use photocoupler with less ambient light.

Coupled Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Transfer Ratio	I_C / I_F	$I_F = 5 \text{ mA}$, $V_{CE} = 5 \text{ V}$ Rank GB	50	—	600	%
			100	—	600	
Saturated CTR	$I_C / I_F (\text{sat})$	$I_F = 1 \text{ mA}$, $V_{CE} = 0.4 \text{ V}$ Rank GB	—	60	—	%
			30	—	—	
Collector-Emitter Saturation Voltage	$V_{CE (\text{sat})}$	$I_C = 2.4 \text{ mA}$, $I_F = 8 \text{ mA}$	—	—	0.4	V
		$I_C = 0.2 \text{ mA}$, $I_F = 1 \text{ mA}$	—	0.2	—	
		Rank GB	—	—	0.4	
Off-State Collector Current	$I_C (\text{off})$	$V_F = 0.7 \text{ V}$, $V_{CE} = 48 \text{ V}$	—	—	10	μA

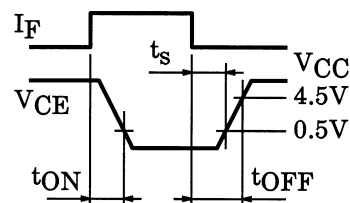
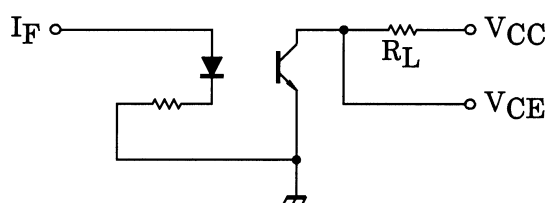
Isolation Characteristics (Ta = 25°C)

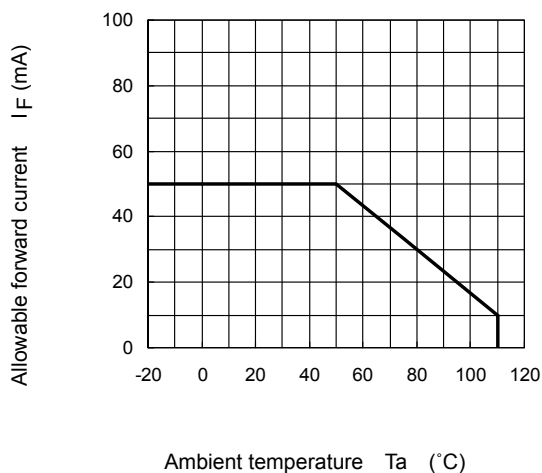
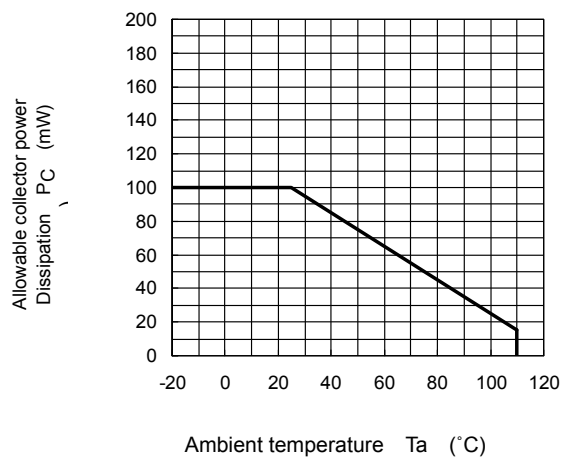
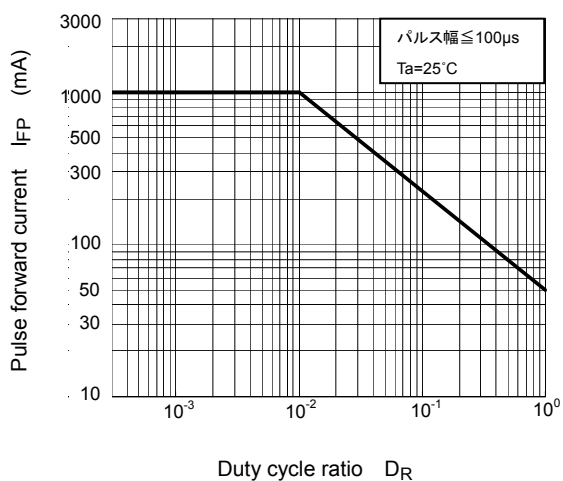
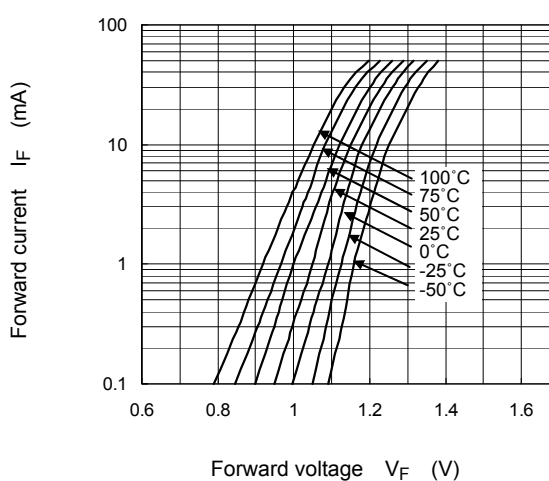
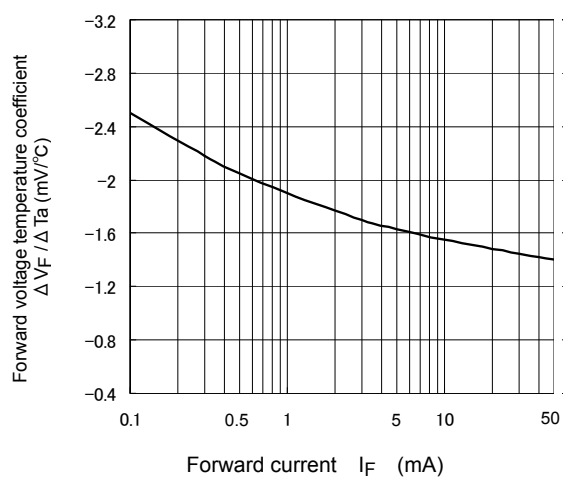
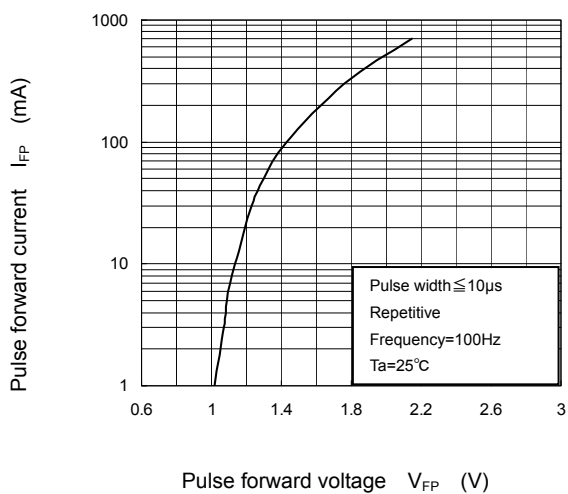
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance (Input to Output)	C_S	$V_S = 0 \text{ V}$, $f = 1 \text{ MHz}$	—	0.8	—	pF
Isolation Resistance	R_S	$V_S = 500 \text{ V}$, R.H. $\leq 60\%$	1×10^{12}	10^{14}	—	Ω
Isolation Voltage	BV_S	AC, 1 minute	3750	—	—	V_{rms}
		AC, 1 second, in OIL	—	10000	—	
		DC, 1 minute, in OIL	—	10000	—	Vdc

Switching Characteristics (Ta = 25°C)

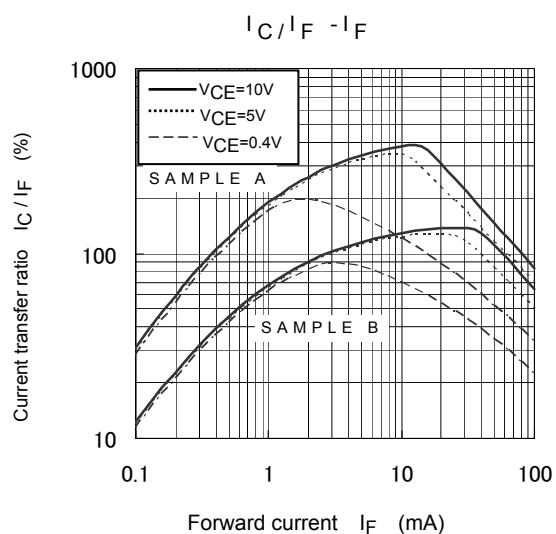
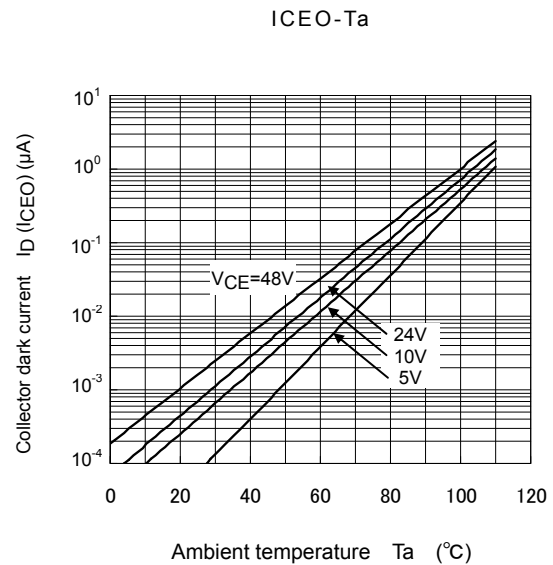
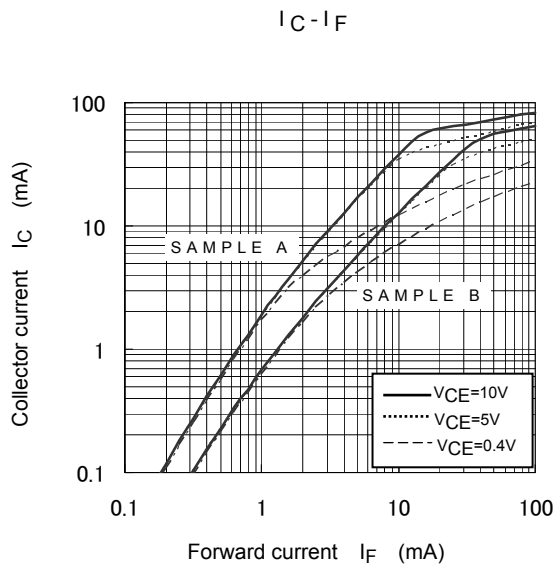
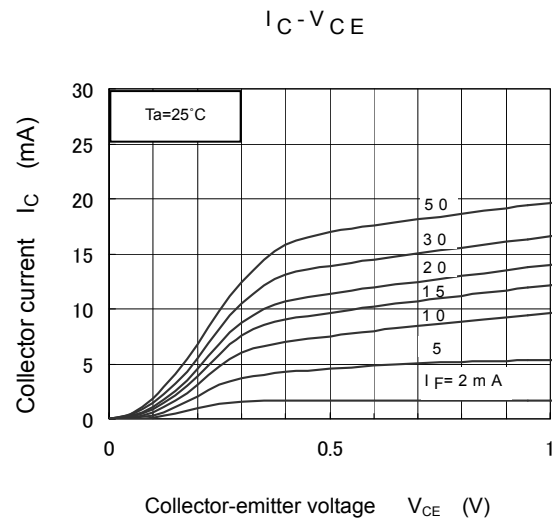
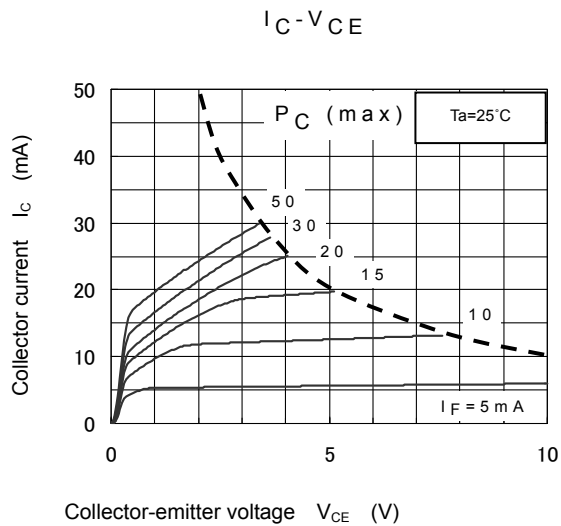
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise Time	t_r	$V_{CC} = 10 \text{ V}$, $I_C = 2 \text{ mA}$ $R_L = 100 \Omega$	—	2	—	μs
Fall Time	t_f		—	3	—	
Turn-On Time	t_{on}		—	3	—	
Turn-Off Time	t_{off}		—	3	—	
Turn-On Time	t_{ON}	$R_L = 1.9 \text{ k}\Omega$ $V_{CC} = 5 \text{ V}$, $I_F = 16 \text{ mA}$ (Fig.1)	—	2	—	μs
Storage Time	t_s		—	25	—	
Turn-Off Time	t_{OFF}		—	40	—	

(Fig.1) Switchin Time Test Circuit

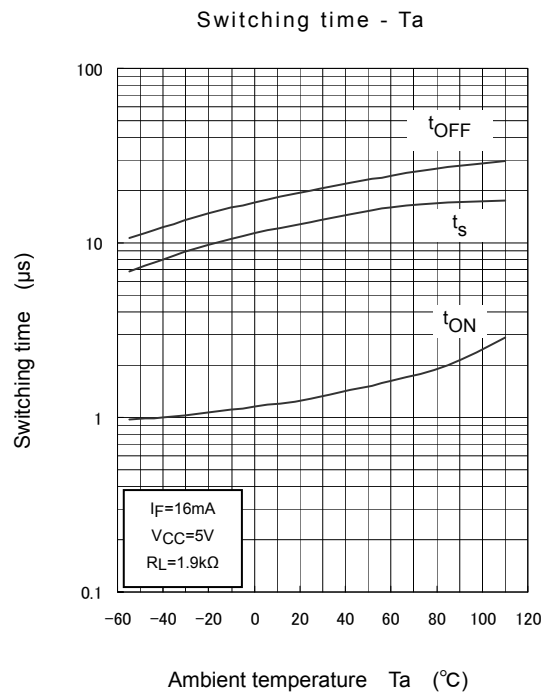
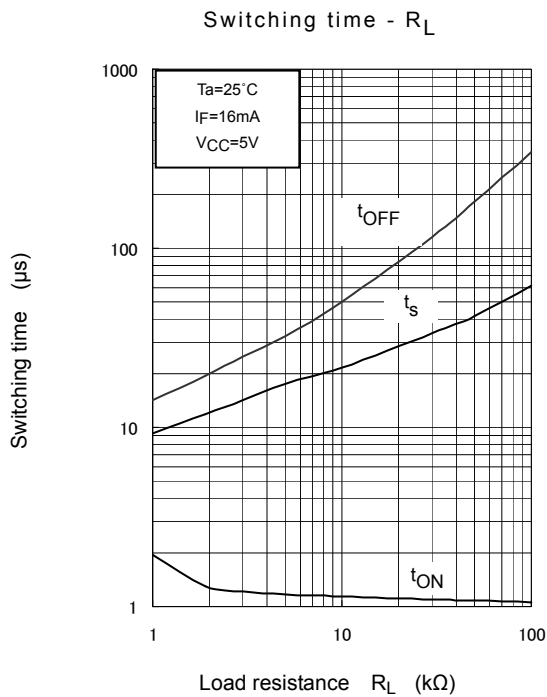
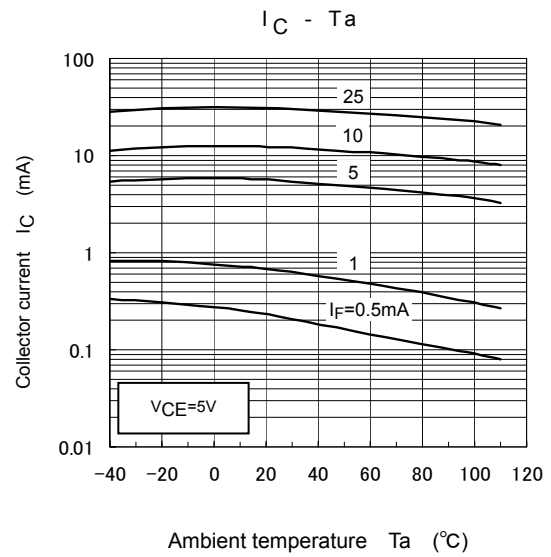
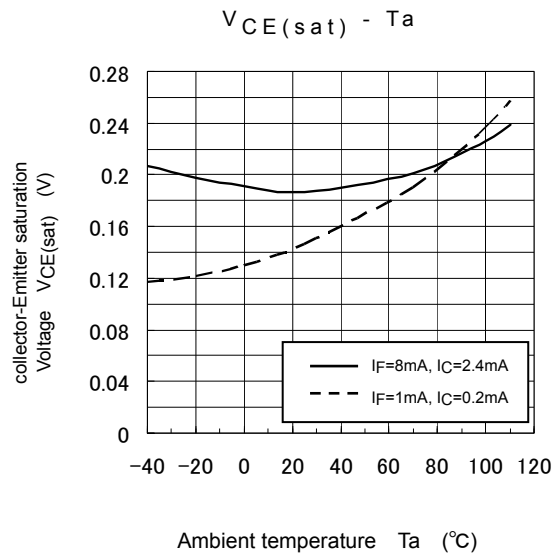


$I_F - T_a$  $P_C - T_a$  $I_{FP} - D_R$  $I_F - V_F$  $\Delta V_F / \Delta T_a - I_F$  $I_{FP} - V_{FP}$ 

*The above graphs show typical characteristic.



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Soldering and Storage

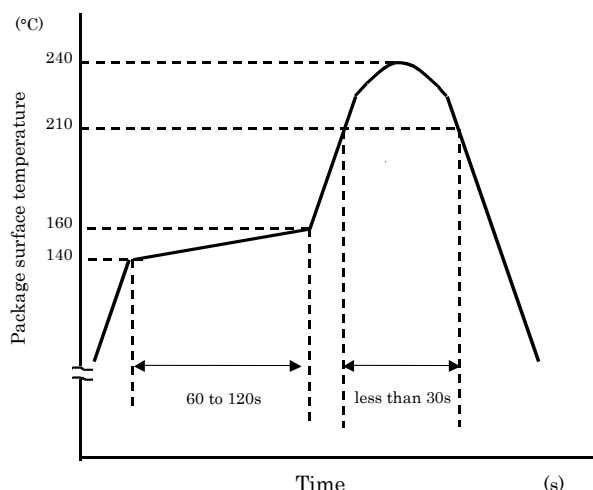
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

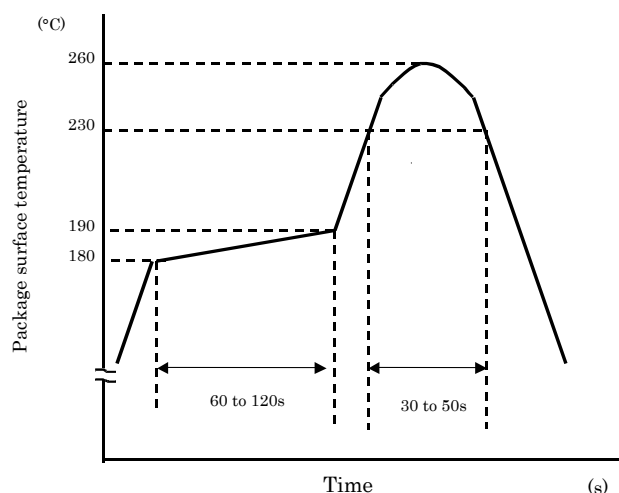
1) Using solder reflow

· Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

· Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

- Please preheat it at 150°C between 60 and 120 seconds.
- Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

- 1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
- 2) Follow the precautions printed on the packing label of the device for transportation and storage.
- 3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
- 4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
- 5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
- 6) When restoring devices after removal from their packing, use anti-static containers.
- 7) Do not allow loads to be applied directly to devices while they are in storage.
- 8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

Option: Specification for Embossed-Tape Packing (TP) for Mini-Flat Coupler

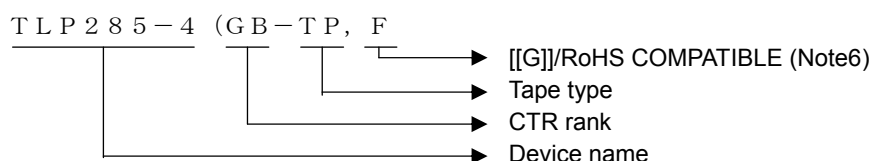
1. Applicable Package

Package Name	Product Type
SOP16	Mini-Flat Coupler

2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

(Example)



3. Tape Dimensions

3.1 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.

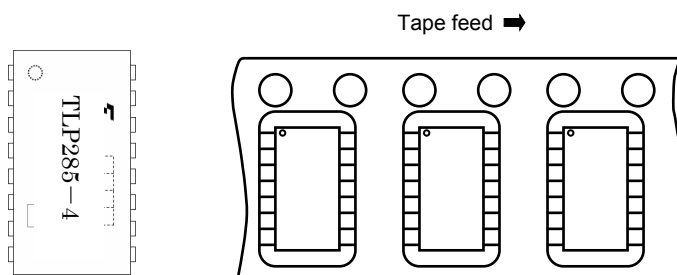


Figure 1 Device Orientation

3.2 Tape Packing Quantity : 2500 devices per reel

3.3 Empty Device Recesses are as Shown in Table 1.

Table 1 Empty Device Recesses

	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 device (max) per reel	Not including leader and trailer

3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

3.5 Tape Specification

- (1) Tape material: Plastic (protection against electrostatics)
- (2) Dimensions: The tape dimensions are as shown in Figure 2 and table 2.

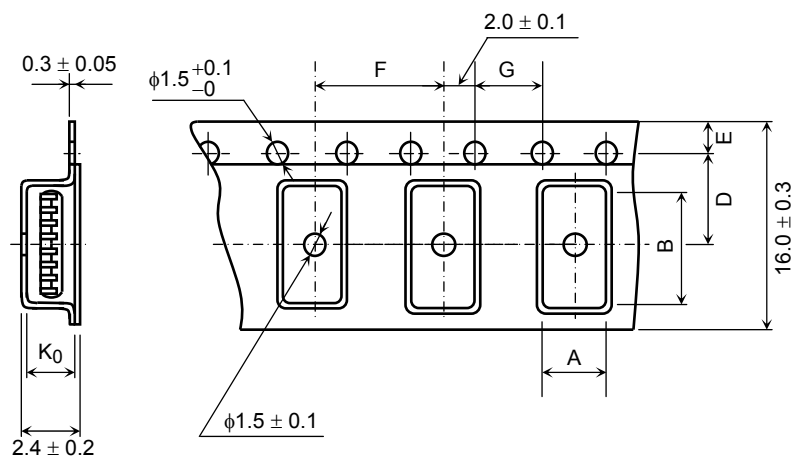


Figure2 Tape Forms

Table2 Tape Dimensions

Unit: mm
Unless otherwise specified: ± 0.1

Symbol	Dimension	Remark
A	7.5	—
B	10.5	—
D	7.5	Center line of indented square hole and sprocket hole
E	1.75	Distance between tape edge and hole center
F	12.0	Cumulative error $\begin{smallmatrix} +0.1 \\ -0.3 \end{smallmatrix}$ (max) per 10 feed holes
G	4.0	Cumulative error $\begin{smallmatrix} +0.1 \\ -0.3 \end{smallmatrix}$ (max) per 10 feed holes
K ₀	2.2	Internal space

3.6 Reel

- (1) Material: Plastic
- (2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 3.

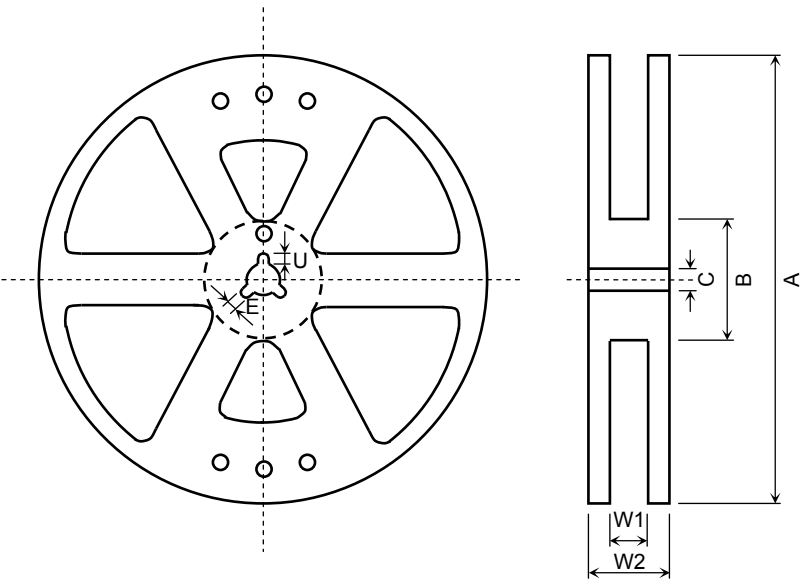


Figure 3 Reel Forms

Table 3 Reel Dimensions

Unit: mm

Symbol	Dimension
A	$\phi 330 \pm 2$
B	$\phi 80 \pm 1$
C	$\phi 13 \pm 0.5$
E	2.0 ± 0.5
U	4.0 ± 0.5
W1	17.5 ± 0.5
W2	21.5 ± 1.0

4. Packing

Either one reel or five reels of photocouplers are packed in a shipping carton.

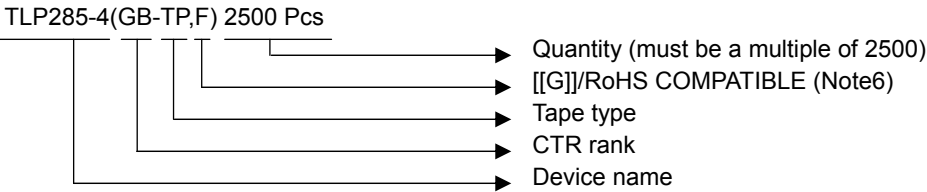
5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.

(Example)



Note6: Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

TOSHIBA Photocoupler

Option:(V4)Attachment : Specifications for EN60747-5-2 option: (V4)

Types : TLP285-4

Type designations for “option: (V4)”, which are tested under EN60747 requirements.

Ex.: TLP285-4 (V4GB-TP,F V4 : EN60747 option
 GB: CTR rank type
 TP : Standard tape & reel type
 F : [[G]]/RoHS COMPATIBLE (Note6)

Note: Use TOSHIBA standard type number for safety standard application.

Ex.: TLP285-4 (V4GB-TP,F → TLP285-4

EN60747 Isolation Characteristics

Description	Symbol	Rating	Unit
Application classification for rated mains voltage $\leq 150\text{Vrms}$ for rated mains voltage $\leq 300\text{Vrms}$		I-IV I-III	—
Climatic classification		55 / 110 / 21	—
Pollution degree		2	—
Maximum operating insulation voltage	V_{IORM}	707	Vpk
Input to output test voltage, Method A $V_{pr}=1.5 \times V_{IORM}$, type and sample test $t_p=10\text{s}$, partial discharge $<5\text{pC}$	V_{pr}	1060	Vpk
Input to output test voltage, Method B $V_{pr}=1.875 \times V_{IORM}$, 100% production test $t_p=1\text{s}$, partial discharge $<5\text{pC}$	V_{pr}	1325	Vpk
Highest permissible overvoltage (transient overvoltage, $t_{pr}=60\text{s}$)	V_{TR}	6000	Vpk
Safety limiting values (max. permissible ratings in case of fault, also refer to thermal derating curve) current (input current I_F , $P_{si}=0$) power (output or total power dissipation) temperature	I_{si} P_{si} T_{si}	250 400 150	mA mW °C
Insulation resistance $V_{IO}=500\text{V}$, $T_a=T_{si}$	R_{si}	$\geq 10^9$	Ω

Insulation Related Specifications

Minimum creepage distance	Cr	5.0mm
Minimum clearance	Cl	5.0mm
Minimum insulation thickness	ti	0.4mm
Comparative tracking index	CTI	175

1. If a printed circuit is incorporated, the creepage distance and clearance may be reduced below this value.
(e.g. at a standard distance between soldering eye centers of 3.5mm).
If this is not permissible, the user shall take suitable measures.
2. This photocoupler is suitable for 'safe electrical isolation' only within the safety limit data.
Maintenance of the safety data shall be ensured by means of protective circuit.

VDE test sign : Marking on product
for EN60747

V

: Marking on packing
for EN60747



Marking Example : TLP285-4(F)

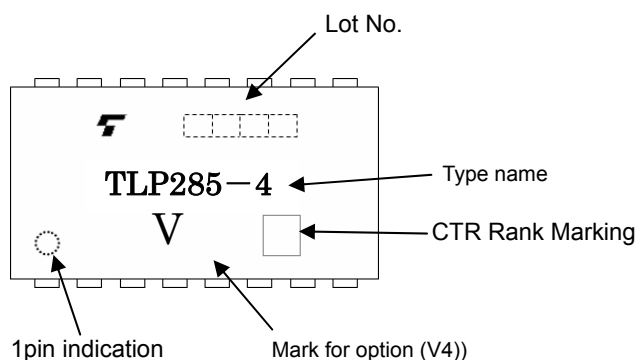


Figure 1 Partial discharge measurement procedure according to EN60747
Destructive test for qualification and sampling tests.

Method A

(for type and sampling tests,
destructive tests)

t_1, t_2 = 1 to 10 s
 t_3, t_4 = 1 s
 t_p (Measuring time for
partial discharge) = 10 s
 t_b = 12 s
 t_{ini} = 60 s

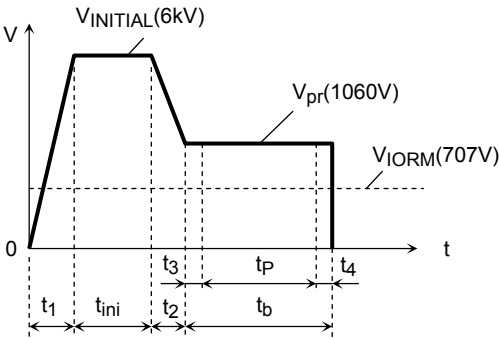


Figure 2 Partial discharge measurement procedure according to EN60747
Non-destructive test for 100% inspection.

Method B

(for sample test, non-
destructive test)

t_3, t_4 = 0.1 s
 t_p (Measuring time for
partial discharge) = 1 s
 t_b = 1.2 s

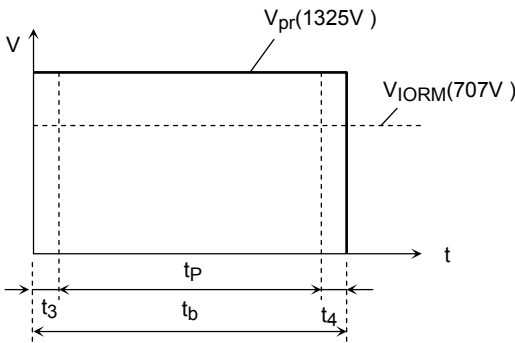
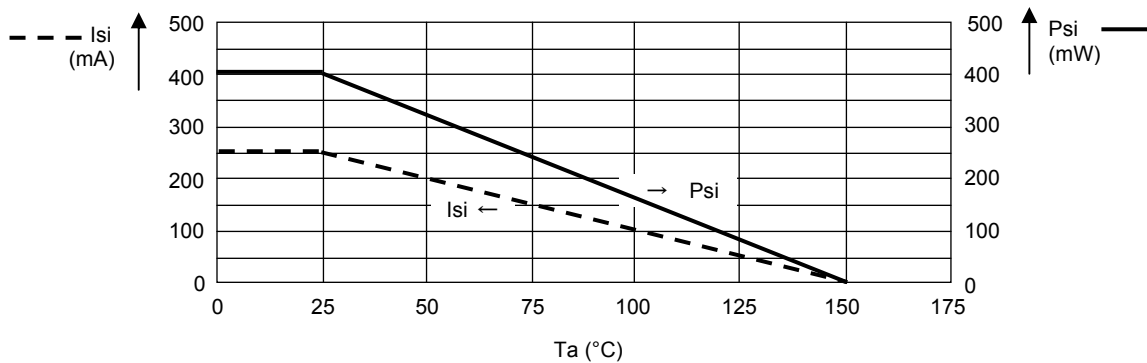


Figure 3 Dependency of maximum safety ratings on ambient temperature



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