

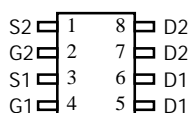
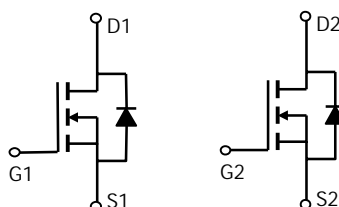
AO4800B, AO4800BL
Dual N-Channel Enhancement Mode Field Effect Transistor
General Description

The AO4800B/L uses advanced trench technology to provide excellent $R_{DS(ON)}$ and low gate charge. The two MOSFETs make a compact and efficient switch and synchronous rectifier combination for use in buck converters. *Standard Product AO4800B/L is Pb-free (meets ROHS & Sony 259 specifications).*

Features

$V_{DS} (V) = 30V$
 $I_D = 6.9A (V_{GS} = 10V)$
 $R_{DS(ON)} < 27m\Omega (V_{GS} = 10V)$
 $R_{DS(ON)} < 32m\Omega (V_{GS} = 4.5V)$
 $R_{DS(ON)} < 50m\Omega (V_{GS} = 2.5V)$

UIS TESTED!
Rg, Ciss, Coss, Crss Tested!


SOIC-8

Absolute Maximum Ratings $T_A=25^\circ C$ unless otherwise noted

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	30	V
Gate-Source Voltage	V_{GS}	± 12	V
Continuous Drain Current ^{AF}	I_D	$T_A=25^\circ C$ 6.9	A
		$T_A=70^\circ C$ 5.8	
Pulsed Drain Current ^B	I_{DM}	40	
Power Dissipation	P_D	$T_A=25^\circ C$ 1.9	W
		$T_A=70^\circ C$ 1.2	
Avalanche Current ^B	I_{AR}	12	A
Repetitive avalanche energy 0.3mH ^B	E_{AR}	22	mJ
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 150	$^\circ C$

Thermal Characteristics

Parameter	Symbol	Typ	Max	Units
Maximum Junction-to-Ambient ^{AF}	$R_{\theta JA}$	55	62.5	$^\circ C/W$
Maximum Junction-to-Ambient ^A		90	110	$^\circ C/W$
Maximum Junction-to-Lead ^C	$R_{\theta JL}$	40	48	$^\circ C/W$

Electrical Characteristics (T_J=25°C unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
STATIC PARAMETERS						
BV _{DSS}	Drain-Source Breakdown Voltage	I _D =250μA, V _{GS} =0V	30			V
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} =24V, V _{GS} =0V T _J =55°C		0.002	1 5	μA
I _{GSS}	Gate-Body leakage current	V _{DS} =0V, V _{GS} = ±12V			100	nA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250μA	0.7	1	1.5	V
I _{D(ON)}	On state drain current	V _{GS} =4.5V, V _{DS} =5V	40			A
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =6.9A T _J =125°C		20 25	27 40	mΩ
		V _{GS} =4.5V, I _D =6A		23	32	mΩ
		V _{GS} =2.5V, I _D =5A		34	50	mΩ
g _{FS}	Forward Transconductance	V _{DS} =5V, I _D =5A	10	26		S
V _{SD}	Diode Forward Voltage	I _S =1A, V _{GS} =0V		0.71	1	V
I _S	Maximum Body-Diode Continuous Current				4.5	A
DYNAMIC PARAMETERS						
C _{iss}	Input Capacitance	V _{GS} =0V, V _{DS} =15V, f=1MHz		900	1100	pF
C _{oss}	Output Capacitance			88		pF
C _{rss}	Reverse Transfer Capacitance			65		pF
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, f=1MHz		0.95	1.5	Ω
SWITCHING PARAMETERS						
Q _g	Total Gate Charge	V _{GS} =4.5V, V _{DS} =15V, I _D =8.5A		10	12	nC
Q _{gs}	Gate Source Charge			1.8		nC
Q _{gd}	Gate Drain Charge			3.75		nC
t _{D(on)}	Turn-On DelayTime	V _{GS} =10V, V _{DS} =15V, R _L =1.8Ω, R _{GEN} =6Ω		3.2		ns
t _r	Turn-On Rise Time			3.5		ns
t _{D(off)}	Turn-Off DelayTime			21.5		ns
t _f	Turn-Off Fall Time			2.7		ns
t _{rr}	Body Diode Reverse Recovery Time	I _F =5A, dI/dt=100A/μs		16.8	20	ns
Q _{rr}	Body Diode Reverse Recovery Charge	I _F =5A, dI/dt=100A/μs		8	12	nC

A: The value of R_{θJA} is measured with the device mounted on 1in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The value in any given application depends on the user's specific board design. The current rating is based on the t_s ≤ 10s thermal resistance rating.

B: Repetitive rating, pulse width limited by junction temperature.

C: The R_{θJA} is the sum of the thermal impedance from junction to lead R_{θJL} and lead to ambient.

D: The static characteristics in Figures 1 to 6 are obtained using <300μs pulses, duty cycle 0.5% max.

E: These tests are performed with the device mounted on 1 in² FR-4 board with 2oz. Copper, in a still air environment with T_A=25°C. The SOA curve provides a single pulse rating.

Rev 1 : Dec 2007

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TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

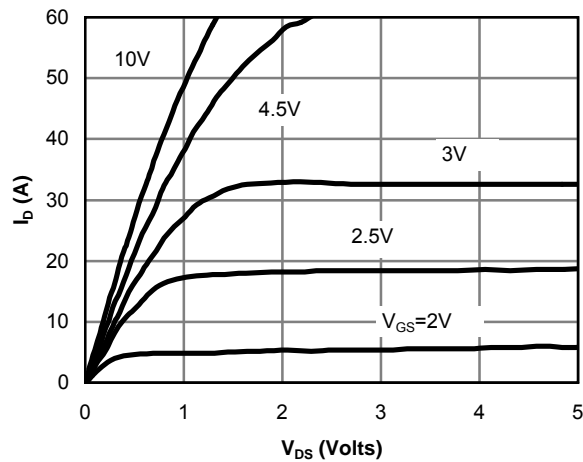


Fig 1: On-Region Characteristics

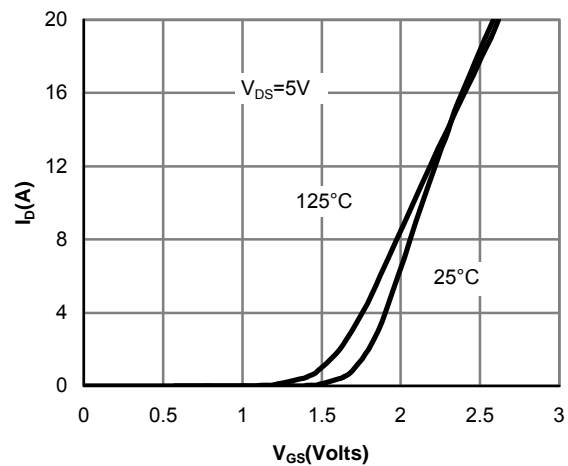


Figure 2: Transfer Characteristics

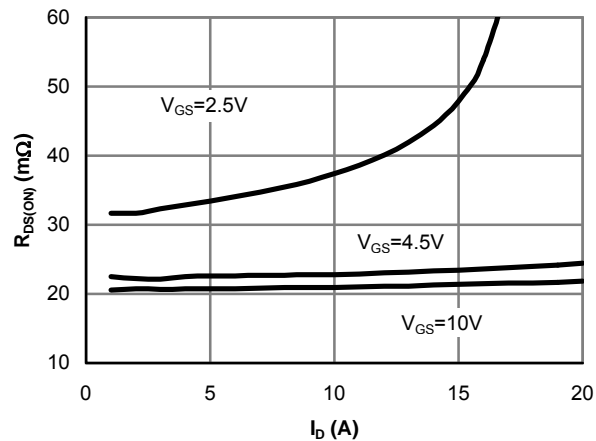


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

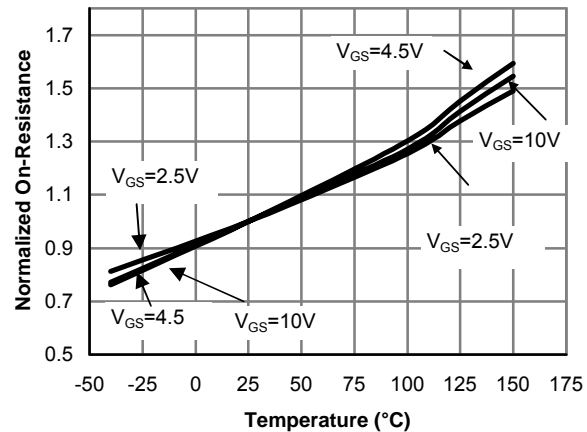


Figure 4: On-Resistance vs. Junction Temperature

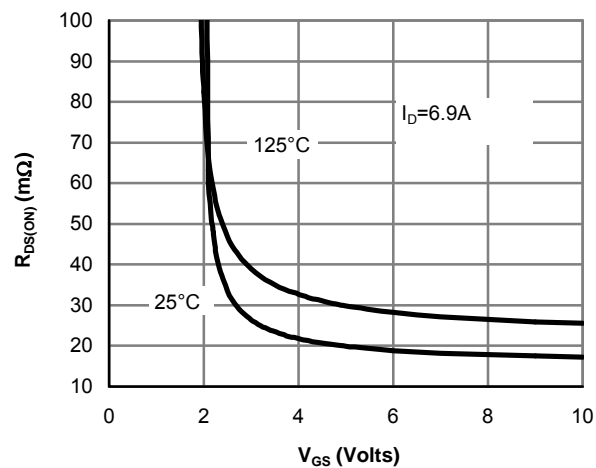


Figure 5: On-Resistance vs. Gate-Source Voltage

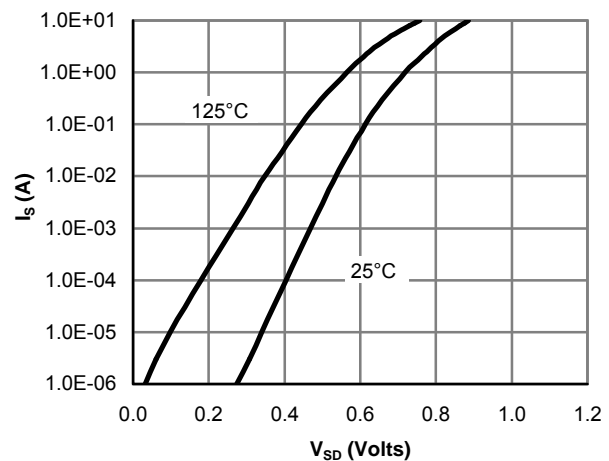


Figure 6: Body-Diode Characteristics

TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

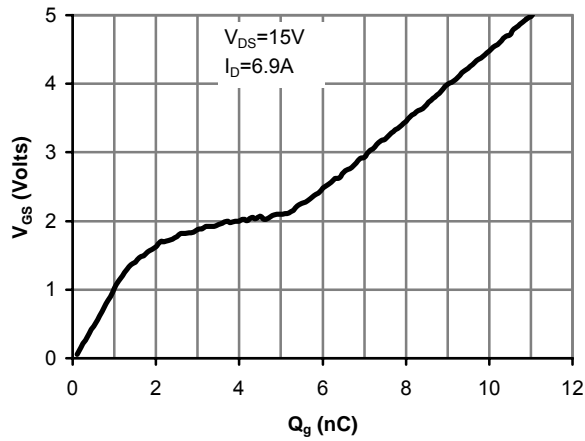


Figure 7: Gate-Charge Characteristics

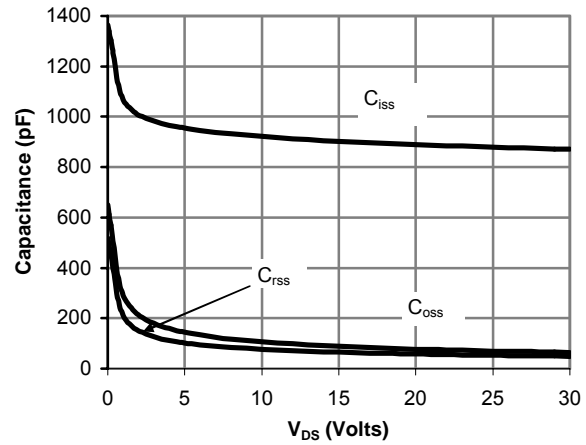


Figure 8: Capacitance Characteristics

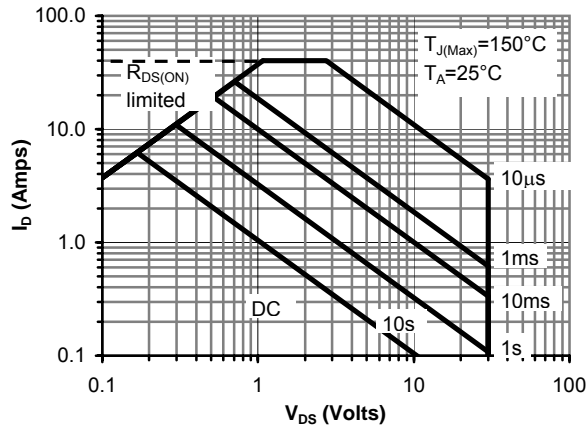


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

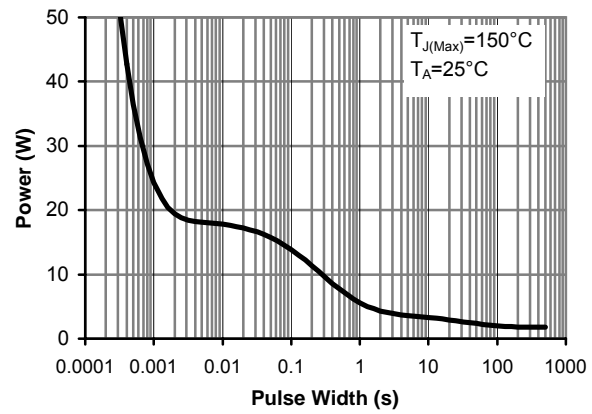


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

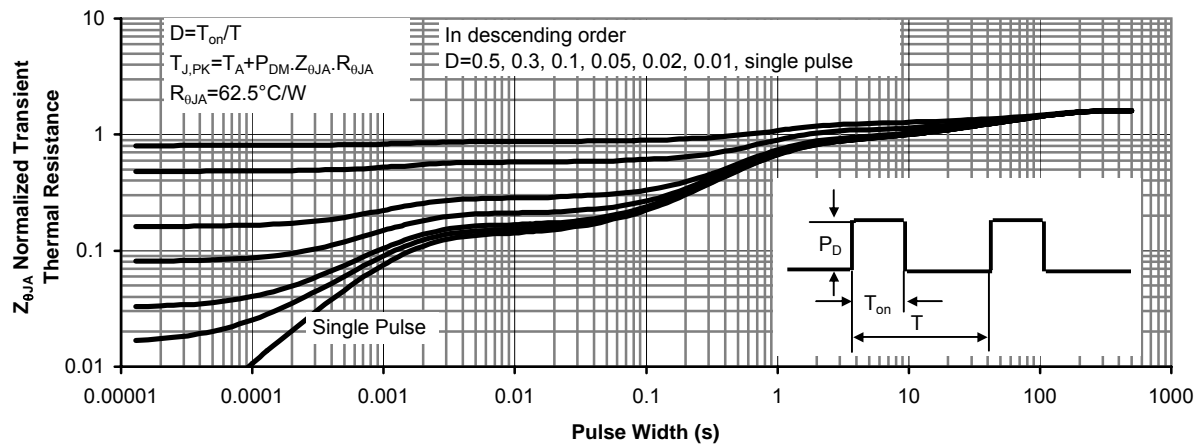


Figure 11: Normalized Maximum Transient Thermal Impedance