Unit: mm

TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

RN1112CT, RN1113CT

Switching Applications
Inverter Circuit Applications
Interface Circuit Applications
Driver Circuit Applications

- Incorporating a bias resistor into a transistor reduces parts count.
 Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Complementary to RN2112CT, RN2113CT

Equivalent Circuit

Note:

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V_{CBO}	20	V
Collector-emitter voltage	V _{CEO}	20	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	Ic	50	mA
Collector power dissipation	PC	50	mW
Junction temperature	Tj	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

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Weight: 0.75 mg (typ.)

JEITA TOSHIBA

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e.operatingtemperature/current/voltage, etc.) are within the absolute maximum ratings.

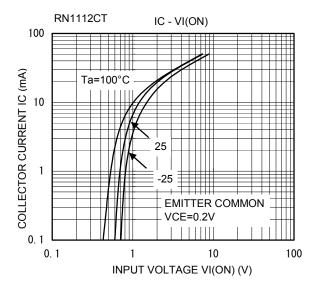
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

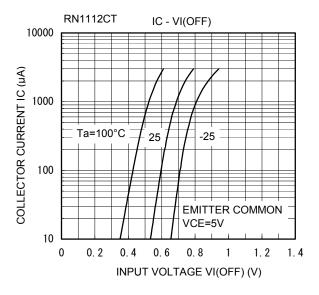


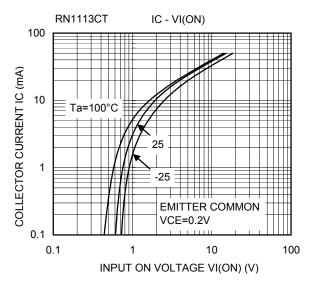
Electrical Characteristics (Ta = 25°C)

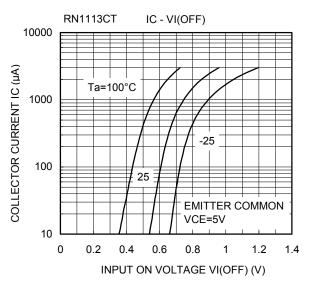
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I _{CBO}	$V_{CB} = 20 \text{ V}, I_E = 0$	_	_	100	nA
Emitter cut-off current		I _{EBO}	$V_{EB} = 5 \text{ V}, I_{C} = 0$	_	_	100	nA
DC current gain		h _{FE}	$V_{CE} = 5 \text{ V, } I_{C} = 1 \text{ mA}$	300	_	_	
Collector-emitter saturation voltage		V _{CE} (sat)	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$	_	_	0.15	V
Collector output capacitance		C _{ob}	V _{CB} = 10 V, I _E = 0, f = 1 MHz	_	1.2	_	pF
Input resistor	RN1112CT	- R1	_	17.6	22	26.4	kΩ
	RN1113CT			37.6	47	56.4	

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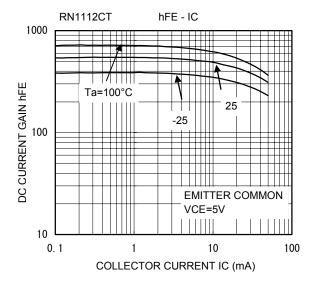


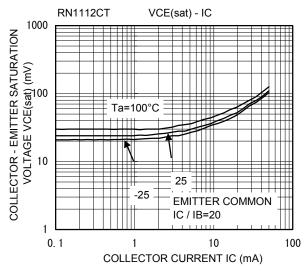


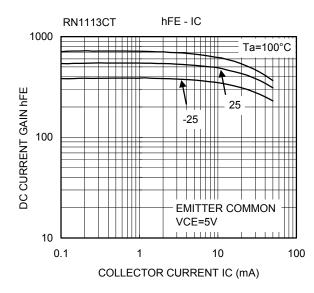


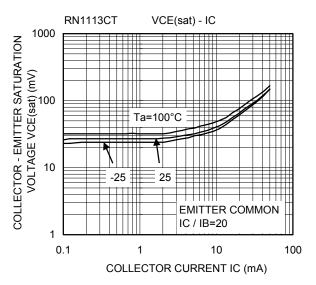


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Type Name	Marking	
RN1112CT	Type name 1 LH 3	
RN1113CT	Type name 1 2	

Handling Precaution

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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