

100mA / 50V Digital transistors (with built-in resistors)

DTC115EEB

●Applications

Inverter, Interface, Driver

●Features

- 1) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 2) Each bias resistor is a thin-film resistor.
Since they are completely insulated, the input can be negatively biased. The insulation also eliminates most of the parasitic effects.
- 3) Only the on / off conditions need to be set for operation, making the device design easy.

●Structure

NPN silicon epitaxial planar digital transistor

●Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
DTC115EEB		○

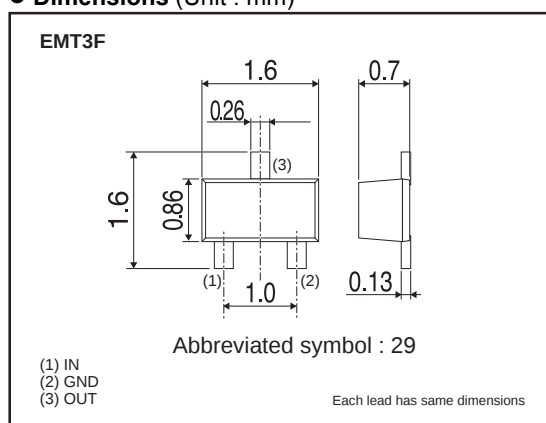
●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V _{CC}	50	V
Input voltage	V _{IN}	-10 to 40	V
Collector current	I _{C(Max.)} ^{*1}	100	mA
Output Current	I _O	20	mA
Power dissipation	P _D ^{*2}	150	mW
Junction temperature	T _j	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

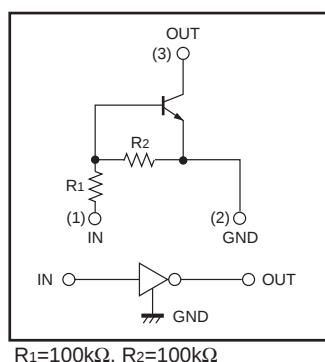
^{*1} Characteristics of built-in transistor

^{*2} Each terminal mounted on a recommended land

●Dimensions (Unit : mm)



●Inner circuit



●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Input voltage	$V_{I(off)}$	—	—	500	mV	$V_{CC}=5V, I_o=100\mu A$
	$V_{I(on)}$	3.0	—	—	V	$V_o=0.3V, I_o=1mA$
Output voltage	$V_{O(on)}$	—	100	300	mV	$I_o/I_i=5mA/0.25mA$
Input current	I_i	—	—	0.15	mA	$V_i=5V$
Output current	$I_{O(off)}$	—	—	500	nA	$V_{CC}=50V, V_i=0V$
DC current gain	G_i	82	—	—	—	$V_o=5V, I_o=5mA$
Transition frequency	f_T *	—	250	—	MHz	$V_{CE}=10V, I_E=-5mA, f=100MHz$
Input resistance	R_i	70	100	130	k Ω	—
Resistance ratio	R_2/R_1	0.8	1	1.2	—	—

* Characteristics of built-in transistor

●Electrical characteristic curves

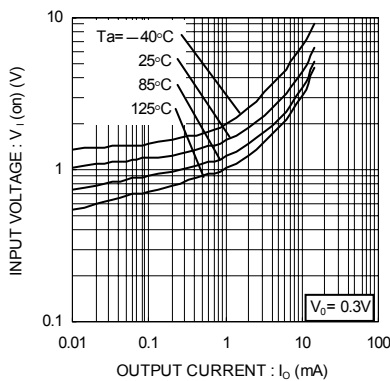


Fig.1 Input Voltage vs. Output Current (ON Characteristics)

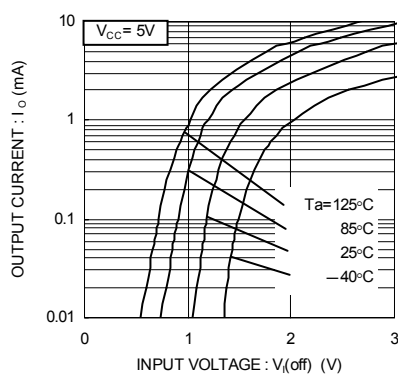


Fig.2 Output Current vs. Input Voltage (OFF Characteristics)

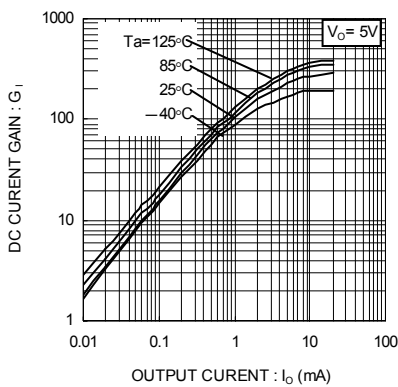


Fig.3 DC Current Gain vs. Output Current

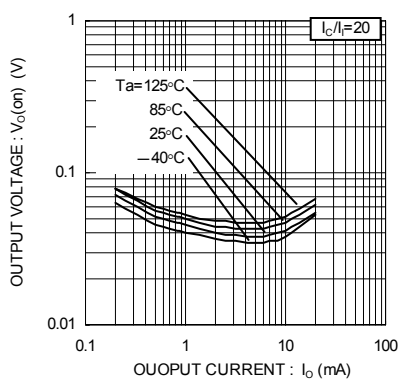


Fig.4 Output Voltage vs. Output Current

Notes

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