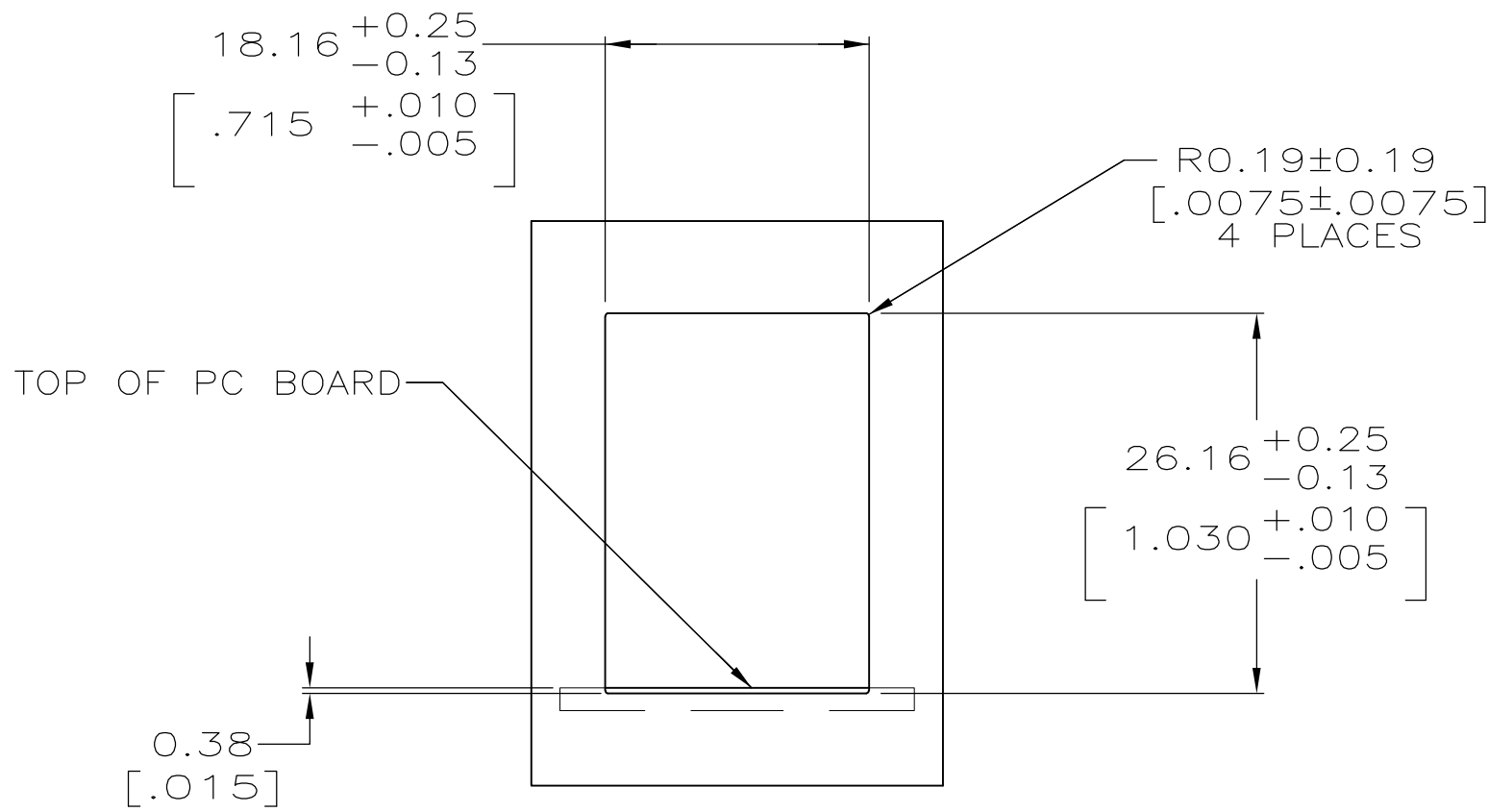
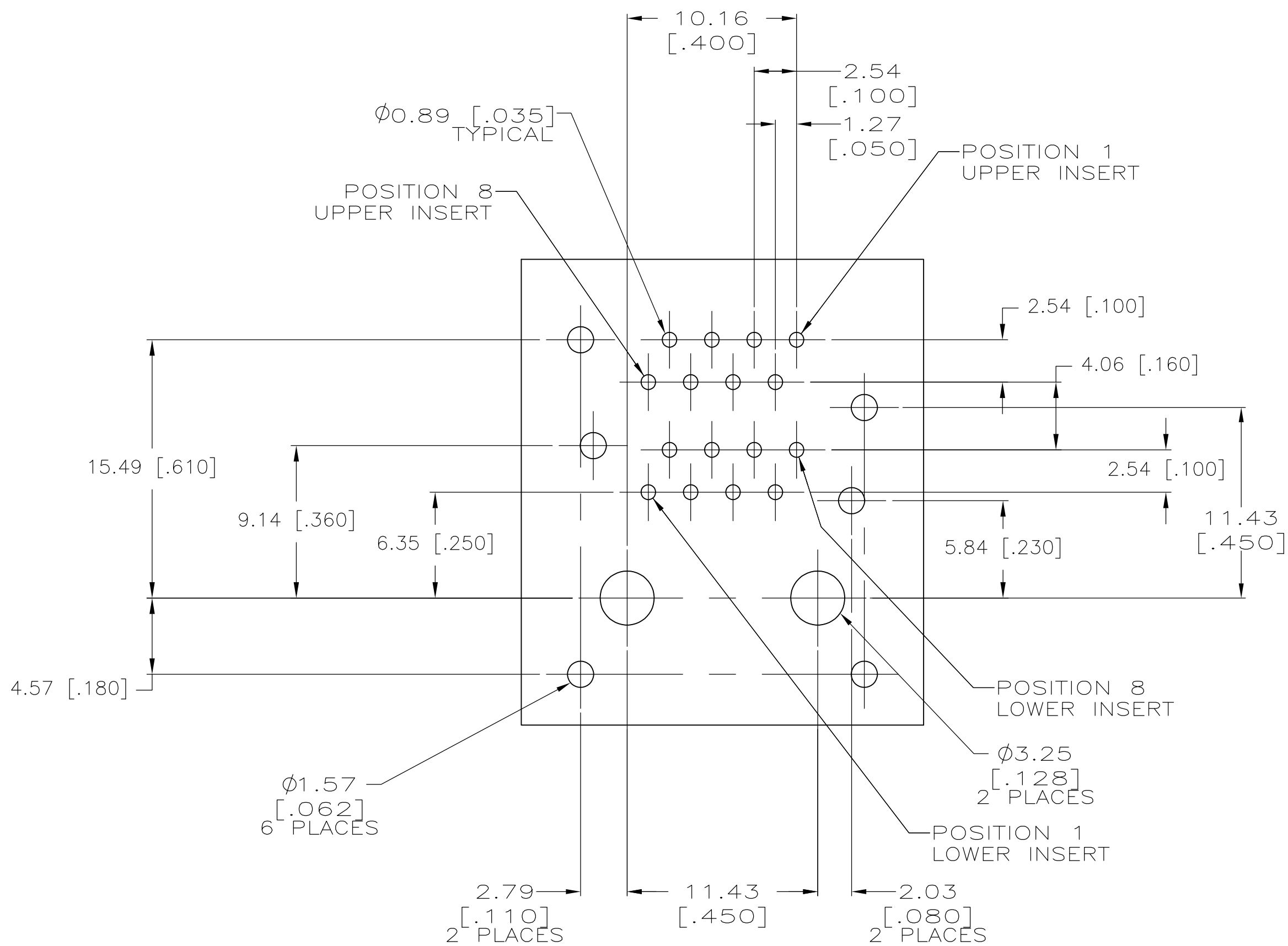
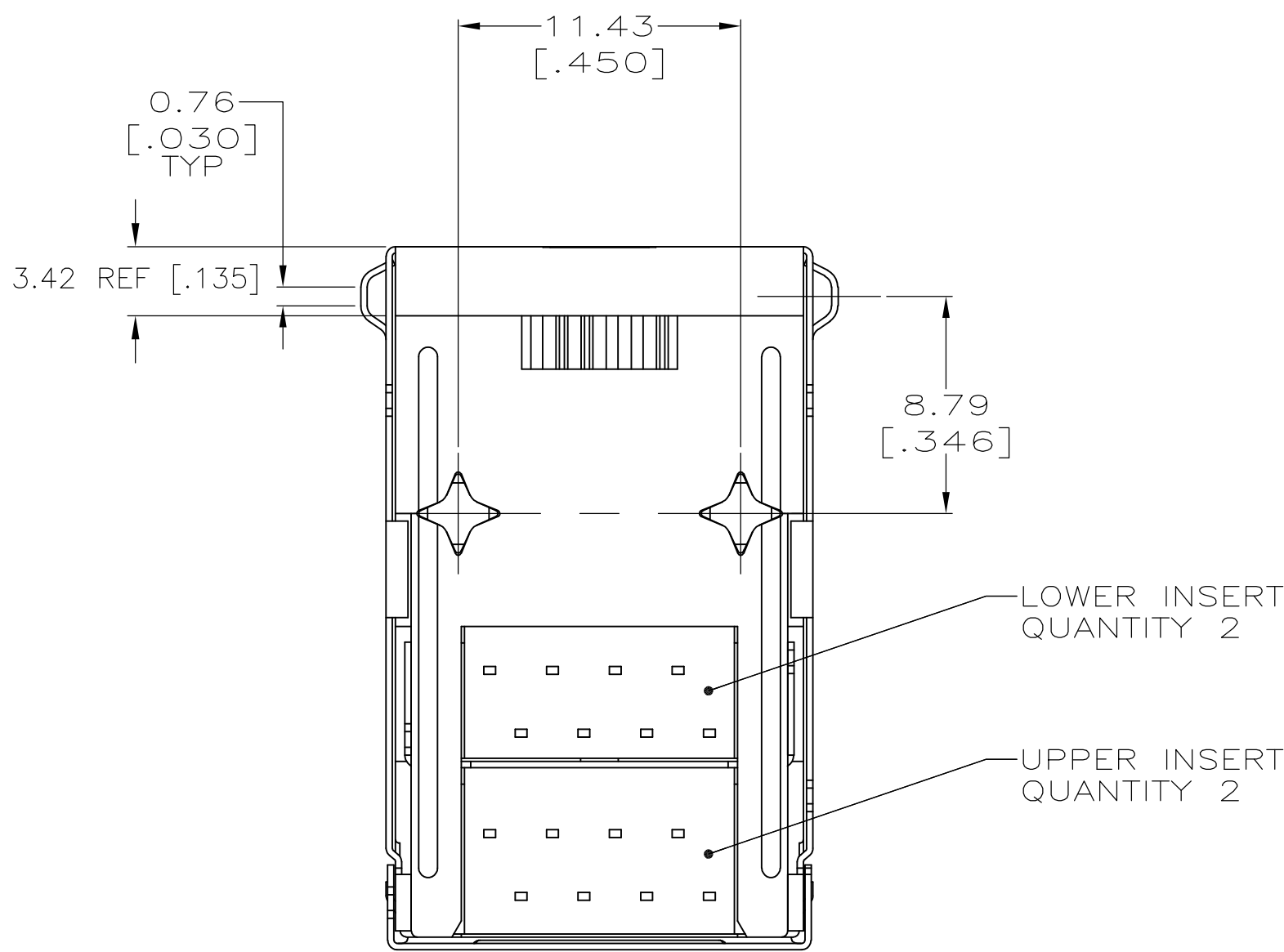
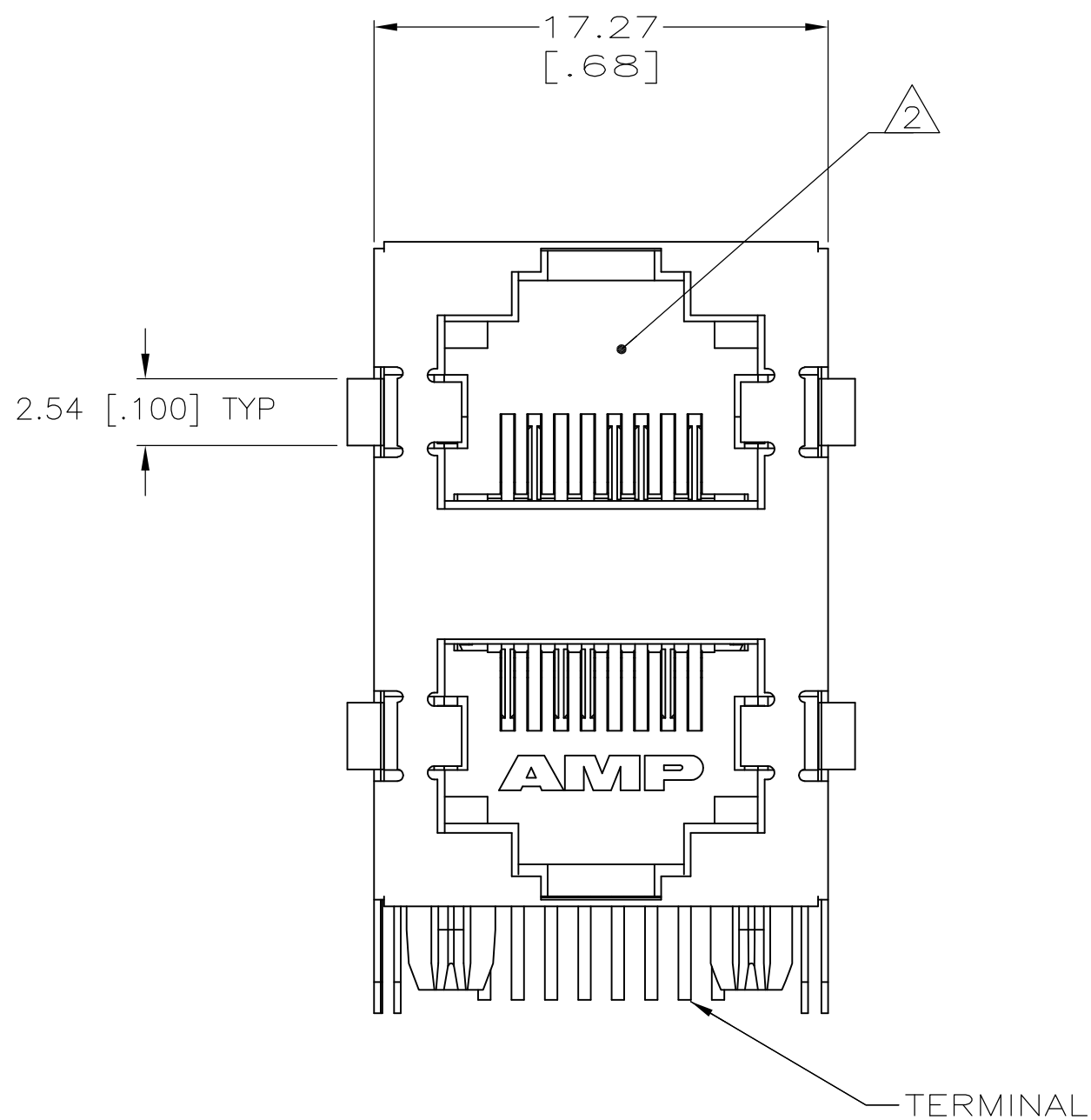
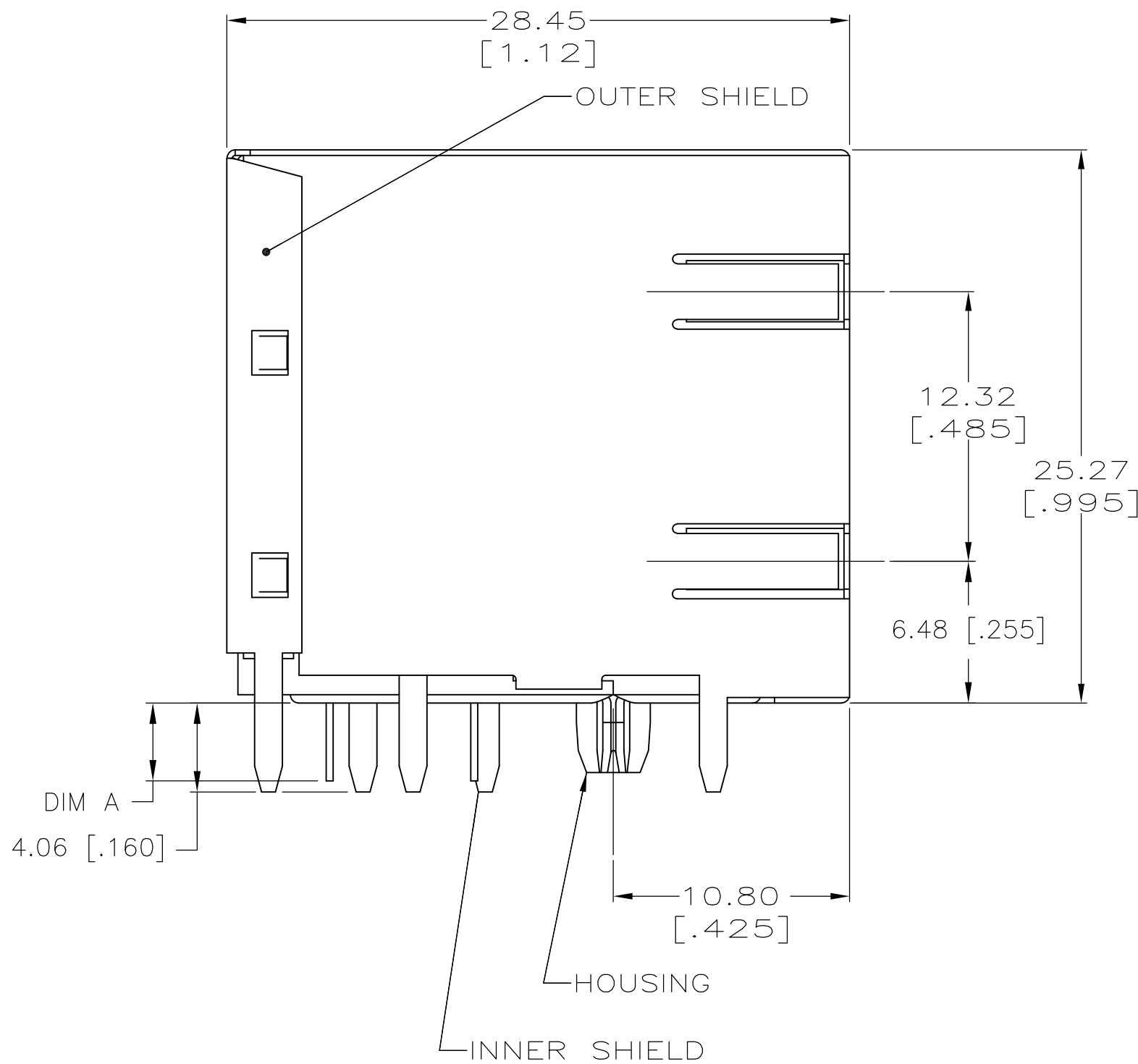
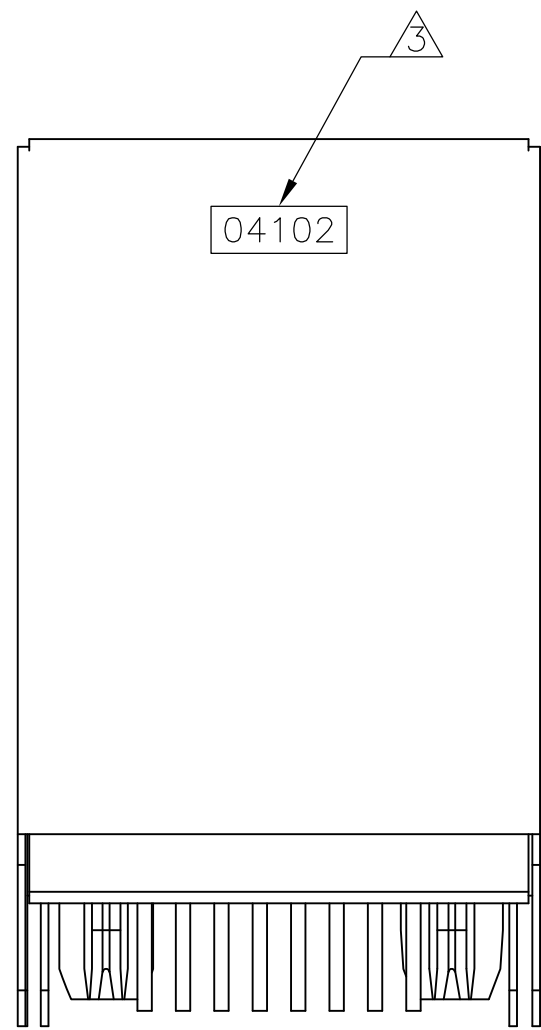


LOC		DIST		REVISIONS						
AA		00		P	LTR	DESCRIPTION		DATE	DWN	APVD
					C2	ECR-12-003706		29FEB2012	JJ	AC



SUGGESTED PANEL CUTOUT DIMENSIONS
SCALE 2:1

1. MATERIAL:
HOUSING - PCT POLYESTER, BLACK, UL94V-0.
OVERMOLDED TERMINAL ARRAY - PBT, BROWN.
TERMINALS - 0.33 [.013] THICK PHOS BRONZE
PLATED WITH 1.27um [.000050] MIN THICK
HARD GOLD IN LOCALIZED AREA AND 3.81um
[.000150] MIN THICK MATTE TIN IN SOLDER
AREA OVER 1.27um [.000050] MIN THICK
NICKEL UNDERPLATE.
SHIELDS - 0.25 [.010] THICK COPPER ALLOY
PLATED WITH 1.27 um [.000050] MINIMUM NICKEL
AND 2.03 um [.000080] MINIMUM HOT TIN DIP
ON PCB GROUND TABS.
- △ JACK CAVITY CONFORMS TO FCC RULES AND
REGULATIONS, PART 68 SUBPART F.
- △ DATE CODE LOCATED ON REAR OF PART
APPROXIMATELY AS SHOWN:
FIRST 2 DIGITS ARE LAST 2 DIGITS OF YEAR,
SECOND 2 DIGITS ARE MANUFACTURING
WORK WEEK,
LAST DIGIT IS DAY OF WEEK, WITH SUNDAY = 1.

PC BOARD LAYOUT VIEWED FROM COMPONENT SIDE

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN T. SPRINKLE/L.A. MAYER 21MAY2005	Tyco Electronics Corporation Harrisburg, Pa 17105-3608	
DIMENSIONS: mm [INCHES]		CHK J. WESTMAN 21MAY2005	NAME S. FLICKINGER 21MAY2005	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		PRODUCT SPEC 108-1163-2	APPLICATION SPEC 114-2048	
MATERIAL SEE NOTE 1		WEIGHT —	SIZE A1	
FINISH SEE NOTE 1		CUSTOMER DRAWING	SCALE 4:1	
		SHEET 1 OF 1	REV C2	