

# XTR112 XTR114

## 4-20mA CURRENT TRANSMITTERS with Sensor Excitation and Linearization

### FEATURES

- LOW UNADJUSTED ERROR
- PRECISION CURRENT SOURCES  
XTR112: Two 250 $\mu$ A  
XTR114: Two 100 $\mu$ A
- RTD OR BRIDGE EXCITATION
- LINEARIZATION
- TWO OR THREE-WIRE RTD OPERATION
- LOW OFFSET DRIFT: 0.4 $\mu$ V/ $^{\circ}$ C
- LOW OUTPUT CURRENT NOISE: 30nAp-p
- HIGH PSR: 110dB min
- HIGH CMR: 86dB min
- WIDE SUPPLY RANGE: 7.5V TO 36V
- SO-14 SOIC PACKAGE

### DESCRIPTION

The XTR112 and XTR114 are monolithic 4-20mA, two-wire current transmitters. They provide complete current excitation for high impedance platinum RTD temperature sensors and bridges, instrumentation amplifier, and current output circuitry on a single integrated circuit. The XTR112 has two 250 $\mu$ A current sources while the XTR114 has two 100 $\mu$ A sources for RTD excitation.

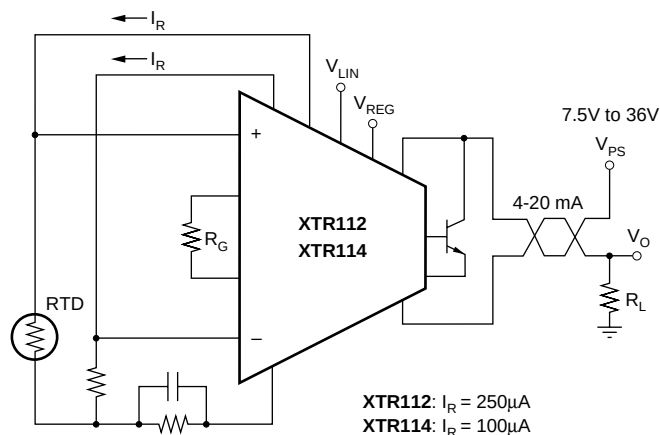
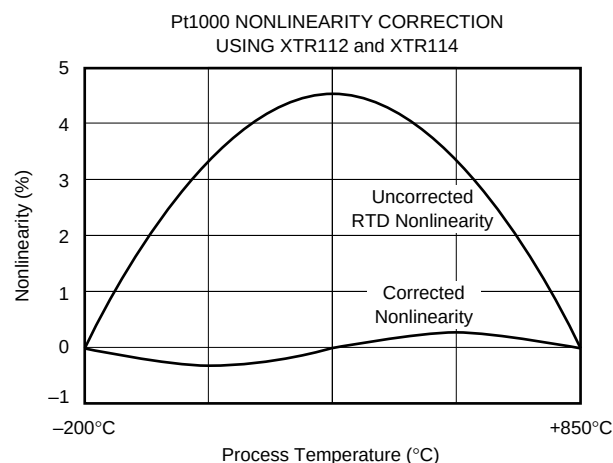
Versatile linearization circuitry provides a 2nd-order correction to the RTD, typically achieving a 40:1 improvement in linearity.

Instrumentation amplifier gain can be configured for a wide range of temperature or pressure measurements. Total unadjusted error of the complete current transmitter is low enough to permit use without adjustment in many applications. This includes zero output current drift, span drift and nonlinearity. The XTR112 and XTR114 operate on loop power supply voltages down to 7.5V.

Both are available in an SO-14 surface-mount package and are specified for the  $-40^{\circ}$ C to  $+85^{\circ}$ C industrial temperature range.

### APPLICATIONS

- INDUSTRIAL PROCESS CONTROL
- FACTORY AUTOMATION
- SCADA REMOTE DATA ACQUISITION
- REMOTE TEMPERATURE AND PRESSURE TRANSDUCERS



# SPECIFICATIONS

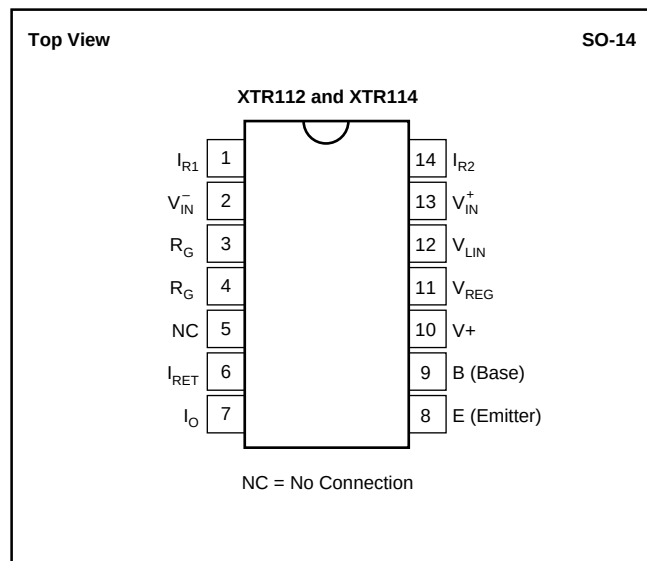
At  $T_A = +25^{\circ}\text{C}$ ,  $V_+ = 24\text{V}$ , and TIP29C external transistor, unless otherwise noted.

|                                                                                                                                                                                                                                                                                                                                 |                                                                                                                 | XTR112U<br>XTR114U                                                                |                                                                                                             |                                                             | XTR112UA<br>XTR114UA |                                                          |                                                                        |                                                                                                     |
|---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------------------|-----------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------|----------------------|----------------------------------------------------------|------------------------------------------------------------------------|-----------------------------------------------------------------------------------------------------|
| PARAMETER                                                                                                                                                                                                                                                                                                                       | CONDITIONS                                                                                                      | MIN                                                                               | TYP                                                                                                         | MAX                                                         | MIN                  | TYP                                                      | MAX                                                                    | UNITS                                                                                               |
| <b>OUTPUT</b><br>Output Current Equation<br>Output Current, Specified Range<br>Over-Scale Limit<br>Under-Scale Limit: XTR112<br>XTR114                                                                                                                                                                                          | $I_{REG} = 0$                                                                                                   | $I_O = V_{IN} \cdot (40/R_G) + 4\text{mA}$ , $V_{IN}$ in Volts, $R_G$ in $\Omega$ |                                                                                                             |                                                             |                      |                                                          |                                                                        | A<br>mA<br>mA<br>mA<br>mA                                                                           |
| <b>ZERO OUTPUT</b> <sup>(1)</sup><br>Initial Error<br>vs Temperature<br>vs Supply Voltage, V+<br>vs Common-Mode Voltage<br>vs V <sub>REG</sub> Output Current<br>Noise: 0.1Hz to 10Hz                                                                                                                                           | $V_{IN} = 0\text{V}$ , $R_G = \infty$<br><br>V+ = 7.5V to 36V<br>$V_{CM} = 1.25\text{V}$ to 3.5V <sup>(2)</sup> |                                                                                   | 4<br>±5<br>±0.07<br>0.04<br>0.02<br>0.3<br>0.03                                                             | ±25<br>±0.5<br>0.2                                          |                      | *<br>*<br>*<br>*<br>*<br>*<br>*                          | ±50<br>±0.9<br>*<br>*<br>*<br>*<br>*                                   | mA<br>μA<br>μA/°C<br>μA/V<br>μA/V<br>μA/mA<br>μAp-p                                                 |
| <b>SPAN</b><br>Span Equation (transconductance)<br>Initial Error <sup>(3)</sup><br>vs Temperature <sup>(3)</sup><br>Nonlinearity: Ideal Input <sup>(4)</sup>                                                                                                                                                                    | Full Scale ( $V_{IN}$ ) = 50mV<br><br>Full Scale ( $V_{IN}$ ) = 50mV                                            |                                                                                   | S = 40/ $R_G$<br>±0.05<br>±3<br>0.003                                                                       | ±0.2<br>±25<br>0.01                                         |                      | *<br>*<br>*<br>*                                         | ±0.4<br>*<br>*<br>*                                                    | A/V<br>%<br>ppm/°C<br>%                                                                             |
| <b>INPUT</b> <sup>(5)</sup><br>Offset Voltage<br>vs Temperature<br>vs Supply Voltage, V+<br>vs Common-Mode Voltage,<br>RTI (CMRR)<br>Common-Mode Input Range <sup>(2)</sup><br>Input Bias Current<br>vs Temperature<br>Input Offset Current<br>vs Temperature<br>Impedance: Differential<br>Common-Mode<br>Noise: 0.1Hz to 10Hz | $V_{CM} = 2\text{V}$<br><br>V+ = 7.5V to 36V<br>$V_{CM} = 1.25\text{V}$ to 3.5V <sup>(2)</sup>                  | 1.25                                                                              | ±50<br>±0.4<br>±0.3<br>±10<br><br>5<br>20<br>±0.2<br>5<br>0.1    1<br>5    10<br>0.6                        | ±100<br>±1.5<br>±3<br>±50<br><br>3.5<br>25<br>±3<br><br>0.6 | *                    | *<br>*<br>*<br>*<br><br>*<br>*<br>*<br>*<br>*<br>*       | ±250<br>±3<br>*<br>±100<br><br>*<br>50<br>±10<br><br>*                 | μV<br>μV/°C<br>μV/V<br>μV/V<br><br>V<br>nA<br>pA/°C<br>nA<br>pA/°C<br>GΩ    pF<br>GΩ    pF<br>μVp-p |
| <b>CURRENT SOURCES</b><br>Current: XTR112<br>XTR114<br>Accuracy<br>vs Temperature<br>vs Power Supply, V+<br>Matching<br>vs Temperature<br>vs Power Supply, V+<br>Compliance Voltage, Positive<br>Negative <sup>(2)</sup><br>Output Impedance: XTR112<br>XTR114<br>Noise: 0.1Hz to 10Hz: XTR112<br>XTR114                        | $V_O = 2\text{V}$ <sup>(6)</sup><br><br>V+ = 7.5V to 36V<br><br>V+ = 7.5V to 36V                                | (V+) -3<br>0                                                                      | 250<br>100<br>±0.05<br>±15<br>±10<br>±0.02<br>±3<br>1<br>(V+) -2.5<br>-0.2<br>500<br>1.2<br>0.001<br>0.0004 | ±0.2<br>±35<br>±25<br>±0.1<br>±15<br>10                     | *<br>*               | *<br>*<br>*<br>*<br>*<br>*<br>*<br>*<br>*<br>*<br>*<br>* | ±0.4<br>±75<br>*<br>±0.2<br>±30<br>*<br><br>*<br>*<br>*<br>*<br>*<br>* | μA<br>μA<br>%<br>ppm/°C<br>ppm/V<br>%<br>ppm/°C<br>ppm/V<br>V<br>V<br>MΩ<br>GΩ<br>μAp-p<br>μAp-p    |
| <b>V<sub>REG</sub></b> <sup>(2)</sup><br>Accuracy<br>vs Temperature<br>vs Supply Voltage, V+<br>Output Current: XTR112<br>XTR114<br>Output Impedance                                                                                                                                                                            |                                                                                                                 |                                                                                   | 5.1<br>±0.02<br>±0.2<br>1<br>-1, +2.1<br>-1, +2.4<br>75                                                     | ±0.1                                                        |                      | *<br>*<br>*<br>*<br>*<br>*<br>*                          | *<br><br><br><br><br><br><br>                                          | V<br>V<br>mV/°C<br>mV/V<br>mA<br>mA<br>Ω                                                            |
| <b>LINEARIZATION</b><br>R <sub>LIN</sub> (internal)<br>Accuracy<br>vs Temperature                                                                                                                                                                                                                                               |                                                                                                                 |                                                                                   | 1<br>±0.2<br>±25                                                                                            | ±0.5<br>±100                                                |                      | *<br>*<br>*                                              | ±1<br>*                                                                | kΩ<br>%<br>ppm/°C                                                                                   |
| <b>POWER SUPPLY</b><br>Specified Voltage<br>Operating Voltage Range                                                                                                                                                                                                                                                             |                                                                                                                 | +7.5                                                                              | +24                                                                                                         | +36                                                         | *                    | *                                                        | *                                                                      | V<br>V                                                                                              |
| <b>TEMPERATURE RANGE</b><br>Specification, T <sub>MIN</sub> to T <sub>MAX</sub><br>Operating/Storage Range<br>Thermal Resistance, θ <sub>JA</sub><br>SO-14 Surface-Mount                                                                                                                                                        |                                                                                                                 | -40<br>-55                                                                        |                                                                                                             | +85<br>+125                                                 | *<br>*               |                                                          | *<br>*                                                                 | °C<br>°C<br>°C/W                                                                                    |

\* Specification same as XTR112U, XTR114U.

NOTES: (1) Describes accuracy of the 4mA low-scale offset current. Does not include input amplifier effects. Can be trimmed to zero. (2) Voltage measured with respect to  $I_{\text{RET}}$  pin. (3) Does not include initial error or TCR of gain-setting resistor,  $R_G$ . (4) Increasing the full-scale input range improves nonlinearity. (5) Does not include Zero Output initial error. (6) Current source output voltage with respect to  $I_{\text{RET}}$  pin.

## PIN CONFIGURATION



## ABSOLUTE MAXIMUM RATINGS<sup>(1)</sup>

|                                                                  |                 |
|------------------------------------------------------------------|-----------------|
| Power Supply, $V^+$ (referenced to $I_O$ pin)                    | 40V             |
| Input Voltage, $V_{IN}^+$ , $V_{IN}^-$ (referenced to $I_O$ pin) | 0V to $V^+$     |
| Storage Temperature Range                                        | -55°C to +125°C |
| Lead Temperature (soldering, 10s)                                | +300°C          |
| Output Current Limit                                             | Continuous      |
| Junction Temperature                                             | +165°C          |

NOTE: (1) Stresses above these ratings may cause permanent damage. Exposure to absolute maximum conditions for extended periods may degrade device reliability.



## ELECTROSTATIC DISCHARGE SENSITIVITY

This integrated circuit can be damaged by ESD. Burr-Brown recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

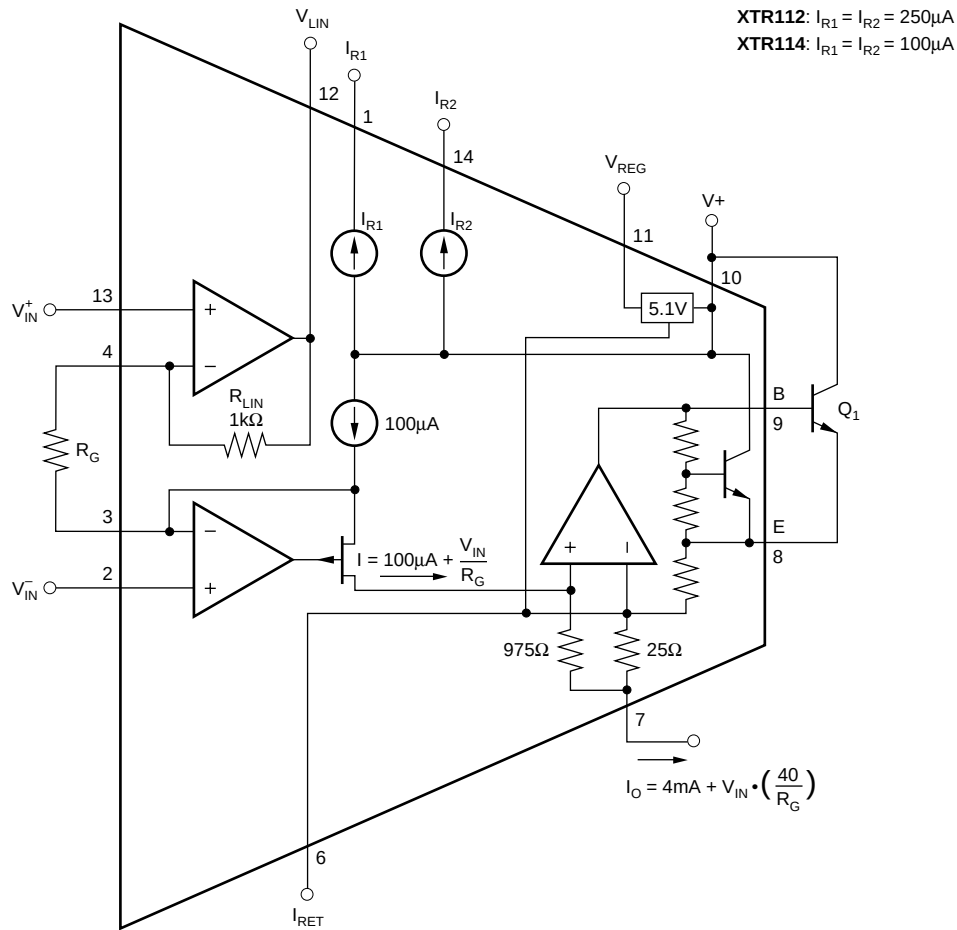
ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## PACKAGE/ORDERING INFORMATION

| PRODUCT  | CURRENT SOURCES | PACKAGE             | PACKAGE DRAWING NUMBER <sup>(1)</sup> | SPECIFIED TEMPERATURE RANGE | ORDERING NUMBER <sup>(2)</sup> | TRANSPORT MEDIA |
|----------|-----------------|---------------------|---------------------------------------|-----------------------------|--------------------------------|-----------------|
| XTR112U  | 2 x 250μA       | SO-14 Surface Mount | 235                                   | -40°C to +85°C              | XTR112U                        | Rails           |
| "        | "               | "                   | "                                     | "                           | XTR112U/2K5                    | Tape and Reel   |
| XTR112UA | 2 x 250μA       | SO-14 Surface Mount | 235                                   | -40°C to +85°C              | XTR112UA                       | Rails           |
| "        | "               | "                   | "                                     | "                           | XTR112UA/2K5                   | Tape and Reel   |
| XTR114U  | 2 x 100μA       | SO-14 Surface Mount | 235                                   | -40°C to +85°C              | XTR114U                        | Rails           |
| "        | "               | "                   | "                                     | "                           | XTR114U/2K5                    | Tape and Reel   |
| XTR114UA | 2 x 100μA       | SO-14 Surface Mount | 235                                   | -40°C to +85°C              | XTR114UA                       | Rails           |
| "        | "               | "                   | "                                     | "                           | XTR114UA/2K5                   | Tape and Reel   |

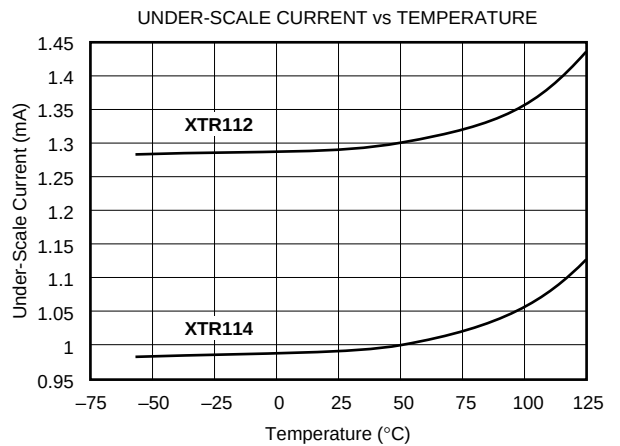
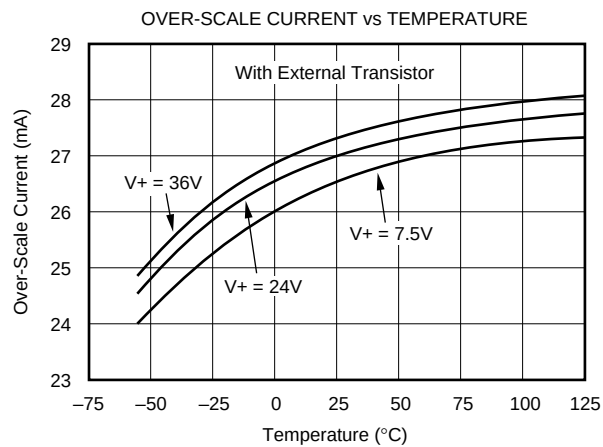
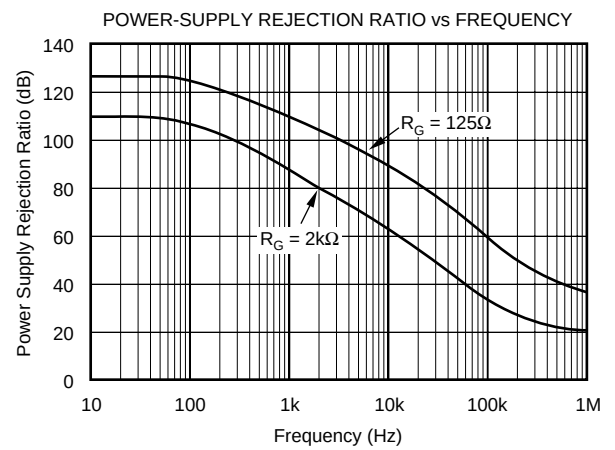
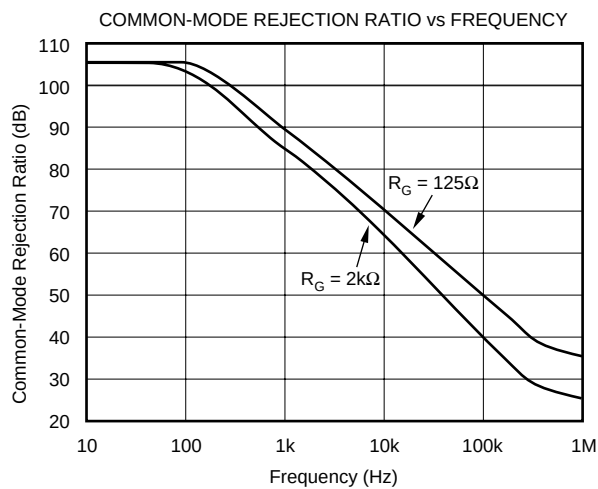
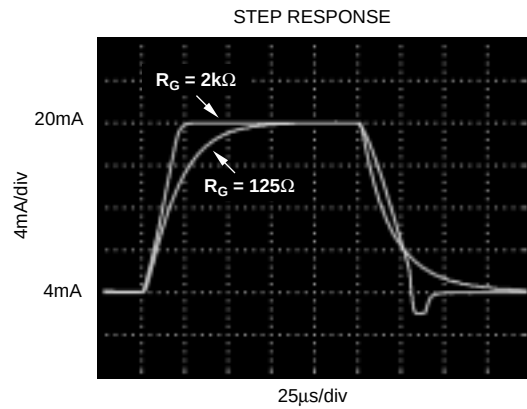
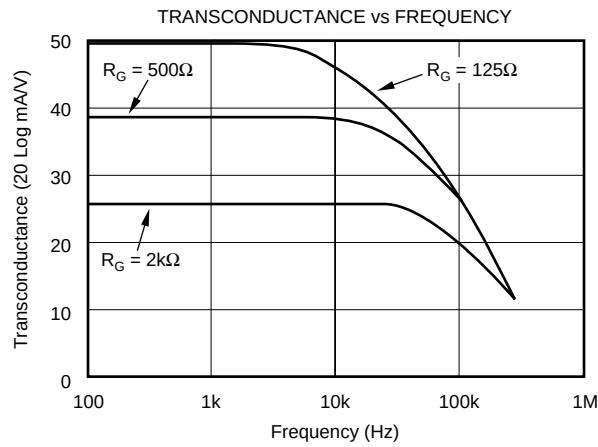
NOTES: (1) For detailed drawing and dimension table, please see end of data sheet, or Appendix C of Burr-Brown IC Data Book. (2) Models with a slash (/) are available only in Tape and Reel in the quantities indicated (e.g., /2K5 indicates 2500 devices per reel). Ordering 2500 pieces of "XTR112UA/2K5" will get a single 2500-piece Tape and Reel. For detailed Tape and Reel mechanical information, refer to Appendix B of Burr-Brown IC Data Book.

# FUNCTIONAL BLOCK DIAGRAM



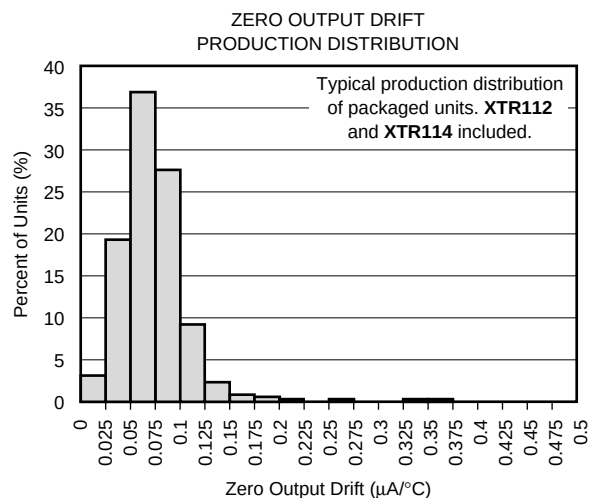
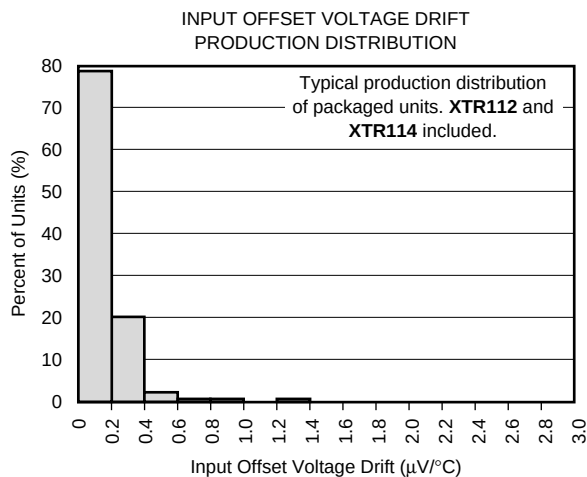
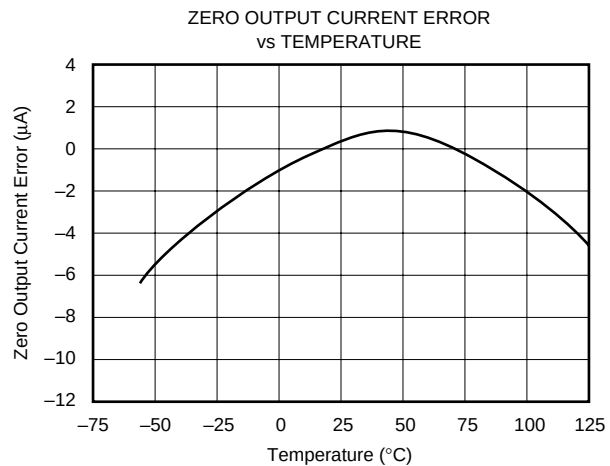
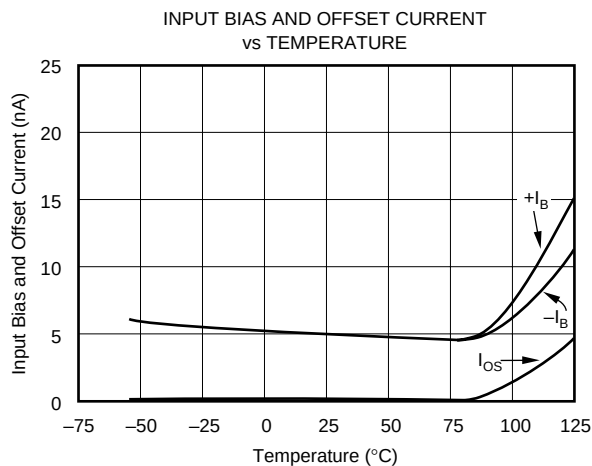
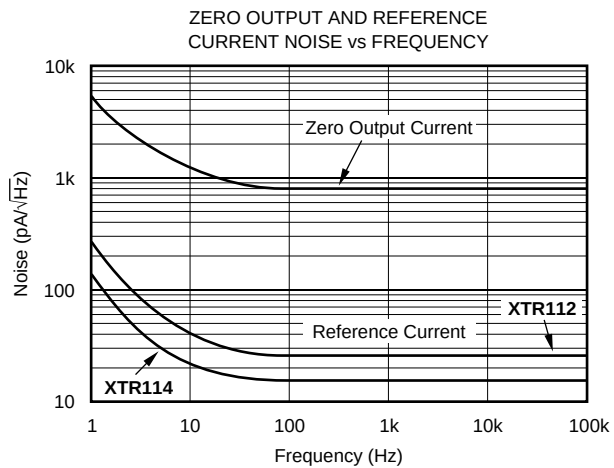
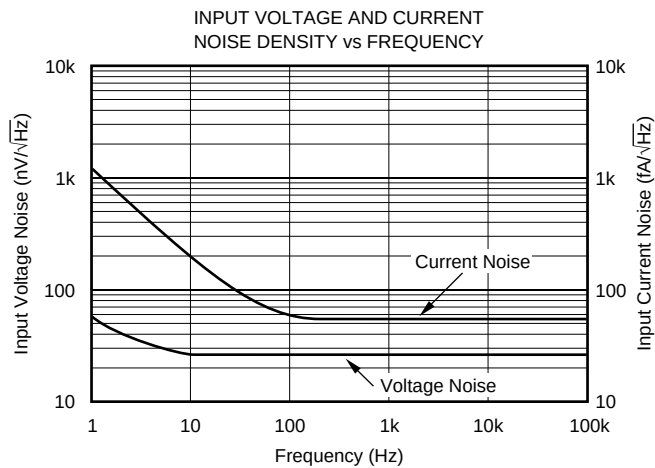
# TYPICAL PERFORMANCE CURVES

At  $T_A = +25^\circ\text{C}$ , and  $V_+ = 24\text{V}$ , unless otherwise noted.



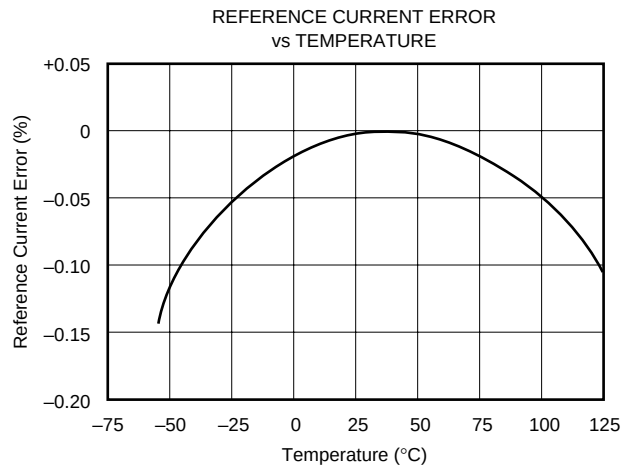
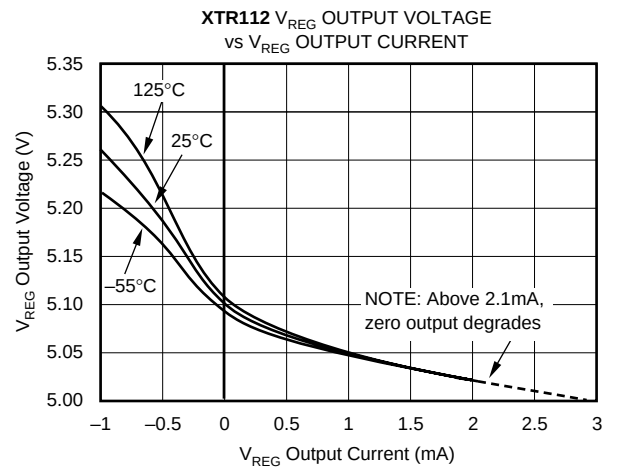
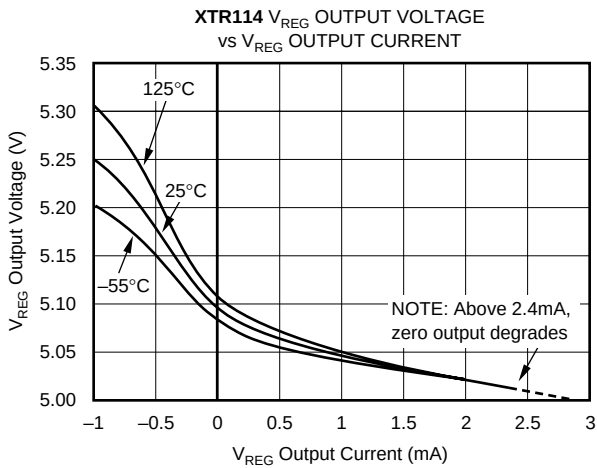
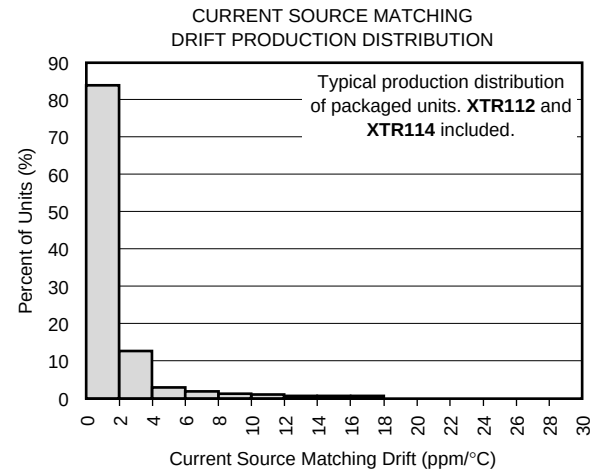
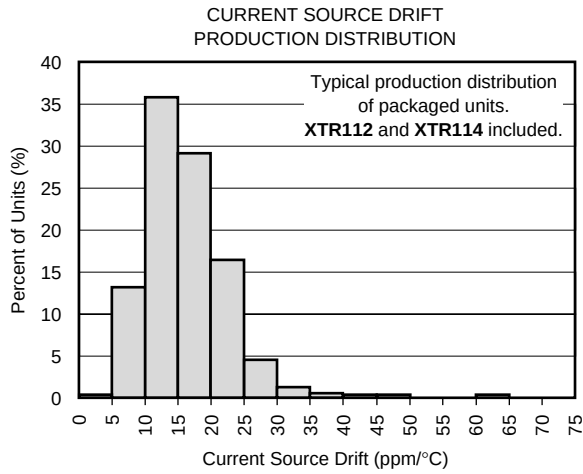
# TYPICAL PERFORMANCE CURVES (CONT)

At  $T_A = +25^\circ\text{C}$ , and  $V_+ = 24\text{V}$ , unless otherwise noted.



# TYPICAL PERFORMANCE CURVES (CONT)

At  $T_A = +25^\circ\text{C}$ , and  $V_+ = 24\text{V}$ , unless otherwise noted.



# APPLICATION INFORMATION

Figure 1 shows the basic connection diagram for the XTR112 and XTR114. The loop power supply,  $V_{PS}$ , provides power for all circuitry. Output loop current is measured as a voltage across the series load resistor,  $R_L$ .

Two matched current sources drive the RTD and zero-setting resistor,  $R_Z$ . These current sources are  $250\mu A$  for the XTR112 and  $100\mu A$  for the XTR114. Their instrumentation amplifier input measures the voltage difference between the RTD and  $R_Z$ . The value of  $R_Z$  is chosen to be equal to the resistance of the RTD at the low-scale (minimum) measurement temperature.  $R_Z$  can be adjusted to achieve 4mA output at the minimum measurement temperature to correct for input offset voltage and reference current mismatch of the XTR112 and XTR114.

$R_{CM}$  provides an additional voltage drop to bias the inputs of the XTR112 and XTR114 within their common-mode input range.  $R_{CM}$  should be bypassed with a  $0.01\mu F$  capacitor to minimize common-mode noise. Resistor  $R_G$  sets the gain of the instrumentation amplifier according to the desired temperature range.  $R_{LIN1}$  provides second-order linearization correction to the RTD, typically achieving a 40:1 improvement in linearity. An additional resistor is required for three-wire RTD connections, see Figure 3.

The transfer function through the complete instrumentation amplifier and voltage-to-current converter is:

$$I_O = 4mA + V_{IN} \cdot (40/R_G)$$

( $V_{IN}$  in volts,  $R_G$  in ohms)

where  $V_{IN}$  is the differential input voltage. As evident from the transfer function, if  $R_G$  is not used the gain is zero and the output is simply the XTR's zero current. The value of  $R_G$  varies slightly for two-wire RTD and three-wire RTD connections with linearization.  $R_G$  can be calculated from the equations given in Figure 1 (two-wire RTD connection) and Table I (three-wire RTD connection).

The  $I_{RET}$  pin is the return path for all current from the current sources and  $V_{REG}$ . The  $I_{RET}$  pin allows any current used in external circuitry to be sensed by the XTR112 and XTR114 and to be included in the output current without causing an error.

The  $V_{REG}$  pin provides an on-chip voltage source of approximately 5.1V and is suitable for powering external input circuitry (refer to Figure 6). It is a moderately accurate voltage reference—it is not the same reference used to set the precision current references.  $V_{REG}$  is capable of sourcing approximately 2.1mA of current for the XTR112 and 2.4mA for the XTR114. Exceeding these values may affect the 4mA zero output. Both products can sink approximately 1mA.

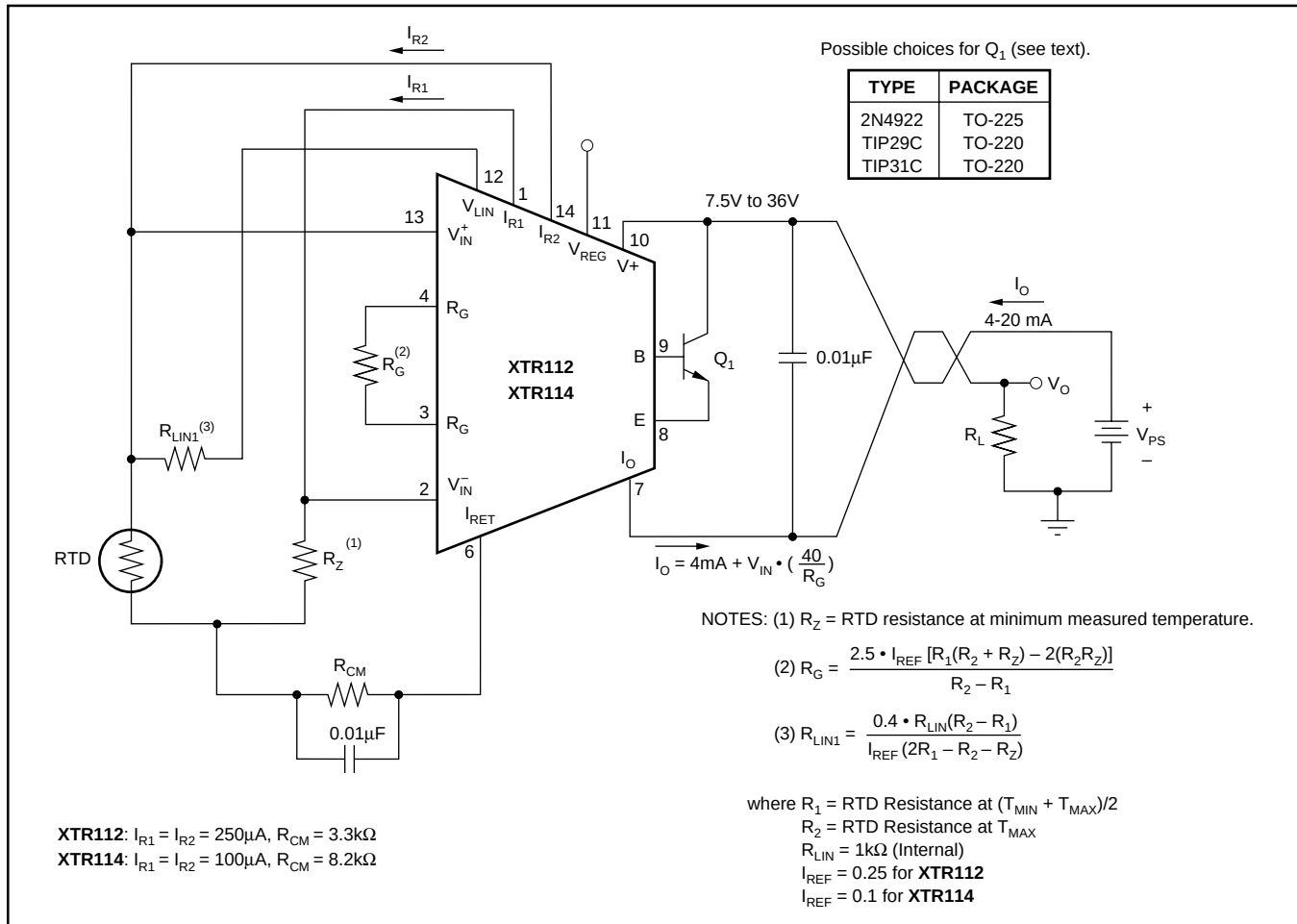


FIGURE 1. Basic Two-Wire RTD Temperature Measurement Circuit with Linearization.



A negative input voltage,  $V_{IN}$ , will cause the output current to be less than 4mA. Increasingly negative  $V_{IN}$  will cause the output current to limit at approximately 1.3mA for the XTR112 and 1mA for the XTR114. Refer to the typical curve “Under-Scale Current vs Temperature.”

Increasingly positive input voltage (greater than the full-scale input) will produce increasing output current according to the transfer function, up to the output current limit of approximately 27mA. Refer to the typical curve “Over-Scale Current vs Temperature.”

## EXTERNAL TRANSISTOR

Transistor  $Q_1$  conducts the majority of the signal-dependent 4-20mA loop current. Using an external transistor isolates the majority of the power dissipation from the precision input and reference circuitry of the XTR112 and XTR114, maintaining excellent accuracy.

Since the external transistor is inside a feedback loop its characteristics are not critical. Requirements are:  $V_{CEO} = 45V$  min,  $\beta = 40$  min and  $P_D = 800mW$ . Power dissipation requirements may be lower if the loop power supply voltage is less than 36V. Some possible choices for  $Q_1$  are listed in Figure 1.

The XTR112 and XTR114 can be operated without this external transistor, however, accuracy will be somewhat degraded due to the internal power dissipation. Operation without  $Q_1$  is not recommended for extended temperature ranges. A resistor ( $R = 3.3k\Omega$ ) connected between the  $I_{RET}$  pin and the E (emitter) pin may be needed for operation below  $0^\circ C$  without  $Q_1$  to guarantee the full 20mA full-scale output, especially with  $V+$  near 7.5V.

## LOOP POWER SUPPLY

The voltage applied to the XTR112 and XTR114,  $V+$ , is measured with respect to the  $I_O$  connection, pin 7.  $V+$  can

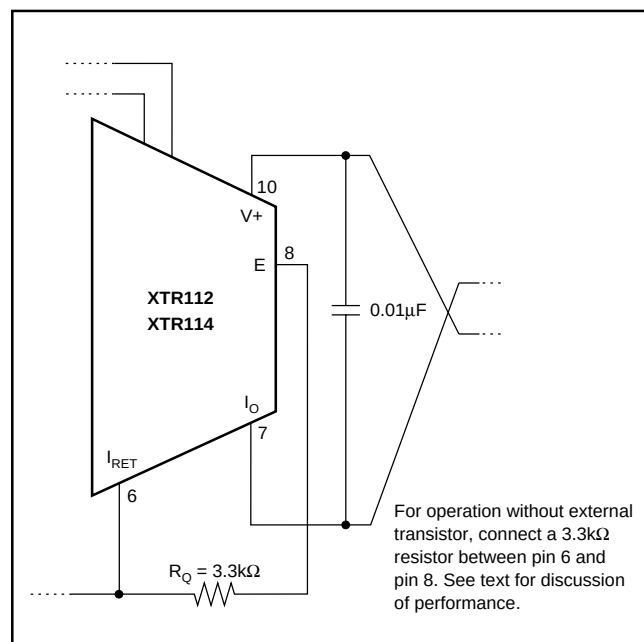


FIGURE 2. Operation Without External Transistor.

range from 7.5V to 36V. The loop supply voltage,  $V_{PS}$ , will differ from the applied voltage according to the voltage drop on the current sensing resistor,  $R_L$  (plus any other voltage drop in the line).

If a low loop supply voltage is used,  $R_L$  (including the loop wiring resistance) must be made a relatively low value to assure that  $V+$  remains 7.5V or greater for the maximum loop current of 20mA:

$$R_L \text{ max} = \left( \frac{(V+) - 7.5V}{20mA} \right) - R_{\text{WIRING}}$$

It is recommended to design for  $V+$  equal or greater than 7.5V with loop currents up to 30mA to allow for out-of-range input conditions.

The low operating voltage (7.5V) of the XTR112 and XTR114 allow operation directly from personal computer power supplies ( $12V \pm 5\%$ ). When used with the RCV420 Current Loop Receiver (Figure 7), load resistor voltage drop is limited to 3V.

## ADJUSTING INITIAL ERRORS

Many applications require adjustment of initial errors. Input offset and reference current mismatch errors can be corrected by adjustment of the zero resistor,  $R_Z$ . Adjusting the gain-setting resistor,  $R_G$ , corrects any errors associated with gain.

## TWO-WIRE AND THREE-WIRE RTD CONNECTIONS

In Figure 1, the RTD can be located remotely simply by extending the two connections to the RTD. With this remote two-wire connection to the RTD, line resistance will introduce error. This error can be partially corrected by adjusting the values of  $R_Z$ ,  $R_G$ , and  $R_{LIN1}$ .

A better method for remotely located RTDs is the three-wire RTD connection shown in Figure 3. This circuit offers improved accuracy.  $R_Z$ 's current is routed through a third wire to the RTD. Assuming line resistance is equal in RTD lines 1 and 2, this produces a small common-mode voltage which is rejected by the XTR112 and XTR114. A second resistor,  $R_{LIN2}$ , is required for linearization.

Note that although the two-wire and three-wire RTD connection circuits are very similar, the gain-setting resistor,  $R_G$ , has slightly different equations:

$$\text{Two-wire: } R_G = \frac{2.5 \cdot I_{REF} [R_1(R_2 + R_Z) - 2(R_2 R_Z)]}{R_2 - R_1}$$

$$\text{Three-wire: } R_G = \frac{2.5 \cdot I_{REF} (R_2 - R_Z)(R_1 - R_Z)}{R_2 - R_1}$$

where  $R_Z$  = RTD resistance at  $T_{MIN}$   
 $R_1$  = RTD resistance at  $(T_{MIN} + T_{MAX})/2$   
 $R_2$  = RTD resistance at  $T_{MAX}$   
 $I_{REF} = 0.25$  for XTR112  
 $I_{REF} = 0.1$  for XTR114

Table I summarizes the resistor equations for two-wire and three-wire RTD connections. An example calculation is also provided. To maintain good accuracy, at least 1% (or better) resistors should be used for  $R_G$ . Table II provides standard 1%  $R_G$  values for a three-wire Pt1000 RTD connection with linearization for the XTR112. Table III gives  $R_G$  values for the XTR114.

## LINEARIZATION

RTD temperature sensors are inherently (but predictably) nonlinear. With the addition of one or two external resistors,  $R_{LIN1}$  and  $R_{LIN2}$ , it is possible to compensate for most of this nonlinearity resulting in 40:1 improvement in linearity over the uncompensated output.

|                                               | TWO-WIRE                                                                  |                                                                            | THREE-WIRE                                                        |                                                                            |                                                                                   |
|-----------------------------------------------|---------------------------------------------------------------------------|----------------------------------------------------------------------------|-------------------------------------------------------------------|----------------------------------------------------------------------------|-----------------------------------------------------------------------------------|
|                                               | $R_G$                                                                     | $R_{LIN1}$                                                                 | $R_G$                                                             | $R_{LIN1}$                                                                 | $R_{LIN2}$                                                                        |
| General Equations                             | $= \frac{I_{REF} \cdot 2.5 [R_1 (R_2 + R_Z) - 2 (R_2 R_Z)]}{(R_2 - R_1)}$ | $= \frac{0.4 \cdot R_{LIN} (R_2 - R_1)}{I_{REF} \cdot (2R_1 - R_2 - R_Z)}$ | $= \frac{I_{REF} \cdot 2.5 (R_2 - R_Z) (R_1 - R_Z)}{(R_2 - R_1)}$ | $= \frac{0.4 \cdot R_{LIN} (R_2 - R_1)}{I_{REF} \cdot (2R_1 - R_2 - R_Z)}$ | $= \frac{0.4 \cdot (R_{LIN} + R_G)(R_2 - R_1)}{I_{REF} \cdot (2R_1 - R_2 - R_Z)}$ |
| XTR112 ( $I_{REF} = 0.25$ )<br>(see Table II) | $= \frac{0.625 \cdot [R_1 (R_2 + R_Z) - 2 (R_2 R_Z)]}{(R_2 - R_1)}$       | $= \frac{1.6 \cdot R_{LIN} (R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$               | $= \frac{0.625 \cdot (R_2 - R_Z) (R_1 - R_Z)}{(R_2 - R_1)}$       | $= \frac{1.6 \cdot R_{LIN} (R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$               | $= \frac{1.6 \cdot (R_{LIN} + R_G)(R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$               |
| XTR114 ( $I_{REF} = 0.1$ )<br>(see Table III) | $= \frac{0.25 \cdot [R_1 (R_2 + R_Z) - 2 (R_2 R_Z)]}{(R_2 - R_1)}$        | $= \frac{4 \cdot R_{LIN} (R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$                 | $= \frac{0.25 \cdot (R_2 - R_Z) (R_1 - R_Z)}{(R_2 - R_1)}$        | $= \frac{4 \cdot R_{LIN} (R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$                 | $= \frac{4 \cdot (R_{LIN} + R_G)(R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$                 |

where  $R_Z$  = RTD resistance at the minimum measured temperature,  $T_{MIN}$

$R_1$  = RTD resistance at the midpoint measured temperature,  $T_{MID} = (T_{MIN} + T_{MAX})/2$

$R_2$  = RTD resistance at maximum measured temperature,  $T_{MAX}$

$R_{LIN} = 1k\Omega$  (internal)

### XTR112 RESISTOR EXAMPLE:

The measurement range is  $-100^\circ\text{C}$  to  $+200^\circ\text{C}$  for a 3-wire Pt100 RTD connection. Determine the values for  $R_S$ ,  $R_G$ ,  $R_{LIN1}$ , and  $R_{LIN2}$ . Look up the values from the chart or calculate the values according to the equations provided.

#### METHOD 1: TABLE LOOK UP

$T_{MIN} = -100^\circ\text{C}$  and  $\Delta T = 300^\circ\text{C}$  ( $T_{MAX} = +200^\circ\text{C}$ ),

Using Table II the 1% values are:

$$R_Z = 604\Omega \quad R_{LIN1} = 33.2k\Omega$$

$$R_G = 750\Omega \quad R_{LIN2} = 59k\Omega$$

#### METHOD 2: CALCULATION

**Step 1:** Determine  $R_Z$ ,  $R_1$ , and  $R_2$ .

$R_Z$  is the RTD resistance at the minimum measured temperature,  $T_{MIN} = -100^\circ\text{C}$ . Using Equation (1) at right gives  $R_Z = 602.5\Omega$  (1% value is  $604\Omega$ ).

$R_2$  is the RTD resistance at the maximum measured temperature,  $T_{MAX} = 200^\circ\text{C}$ . Using Equation (2) at right gives  $R_2 = 1758.4\Omega$ .

$R_1$  is the RTD resistance at the midpoint measured temperature,  $T_{MID} = (T_{MIN} + T_{MAX})/2 = (-100 + 200)/2 = 50^\circ\text{C}$ .  $R_1$  is NOT the average of  $R_Z$  and  $R_2$ . Using Equation (2) at right gives  $R_1 = 1194\Omega$ .

**Step 2:** Calculate  $R_G$ ,  $R_{LIN1}$ , and  $R_{LIN2}$  using equations above.

$$R_G = 757\Omega \text{ (1% value is } 750\Omega\text{)}$$

$$R_{LIN1} = 33.322k\Omega \text{ (1% value is } 33.2k\Omega\text{)}$$

$$R_{LIN2} = 58.548k\Omega \text{ (1% value is } 59k\Omega\text{)}$$

#### Calculation of Pt1000 Resistance Values

(according to DIN IEC 751)

Equation (1) Temperature range from  $-200^\circ\text{C}$  to  $0^\circ\text{C}$ :

$$R_{(T)} = 1000 [1 + 3.90802 \cdot 10^{-3} \cdot T - 0.5802 \cdot 10^{-6} \cdot T^2 - 4.27350 \cdot 10^{-12} \cdot (T - 100) \cdot T^3]$$

Equation (2) Temperature range from  $0^\circ\text{C}$  to  $+850^\circ\text{C}$ :

$$R_{(T)} = 1000 (1 + 3.90802 \cdot 10^{-3} \cdot T - 0.5802 \cdot 10^{-6} \cdot T^2)$$

where:  $R_{(T)}$  is the resistance in  $\Omega$  at temperature  $T$ .

$T$  is the temperature in  $^\circ\text{C}$ .

NOTE: Most RTD manufacturers provide reference tables for resistance values at various temperatures.

**Resistor values for other RTD types (such as Pt2000) can be calculated using the XTR resistor selection program in the Applications Section on Burr-Brown's web site ([www.burr-brown.com](http://www.burr-brown.com))**

TABLE I. Summary of Resistor Equations for Two-Wire and Three-Wire Pt1000 RTD Connections.

# XTR112 1% RESISTOR VALUES FOR A THREE-WIRE RTD CONNECTION

MEASUREMENT TEMPERATURE SPAN  $\Delta T$  (°C)

| T <sub>MIN</sub> | 100°C                        | 200°C                      | 300°C                      | 400°C                      | 500°C                       | 600°C                       | 700°C                       | 800°C                       | 900°C                      | 1000°C                    |
|------------------|------------------------------|----------------------------|----------------------------|----------------------------|-----------------------------|-----------------------------|-----------------------------|-----------------------------|----------------------------|---------------------------|
| -200°C           | 187/267<br>48700<br>61900    | 187/536<br>31600<br>48700  | 187/806<br>25500<br>46400  | 187/1050<br>21500<br>44200 | 187/1330<br>17800<br>41200  | 187/1580<br>15000<br>39200  | 187/1820<br>13000<br>36500  | 187/2100<br>11300<br>34800  | 187/2370<br>9760<br>33200  | 187/2670<br>8660<br>31600 |
| -100°C           | 604/255<br>86600<br>110000   | 604/499<br>49900<br>75000  | 604/4750<br>33200<br>59000 | 604/1000<br>24900<br>49900 | 604/1270<br>19600<br>44200  | 604/1500<br>15800<br>40200  | 604/1780<br>13300<br>37400  | 604/2050<br>11500<br>34800  | 604/2260<br>10000<br>32400 |                           |
| 0°C              | 1000/243<br>105000<br>130000 | 1000/487<br>51100<br>76800 | 1000/732<br>33200<br>57600 | 1000/976<br>24300<br>48700 | 1000/1210<br>19100<br>42200 | 1000/1470<br>15400<br>38300 | 1000/1740<br>13000<br>35700 | 1000/1960<br>11000<br>33200 |                            |                           |
| 100°C            | 1370/237<br>102000<br>127000 | 1370/475<br>49900<br>73200 | 1370/715<br>32400<br>56200 | 1370/953<br>23700<br>46400 | 1370/1180<br>18700<br>40200 | 1370/1430<br>15000<br>36500 | 1370/1690<br>12400<br>33200 |                             |                            |                           |
| 200°C            | 1740/232<br>100000<br>121000 | 1740/464<br>48700<br>69800 | 1740/698<br>31600<br>53600 | 1740/931<br>23200<br>44200 | 1740/1150<br>17800<br>38300 | 1740/1400<br>14300<br>34800 |                             |                             |                            |                           |
| 300°C            | 2100/221<br>95300<br>118000  | 2100/442<br>46400<br>68100 | 2100/665<br>30100<br>51100 | 2100/887<br>22100<br>42200 | 2100/1130<br>17400<br>36500 |                             |                             |                             |                            |                           |
| 400°C            | 2490/215<br>93100<br>113000  | 2490/432<br>45300<br>64900 | 2490/649<br>29400<br>48700 | 2490/866<br>21500<br>40200 |                             |                             |                             |                             |                            |                           |
| 500°C            | 2800/210<br>887000<br>107000 | 2800/412<br>43200<br>61900 | 2800/619<br>43200<br>45300 |                            |                             |                             |                             |                             |                            |                           |
| 600°C            | 3160/200<br>86600<br>102000  | 3160/402<br>42200<br>59000 |                            |                            |                             |                             |                             |                             |                            |                           |
| 700°C            | 3480/191<br>82500<br>100000  |                            |                            |                            |                             |                             |                             |                             |                            |                           |
| 800°C            | 3740/187<br>80600<br>95300   |                            |                            |                            |                             |                             |                             |                             |                            |                           |

$\left[ \begin{array}{c} R_Z/R_G \\ R_{LIN1} \\ R_{LIN2} \end{array} \right]$

NOTE: The values listed in the table are 1% resistors (in  $\Omega$ ).  
Exact values may be calculated from the following equations:

$R_Z$  = RTD resistance at minimum measured temperature,  $T_{MIN}$ .

$$R_G = \frac{0.625 \cdot (R_2 - R_Z)(R_1 - R_Z)}{(R_2 - R_1)}$$

$$R_{LIN1} = \frac{1.6 \cdot R_{LIN}(R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$$

$$R_{LIN2} = \frac{1.6 \cdot (R_{LIN} + R_G)(R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$$

where  $R_1$  = RTD resistance at the midpoint measured temperature,  $(T_{MIN} + T_{MAX})/2$

$R_2$  = RTD resistance at  $T_{MAX}$

$R_{LIN}$  = 1k $\Omega$  (Internal)

TABLE II. XTR112  $R_Z$ ,  $R_G$ ,  $R_{LIN1}$ , and  $R_{LIN2}$  Standard 1% Resistor Values for Three-Wire Pt1000 RTD Connection with Linearization.

# XTR114 1% RESISTOR VALUES FOR A THREE-WIRE RTD CONNECTION

MEASUREMENT TEMPERATURE SPAN  $\Delta T$  (°C)

| T <sub>MIN</sub> | 100°C                         | 200°C                        | 300°C                       | 400°C                      | 500°C                      | 600°C                      | 700°C                      | 800°C                      | 900°C                     | 1000°C                     |
|------------------|-------------------------------|------------------------------|-----------------------------|----------------------------|----------------------------|----------------------------|----------------------------|----------------------------|---------------------------|----------------------------|
| -200°C           | 187/107<br>121000<br>133000   | 187/215<br>78700<br>95300    | 187/316<br>64900<br>84500   | 187/422<br>53600<br>76800  | 187/523<br>45300<br>68100  | 187/634<br>38300<br>68100  | 187/732<br>32400<br>56200  | 187/845<br>28000<br>52300  | 187/953<br>24900<br>47500 | 187/1050<br>21500<br>45300 |
| -100°C           | 604/102<br>221000<br>243000   | 604/200<br>124000<br>150000  | 604/301<br>84500<br>110000  | 604/402<br>61900<br>86600  | 604/511<br>48700<br>73200  | 604/604<br>40200<br>63400  | 604/715<br>33200<br>57600  | 604/806<br>28700<br>52300  | 604/909<br>24900<br>47500 |                            |
| 0°C              | 1000/97.6<br>261000<br>287000 | 1000/196<br>130000<br>154000 | 1000/294<br>84500<br>107000 | 1000/392<br>61900<br>84500 | 1000/487<br>47500<br>71500 | 1000/590<br>39200<br>61900 | 1000/681<br>32400<br>54900 | 1000/787<br>27400<br>49900 |                           |                            |
| 100°C            | 1370/95.3<br>255000<br>280000 | 1370/191<br>124000<br>147000 | 1370/287<br>80600<br>105000 | 1370/383<br>59000<br>82500 | 1370/475<br>46400<br>68100 | 1370/576<br>37400<br>59000 | 1370/665<br>31600<br>52300 |                            |                           |                            |
| 200°C            | 1740/90.9<br>249000<br>267000 | 1740/182<br>121000<br>143000 | 1740/274<br>78700<br>100000 | 1740/365<br>57600<br>78700 | 1740/464<br>44200<br>64900 | 1740/549<br>36500<br>56200 |                            |                            |                           |                            |
| 300°C            | 2100/88.9<br>237000<br>261000 | 2100/178<br>118000<br>137000 | 2100/267<br>75000<br>95300  | 2100/357<br>54900<br>75000 | 2100/348<br>43200<br>61900 |                            |                            |                            |                           |                            |
| 400°C            | 2490/86.6<br>232000<br>249000 | 2490/174<br>113000<br>133000 | 2490/261<br>73200<br>93100  | 2490/249<br>53600<br>71500 |                            |                            |                            |                            |                           |                            |
| 500°C            | 2800/82.5<br>221000<br>243000 | 2800/165<br>110000<br>127000 | 2800/49<br>69800<br>88700   |                            |                            |                            |                            |                            |                           |                            |
| 600°C            | 3160/80.6<br>215000<br>215000 | 3160/162<br>105000<br>121000 |                             |                            |                            |                            |                            |                            |                           |                            |
| 700°C            | 3480/76.8<br>205000<br>221000 |                              |                             |                            |                            |                            |                            |                            |                           |                            |
| 800°C            | 3740/75<br>200000<br>215000   |                              |                             |                            |                            |                            |                            |                            |                           |                            |

$\left[ \begin{array}{c} R_Z/R_G \\ R_{LIN1} \\ R_{LIN2} \end{array} \right]$

NOTE: The values listed in the table are 1% resistors (in  $\Omega$ ).  
Exact values may be calculated from the following equations:

$R_Z$  = RTD resistance at minimum measured temperature,  $T_{MIN}$ .

$$R_G = \frac{0.25 \cdot (R_2 - R_Z)(R_1 - R_Z)}{(R_2 - R_1)}$$

$$R_{LIN1} = \frac{4 \cdot R_{LIN}(R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$$

$$R_{LIN2} = \frac{4 \cdot (R_{LIN} + R_G)(R_2 - R_1)}{(2R_1 - R_2 - R_Z)}$$

where  $R_1$  = RTD resistance at the midpoint measured temperature,  $(T_{MIN} + T_{MAX})/2$

$R_2$  = RTD resistance at  $T_{MAX}$

$R_{LIN}$  = 1k $\Omega$  (Internal)

TABLE III. XTR114  $R_Z$ ,  $R_G$ ,  $R_{LIN1}$ , and  $R_{LIN2}$  Standard 1% Resistor Values for Three-Wire Pt1000 RTD Connection with Linearization.

A typical two-wire RTD application with linearization is shown in Figure 1. Resistor  $R_{LIN1}$  provides positive feedback and controls linearity correction.  $R_{LIN1}$  is chosen according to the desired temperature range. An equation is given in Figure 1.

In three-wire RTD connections, an additional resistor,  $R_{LIN2}$ , is required. As with the two-wire RTD application,  $R_{LIN1}$  provides positive feedback for linearization.  $R_{LIN2}$  provides an offset canceling current to compensate for wiring resistance encountered in remotely located RTDs.  $R_{LIN1}$  and  $R_{LIN2}$  are chosen such that their currents are equal. This makes the voltage drop in the wiring resistance to the RTD a common-mode signal which is rejected by the XTR112 and XTR114. The nearest standard 1% resistor values for  $R_{LIN1}$  and  $R_{LIN2}$  should be adequate for most applications. Tables II and III provide the 1% resistor values for a three-wire Pt1000 RTD connection.

If no linearity correction is desired, the  $V_{LIN}$  pin should be left open. With no linearization,  $R_G = 2500 \cdot V_{FS}$ , where  $V_{FS}$  = full-scale input range.

## RTDs

The text and figures thus far have assumed a Pt1000 RTD. With higher resistance RTDs, the temperature range and input voltage variation should be evaluated to ensure proper common-mode biasing of the inputs. As mentioned earlier,

$R_{CM}$  can be adjusted to provide an additional voltage drop to bias the inputs of the XTR112 and XTR114 within their common-mode input range.

## ERROR ANALYSIS

Table IV shows how to calculate the effect various error sources have on circuit accuracy. A sample error calculation for a typical RTD measurement circuit (Pt1000 RTD, 200°C measurement span) is provided. The results reveal the XTR112's and XTR114's excellent accuracy, in this case 1% unadjusted for the XTR112, 1.16% for the XTR114. Adjusting resistors  $R_G$  and  $R_Z$  for gain and offset errors improves the XTR112's accuracy to 0.28% (0.31% for the XTR114). Note that these are worst-case errors; guaranteed maximum values were used in the calculations and all errors were assumed to be positive (additive). The XTR112 and XTR114 achieve performance which is difficult to obtain with discrete circuitry and requires less space.

## OPEN-CIRCUIT PROTECTION

The optional transistor  $Q_2$  in Figure 3 provides predictable behavior with open-circuit RTD connections. It assures that if any one of the three RTD connections is broken, the XTR's output current will go to either its high current limit ( $\approx 27\text{mA}$ ) or low current limit ( $\approx 1.3\text{mA}$  for XTR112 and  $\approx 1\text{mA}$  for XTR114). This is easily detected as an out-of-range condition.

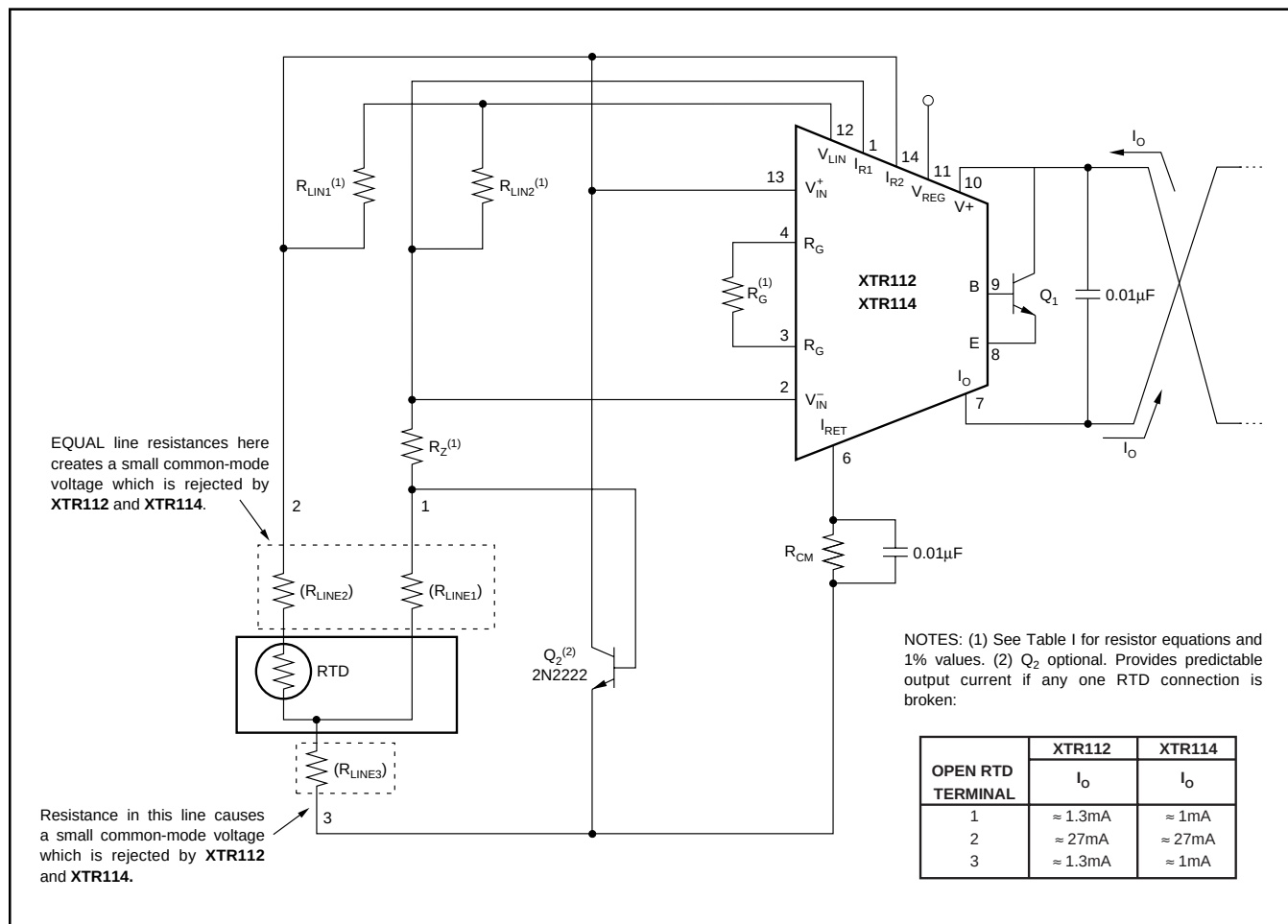


FIGURE 3. Three-Wire Connection for Remotely Located RTDs.

### SAMPLE ERROR CALCULATION FOR XTR112<sup>(1)</sup>

|                                                   |               |
|---------------------------------------------------|---------------|
| RTD value at 4mA Output ( $R_{RTD \text{ MIN}}$ ) | 1000 $\Omega$ |
| RTD Measurement Range                             | 200°C         |
| Ambient Temperature Range ( $\Delta T_A$ )        | 20°C          |
| Supply Voltage Change ( $\Delta V+$ )             | 5V            |
| Common-Mode Voltage Change ( $\Delta CM$ )        | 0.1V          |

| ERROR SOURCE                                        | ERROR EQUATION                                                                                                 | SAMPLE<br>ERROR CALCULATION <sup>(2)</sup>                                                                              | ERROR<br>(ppm of Full Scale) |                |
|-----------------------------------------------------|----------------------------------------------------------------------------------------------------------------|-------------------------------------------------------------------------------------------------------------------------|------------------------------|----------------|
|                                                     |                                                                                                                |                                                                                                                         | UNADJ.                       | ADJUST.        |
| <b>INPUT</b>                                        |                                                                                                                |                                                                                                                         |                              |                |
| Input Offset Voltage                                | $V_{OS}/(V_{IN \text{ MAX}}) \cdot 10^6$                                                                       | $100\mu V/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C) \cdot 10^6$                                             | 526                          | 0              |
| vs Common-Mode                                      | $CMRR \cdot \Delta CM/(V_{IN \text{ MAX}}) \cdot 10^6$                                                         | $50\mu V/V \cdot 0.1V/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C) \cdot 10^6$                                 | 26                           | 26             |
| Input Bias Current                                  | $I_B/I_{REF} \cdot 10^6$                                                                                       | $0.025\mu A/250\mu A \cdot 10^6$                                                                                        | 100                          | 0              |
| Input Offset Current                                | $I_{OS} \cdot R_{RTD \text{ MIN}}/(V_{IN \text{ MAX}}) \cdot 10^6$                                             | $3nA \cdot 1000\Omega/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C) \cdot 10^6$                                 | 16                           | 0              |
|                                                     |                                                                                                                | <b>Total Input Error:</b>                                                                                               | <b>668</b>                   | <b>26</b>      |
| <b>EXCITATION</b>                                   |                                                                                                                |                                                                                                                         |                              |                |
| Current Reference Accuracy                          | $I_{REF} \text{ Accuracy } (\%)/100\% \cdot 10^6$                                                              | $0.2\%/100\% \cdot 10^6$                                                                                                | 2000                         | 0              |
| vs Supply                                           | $(I_{REF} \text{ vs } V+) \cdot \Delta V+$                                                                     | $25ppm/V \cdot 5V$                                                                                                      | 125                          | 125            |
| Current Reference Matching                          | $I_{REF} \text{ Matching } (\%)/100\% \cdot I_{REF} \cdot R_{RTD \text{ MIN}}/(V_{IN \text{ MAX}}) \cdot 10^6$ | $0.1\%/100\% \cdot 250\mu A \cdot 1000\Omega/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C) \cdot 10^6$          | 1316                         | 0              |
| vs Supply                                           | $(I_{REF} \text{ matching vs } V+) \cdot \Delta V+ \cdot R_{RTD \text{ MIN}}/(V_{IN \text{ MAX}})$             | $10ppm/V \cdot 5V \cdot 250\mu A \cdot 1000\Omega/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C)$                | 66                           | 66             |
|                                                     |                                                                                                                | <b>Total Excitation Error:</b>                                                                                          | <b>3507</b>                  | <b>191</b>     |
| <b>GAIN</b>                                         |                                                                                                                |                                                                                                                         |                              |                |
| Span                                                | $\text{Span Error } (\%)/100\% \cdot 10^6$                                                                     | $0.2\%/100\% \cdot 10^6$                                                                                                | 2000                         | 0              |
| Nonlinearity                                        | $\text{Nonlinearity } (\%)/100\% \cdot 10^6$                                                                   | $0.01\%/100\% \cdot 10^6$                                                                                               | 100                          | 100            |
|                                                     |                                                                                                                | <b>Total Gain Error:</b>                                                                                                | <b>2100</b>                  | <b>100</b>     |
| <b>OUTPUT</b>                                       |                                                                                                                |                                                                                                                         |                              |                |
| Zero Output                                         | $(I_{ZERO} - 4mA)/16000\mu A \cdot 10^6$                                                                       | $25\mu A/16000\mu A \cdot 10^6$                                                                                         | 1563                         | 0              |
| vs Supply                                           | $(I_{ZERO} \text{ vs } V+) \cdot \Delta V+/16000\mu A \cdot 10^6$                                              | $0.2\mu A/V \cdot 5V/16000\mu A \cdot 10^6$                                                                             | 63                           | 63             |
|                                                     |                                                                                                                | <b>Total Output Error:</b>                                                                                              | <b>1626</b>                  | <b>63</b>      |
| <b>DRIFT (<math>\Delta T_A = 20^\circ C</math>)</b> |                                                                                                                |                                                                                                                         |                              |                |
| Input Offset Voltage                                | $\text{Drift} \cdot \Delta T_A/(V_{IN \text{ MAX}}) \cdot 10^6$                                                | $1.5\mu V/^\circ C \cdot 20^\circ C/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C) \cdot 10^6$                   | 158                          | 158            |
| Input Bias Current (typical)                        | $\text{Drift} \cdot \Delta T_A/I_{REF} \cdot 10^6$                                                             | $20pA/^\circ C \cdot 20^\circ C/250\mu A \cdot 10^6$                                                                    | 2                            | 2              |
| Input Offset Current (typical)                      | $\text{Drift} \cdot \Delta T_A \cdot R_{RTD \text{ MIN}}/(V_{IN \text{ MAX}}) \cdot 10^6$                      | $5pA/^\circ C \cdot 20^\circ C \cdot 1000\Omega/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C) \cdot 10^6$       | 0.5                          | 0.5            |
| Current Reference Accuracy                          | $\text{Drift} \cdot \Delta T_A$                                                                                | $35ppm/^\circ C \cdot 20^\circ C$                                                                                       | 700                          | 700            |
| Current Reference Matching                          | $\text{Drift} \cdot \Delta T_A \cdot I_{REF} \cdot R_{RTD \text{ MIN}}/(V_{IN \text{ MAX}})$                   | $15ppm/^\circ C \cdot 20^\circ C \cdot 250\mu A \cdot 1000\Omega/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C)$ | 395                          | 395            |
| Span                                                | $\text{Drift} \cdot \Delta T_A$                                                                                | $25ppm/^\circ C \cdot 20^\circ C$                                                                                       | 500                          | 500            |
| Zero Output                                         | $\text{Drift} \cdot \Delta T_A/16000\mu A \cdot 10^6$                                                          | $0.5\mu A/^\circ C \cdot 20^\circ C/16000\mu A \cdot 10^6$                                                              | 626                          | 626            |
|                                                     |                                                                                                                | <b>Total Drift Error:</b>                                                                                               | <b>2382</b>                  | <b>2382</b>    |
| <b>NOISE (0.1Hz to 10Hz, typ)</b>                   |                                                                                                                |                                                                                                                         |                              |                |
| Input Offset Voltage                                | $V_n/(V_{IN \text{ MAX}}) \cdot 10^6$                                                                          | $0.6\mu V/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C) \cdot 10^6$                                             | 3                            | 3              |
| Current Reference                                   | $I_{REF} \text{ Noise} \cdot R_{RTD \text{ MIN}}/(V_{IN \text{ MAX}}) \cdot 10^6$                              | $3nA \cdot 1000\Omega/(250\mu A \cdot 3.8\Omega/^\circ C \cdot 200^\circ C) \cdot 10^6$                                 | 16                           | 16             |
| Zero Output                                         | $I_{ZERO} \text{ Noise}/16000\mu A \cdot 10^6$                                                                 | $0.03\mu A/16000\mu A \cdot 10^6$                                                                                       | 2                            | 2              |
|                                                     |                                                                                                                | <b>Total Noise Error:</b>                                                                                               | <b>21</b>                    | <b>21</b>      |
| <b>TOTAL ERROR:</b>                                 |                                                                                                                |                                                                                                                         | <b>10304</b>                 | <b>2783</b>    |
|                                                     |                                                                                                                |                                                                                                                         | <b>(1.03%)</b>               | <b>(0.28%)</b> |

NOTES: (1) For XTR114,  $I_{REF} = 100\mu A$ . Total unadjusted error is 1.16%, adjusted error 0.31%. (2) All errors are min/max and referred to input, unless otherwise stated.

TABLE IV. Error Calculation.

### REVERSE-VOLTAGE PROTECTION

The XTR112's and XTR114's low compliance rating (7.5V) permits the use of various voltage protection methods without compromising operating range. Figure 4 shows a diode bridge circuit which allows normal operation even when the voltage connection lines are reversed. The bridge causes a two diode drop (approximately 1.4V) loss in loop supply voltage. This results in a compliance voltage of approximately 9V—satisfactory for most applications. If 1.4V drop in loop supply is too much, a diode can be inserted in series with the loop supply voltage and the V+ pin. This protects against reverse output connection lines with only a 0.7V loss in loop supply voltage.

### SURGE PROTECTION

Remote connections to current transmitters can sometimes be subjected to voltage surges. It is prudent to limit the maximum surge voltage applied to the XTR to as low as practical. Various zener diode and surge clamping diodes are specially designed for this purpose. Select a clamp diode with as low a voltage rating as possible for best protection. For example, a 36V protection diode will assure proper transmitter operation at normal loop voltages, yet will provide an appropriate level of protection against voltage surges. Characterization tests on three production lots showed no damage to the XTR112 or XTR114 within loop supply voltages up to 65V.

Most surge protection zener diodes have a diode characteristic in the forward direction that will conduct excessive current, possibly damaging receiving-side circuitry if the loop connections are reversed. If a surge protection diode is used, a series diode or diode bridge should be used for protection against reversed connections.

## RADIO FREQUENCY INTERFERENCE

The long wire lengths of current loops invite radio frequency interference. RF can be rectified by the sensitive input circuitry of the XTR112 and XTR114 causing errors. This generally appears as an unstable output current that varies with the position of loop supply or input wiring.

If the RTD sensor is remotely located, the interference may enter at the input terminals. For integrated transmitter assemblies with short connection to the sensor, the interference more likely comes from the current loop connections.

Bypass capacitors on the input reduce or eliminate this input interference. Connect these bypass capacitors to the  $I_{RET}$  terminal as shown in Figure 5. Although the dc voltage at the  $I_{RET}$  terminal is not equal to 0V (at the loop supply,  $V_{PS}$ ) this circuit point can be considered the transmitter's "ground." The  $0.01\mu\text{F}$  capacitor connected between  $V+$  and  $I_O$  may help minimize output interference.

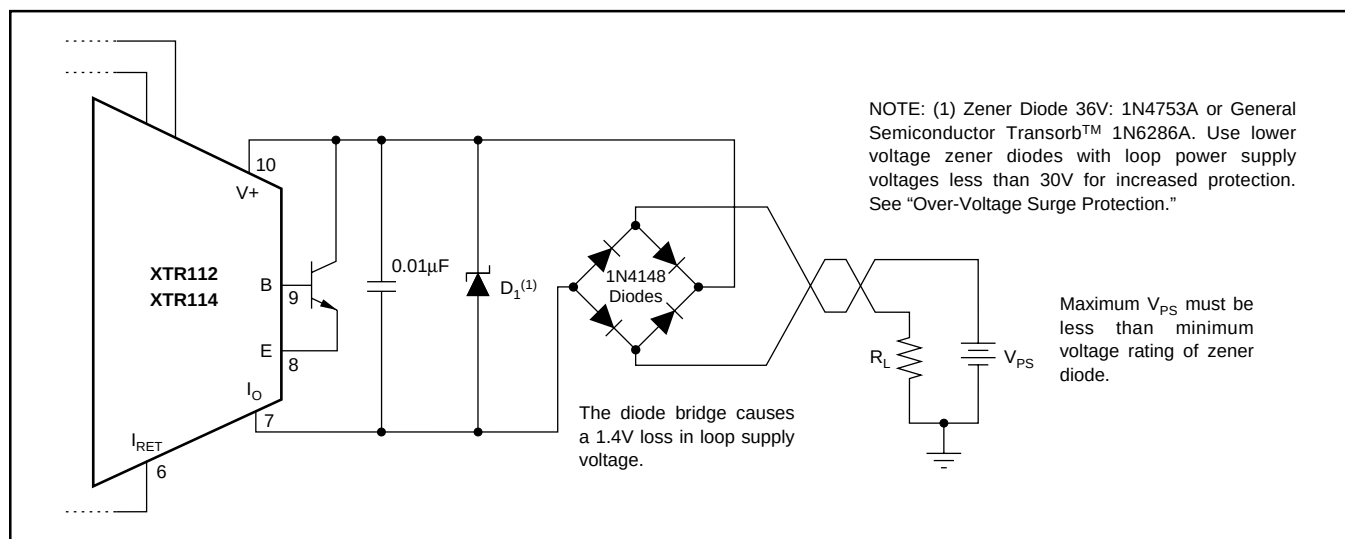


FIGURE 4. Reverse Voltage Operation and Over-Voltage Surge Protection.

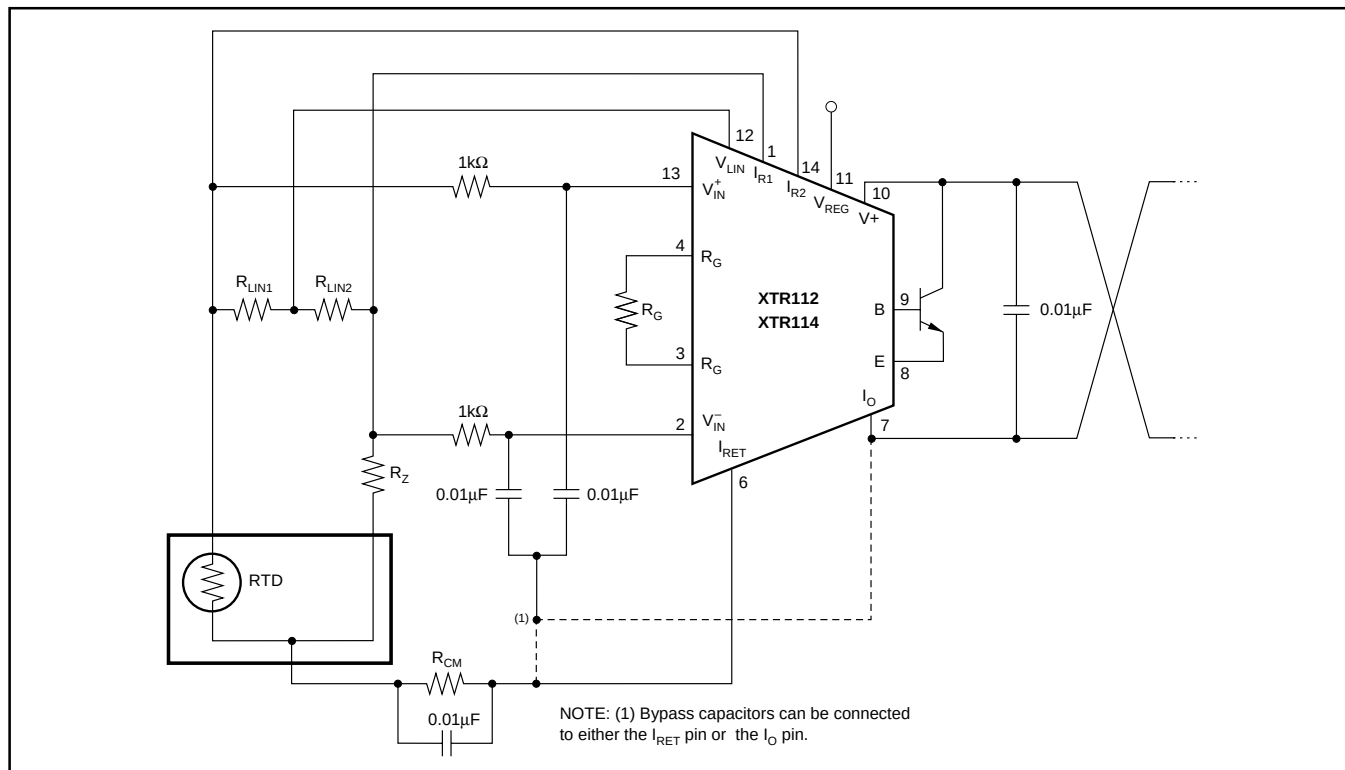


FIGURE 5. Input Bypassing Technique with Linearization.

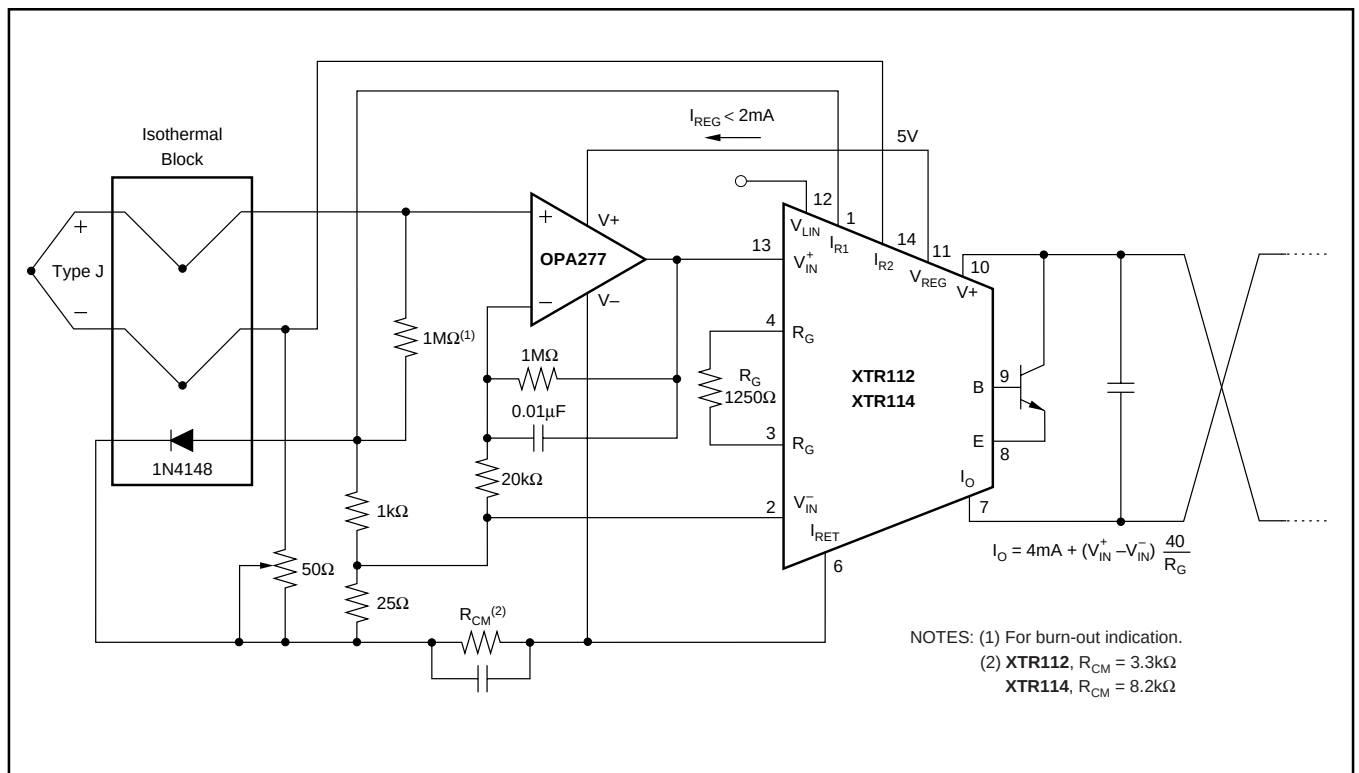


FIGURE 6. Thermocouple Low Offset, Low Drift Loop Measurement with Diode Cold-Junction Compensation.

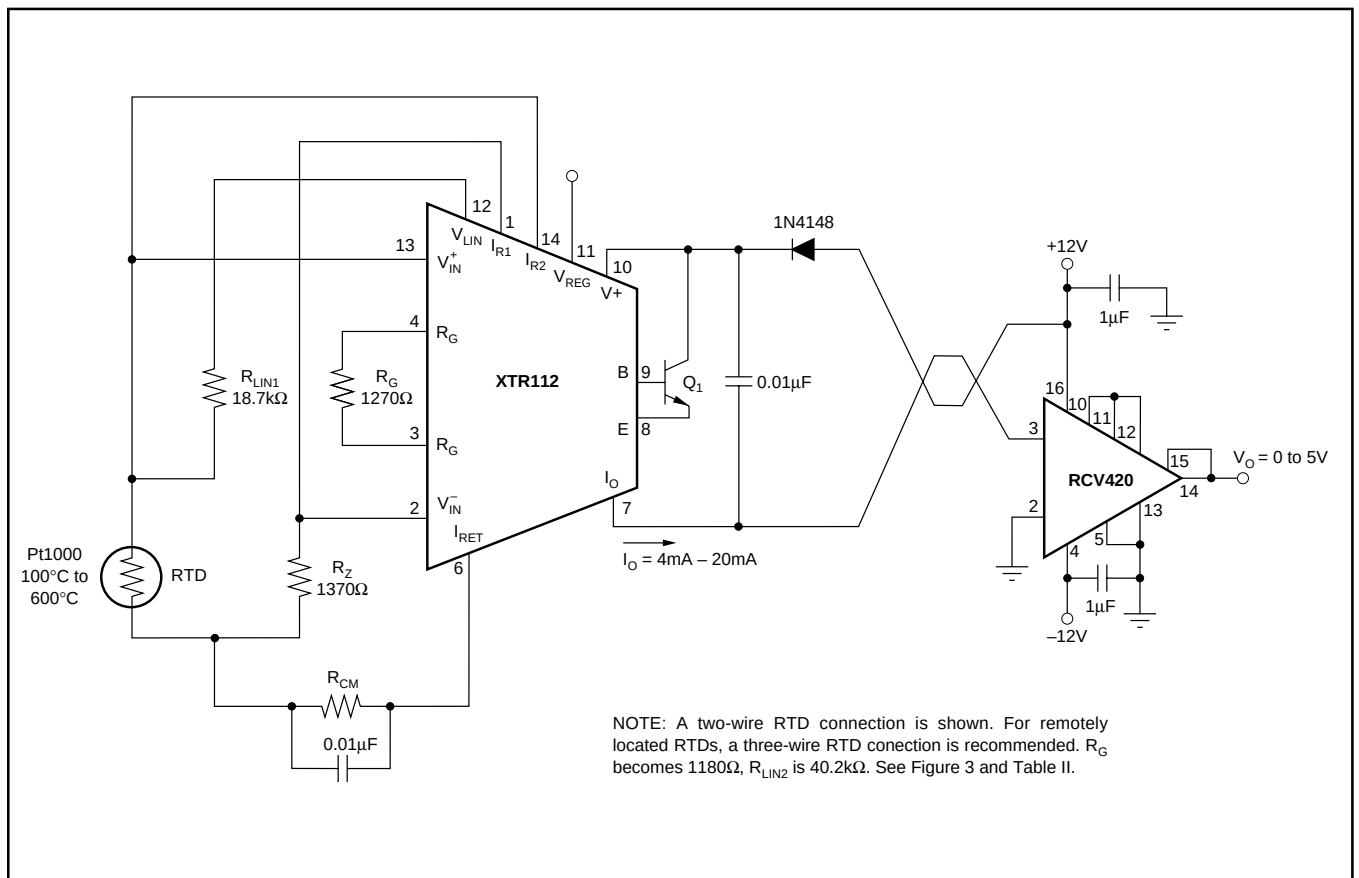


FIGURE 7. ±12V Powered Transmitter/Receiver Loop.

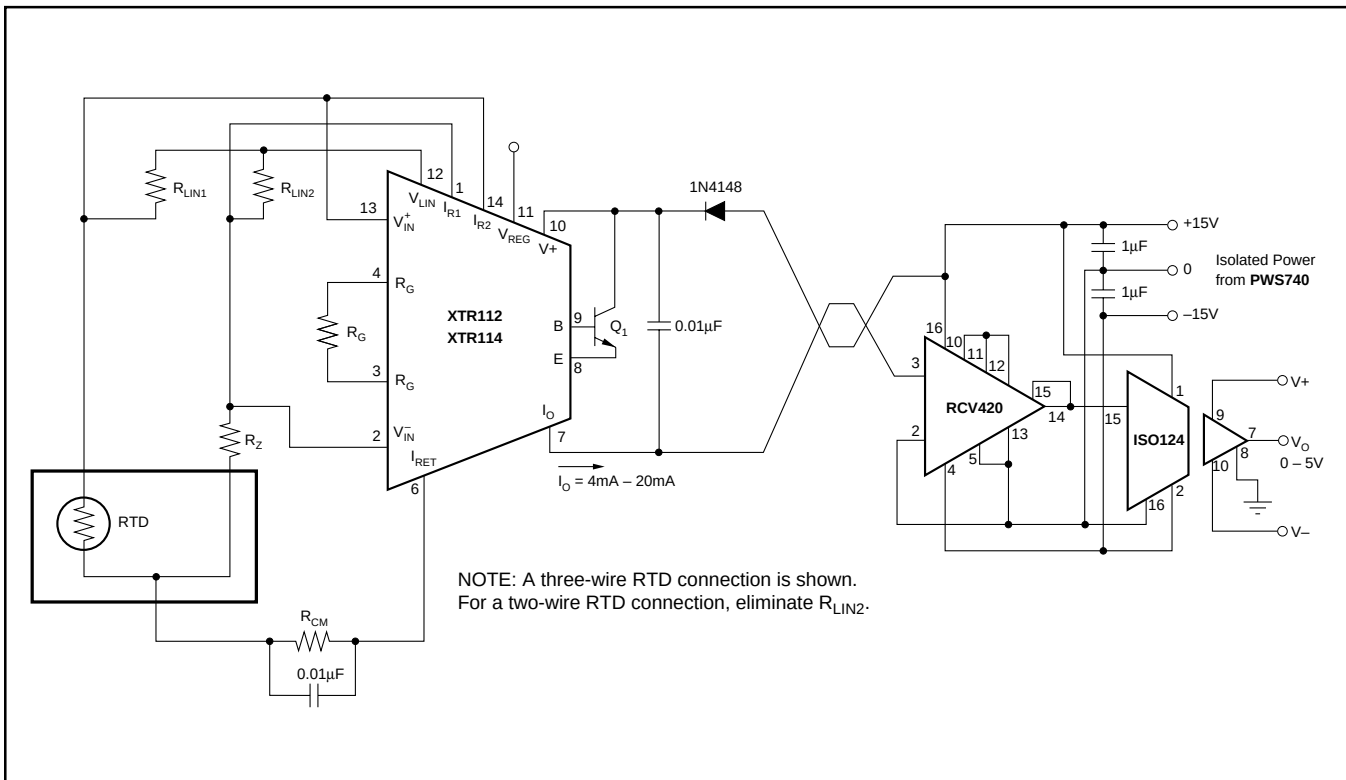


FIGURE 8. Isolated Transmitter/Receiver Loop.

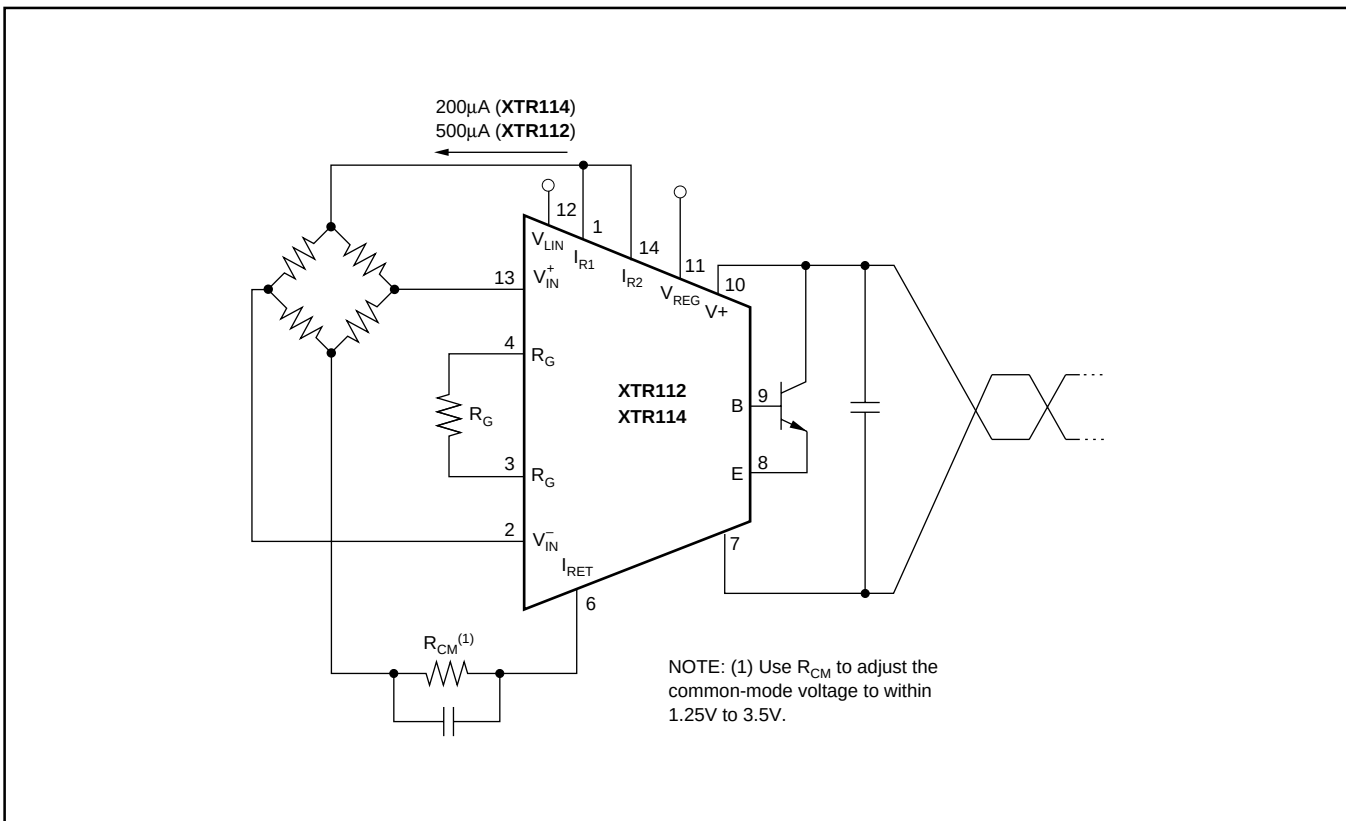


FIGURE 9. Bridge Input, Current Excitation.



**PACKAGING INFORMATION**

| Orderable Device | Status <sup>(1)</sup> | Package Type | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup> | Lead/Ball Finish | MSL Peak Temp <sup>(3)</sup> |
|------------------|-----------------------|--------------|-----------------|------|-------------|-------------------------|------------------|------------------------------|
| XTR112U          | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-3-260C-168 HR          |
| XTR112UA         | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-3-260C-168 HR          |
| XTR112UAE4       | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-3-260C-168 HR          |
| XTR112UE4        | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-3-260C-168 HR          |
| XTR114U          | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-3-260C-168 HR          |
| XTR114UA         | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-3-260C-168 HR          |
| XTR114UAE4       | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-3-260C-168 HR          |
| XTR114UE4        | ACTIVE                | SOIC         | D               | 14   | 50          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-3-260C-168 HR          |

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

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**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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