TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

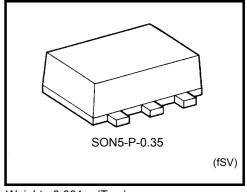
TC7SH86FS

EXCLUSIVE OR GATE

Features

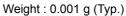
High speed: t_{pd} = 4.8 ns (typ.) at V_{CC} = 5 V Low power dissipation: I_{CC} = 2 μ A (max) at Ta = 25°C High noise immunity: V_{NIH} = V_{NIL} = 28% V_{CC} (min) 5.5V tolerant input.

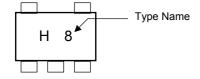
Wide operating voltage range: V_{CC} (opr) = 2~5.5 V

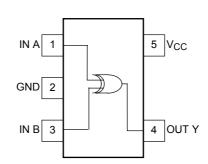


Marking (top view)

Pin Assignment



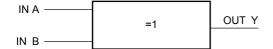




Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	−65~150	°C

Logic Diagram



Truth Table

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	L

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	2.0~5.5	V	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~V _{CC}	V	
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	0~100 (V _{CC} = 3.3 ± 0.3 V)	ns/V	
impat noc and fail time	uuuv	$0~20~(V_{CC} = 5 \pm 0.5~V)$	113/ V	

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test	t			Ta = 25°C		Ta = -40~85°C			
		Circuit	Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High-level input VIH —				2.0	1.50	_	_	1.50	_		
	_	_			V _{CC} × 0.7	_	_	V _{CC} × 0.7	_	V	
Low-level input			_		2.0	_	_	0.50	_	0.50	V
voltage	V _{IL}	_			3.0~ 5.5			V _{CC} × 0.3	_	V _{CC} × 0.3	
			V _{IN} = V _{IH} or V _{IL}	Ι _{ΟΗ} = -50 μΑ	2.0	1.9	2.0	_	1.9	_	V
		_			3.0	2.9	3.0	_	2.9	_	
High-level output voltage	V _{OH}				4.5	4.4	4.5	_	4.4	_	
				I _{OH} = -4 mA	3.0	2.58		_	2.48	_	
				$I_{OH} = -8 \text{ mA}$	4.5	3.94			3.80	_	
				I _{OL} = 50 μA	2.0		0.0	0.1	_	0.1	
					3.0	_	0.0	0.1		0.1	
Low-level output voltage VoL	_	$V_{IN} = V_{IL} \\$		4.5	_	0.0	0.1		0.1	V	
				I _{OL} = 4 mA	3.0	_		0.36		0.44	
				I _{OL} = 8 mA	4.5			0.36	_	0.44	
Input leakage current	I _{IN}	_	V _{IN} = 5.5 V or GND		0~ 5.5	_	1	±0.1	_	±1.0	μА
Quiescent supply current	I _{CC}	_	V _{IN} = V _{CC} or GND			_	_	2.0	_	20.0	μΑ

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AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol	Symbol	Test Circuit	٦	est Condition		Ta = 25°C			Ta = -40~85°C		- Unit
	Symbol			V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
1 1		_	_	3.3 ± 0.3	15		7.0	11.0	1.0	13.0	ns
	${ m t}_{ m pLH}$				50		9.5	14.5	1.0	16.5	
	t _{pHL}			5.0 ± 0.5	15		4.8	6.8	1.0	8.0	
					50		6.3	8.8	1.0	10.0	
Input capacitance	C _{IN}	_		_			4	10	_	10	pF
Power dissipation capacitance	C _{PD}	_			(Note)		18	_	_	_	pF

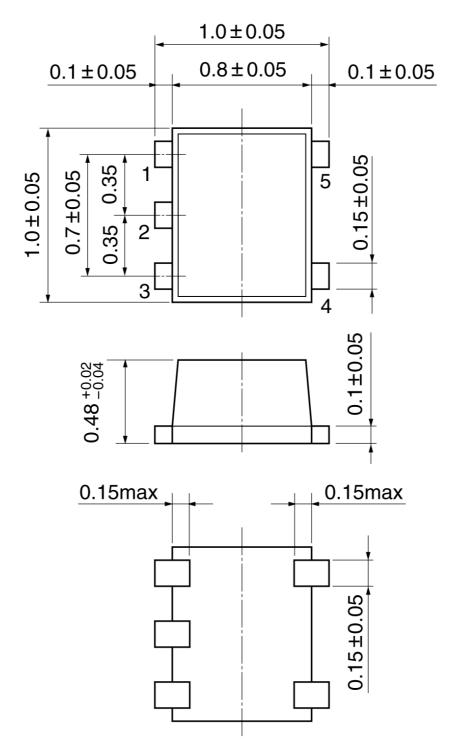
Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions

SON5-P-0.35 Unit:mm



Weight: 0.001 g (typ.)

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