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High-Precision, Low-Voltage, Micropower Op Amp

MAX480

General Description

The MAX480 is a precision micropower operational amplifier with flexible power-supply capability. Its guaranteed 140 μ V maximum offset voltage (25 μ V typ) is the lowest of any micropower op amp. Similarly, input bias current, input offset current, and drift specifications are within tight limits.

Both the input and output voltage ranges include the negative supply rail, allowing maximum signal range capability in single-supply applications. The MAX480 operates with either a single supply ranging from +1.6V to +36V or dual supplies from ± 0.8 V to ± 18 V. The MAX480 consumes less than 20 μ A, allowing operation in excess of 10,000 hours from a 250mA-hr lithium coin cell. Even with a minimal quiescent current, the amplifier sinks or sources 5mA from its output.

The MAX480 is available in 8-pin DIP and SO packages in commercial, extended, and military temperature ranges.

Applications

Precision Micropower Amplifiers

Micropower Signal Processing

Battery-Powered Analog Circuits

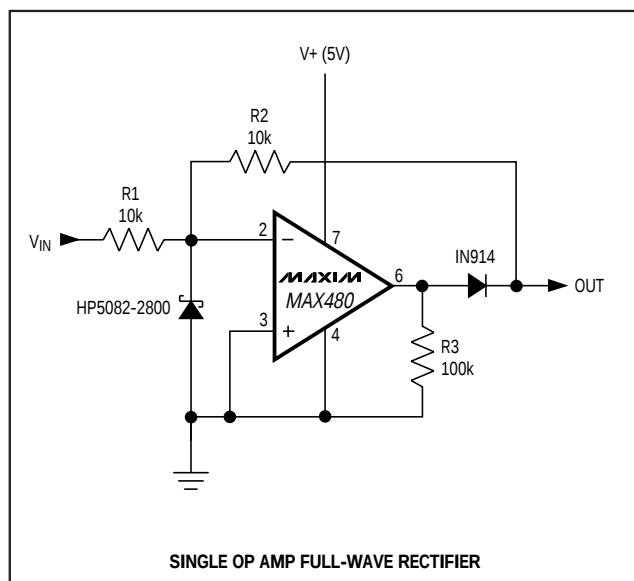
Features

- ◆ Single- or Dual-Supply Operation: +1.6V to +36V, ± 0.8 V to ± 18 V
- ◆ True Single-Supply Operation: Input and Output Voltage Ranges Include Ground
- ◆ 2.0 μ V/ $^{\circ}$ C Max Offset Voltage Drift
- ◆ 20 μ A Max Supply Current
- ◆ 5mA Min Output Drive
- ◆ 140 μ V Max Input Offset Voltage
- ◆ 3nA Max Input Bias Current
- ◆ 500V/mV Min Open-Loop Gain
- ◆ Standard 741 Pinout with Nulling to V-

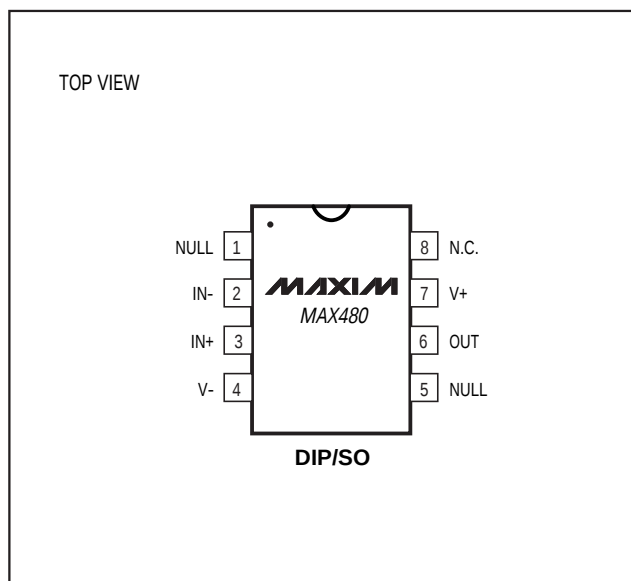
Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX480CPA	0 $^{\circ}$ C to +70 $^{\circ}$ C	8 Plastic DIP
MAX480CSA	0 $^{\circ}$ C to +70 $^{\circ}$ C	8 SO
MAX480EPA	-40 $^{\circ}$ C to +85 $^{\circ}$ C	8 Plastic DIP
MAX480ESA	-40 $^{\circ}$ C to +85 $^{\circ}$ C	8 SO
MAX480MJA	-55 $^{\circ}$ C to +125 $^{\circ}$ C	8 CERDIP

Typical Operating Circuit



Pin Configuration



Maxim Integrated Products 1

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For small orders, phone 408-737-7600 ext. 3468.

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ABSOLUTE MAXIMUM RATINGS

Supply Voltage (V_+ to V_-) $\pm 18V$
 Differential Input Voltage ($V_- - 20V$) to ($V_+ + 20V$)
 Common-Mode Input Voltage ($V_- - 20V$) to ($V_+ + 20V$)
 Output Short-Circuit Duration Indefinite
 Continuous Power Dissipation
 Plastic DIP (derate 9.09mW/°C above +70°C) 727mW
 SO (derate 5.88mW/°C above +70°C) 471mW
 CERDIP (derate 8.0mW/°C above +70°C) 640mW

Operating Temperature Ranges

MAX480C_A 0°C to +70°C
 MAX480E_A -40°C to +85°C
 MAX480MJA -55°C to +125°C
 Junction Temperature (T_J) -65°C to +150°C
 Storage Temperature Range -65°C to +150°C
 Lead Temperature (soldering, 10sec) +300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

($V_S = \pm 1.5V$ to $\pm 15V$, $T_A = +25^\circ C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Input Offset Voltage	V_{OS}			25	140	μV
Input Offset Current	I_{OS}	$V_{CM} = 0$		0.2	1	nA
Input Bias Current	I_B	$V_{CM} = 0$		1	3	nA
Large-Signal Voltage Gain	A_{VO}	$V_S = \pm 15V$, $V_O = \pm 10V$	$R_L = 100k\Omega$	500	1200	V/mV
			$R_L = 10k\Omega$	200	600	
			$R_L = 2k\Omega$	75	250	
		$V_+ = 5V$, $V_- = 0$, $1V < V_O < 4V$	$R_L = 100k\Omega$	100	400	
			$R_L = 10k\Omega$	50	180	
Input Voltage	IVR	$V_+ = 5V$, $V_- = 0$		0/4		V
		$V_S = \pm 15kV$ (Note 1)		-15/13.5		
Output Voltage Swing	V_O	$V_S = \pm 15V$	$R_L = 10k\Omega$	± 14	± 14.2	V
			$R_L = 2k\Omega$	± 11	± 12	
	V_{OH}	$V_+ = 5V$, $V_- = 0$, $R_L = 2k\Omega$		4.0	4.2	μV
	V_{OL}	$V_+ = 5V$, $V_- = 0$, $R_L = 10k\Omega$		100	500	
Common-Mode Rejection Ratio	CMRR	$V_+ = 5V$, $V_- = 0$, $0 < V_{CM} < 4V$	85	110		dB
		$V_S = \pm 15V$, $-15V < V_{CM} < 13.5V$	90	130		
Power-Supply Rejection Ratio	PSRR			1.0	12	$\mu V/V$
Slew Rate	SR	$V_S = \pm 15V$	5	12		V/ms
Supply Current	I_{SY}	$V_S = \pm 1.5V$		9	15	μA
		$V_S = \pm 15V$		14	20	
Capacitive Load Stability		$A_V = +1V/V$, no oscillations (Note 2)	250	650		pF
Input Noise Voltage	e_{np-p}	$f_O = 0.1Hz$ to $10Hz$, $V_S = \pm 15V$		3		$\mu Vp-p$
Differential-Mode Input Resistance	R_{IN}	$V_S = \pm 15V$		30		M Ω
Common-Mode Input Resistance	R_{INCM}	$V_S = \pm 15V$		20		G Ω

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ELECTRICAL CHARACTERISTICS

($V_S = \pm 1.5V$ to $\pm 15V$, $T_A = T_{MIN}$ to T_{MAX} , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MAX480C			MAX480E			MAX480M			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	MIN	TYP	MAX	
Input Offset Voltage	V _{OS}			40	220		50	250		70	300		μV
Input Offset Voltage Drift	TCV _{OS}	(Note 2)		0.3	2.0		0.3	2.0		0.3	2.0		μV/°C
Input Offset Current	I _{OS}	V _{CM} = 0		0.2	2.0		0.3	2.0		0.5	2.0		nA
Input Bias Current	I _B	V _{CM} = 0		1	3		2	5		3	7		nA
Large-Signal Voltage Gain	A _{VO}	V _S = ±15V, V = ±10V	R _L = 100kΩ	350	950		350	800		60	400		V/mV
			R _L = 10kΩ	130	400		130	400		45	240		
			R _L = 2kΩ	55	125		55	150		30	110		
		V ₊ = 5V, V ₋ = 0, 1V < V _O < 4V	R _L = 100kΩ	50	360		50	280		35	200		
			R _L = 10kΩ	30	150		30	140		22	110		
Input Voltage Range	IVR	V ₊ = 5V, V ₋ = 0		0/3.5		0/3.5		0/3.5				V	
		V _S = ±15V (Note 1)		-15/13.5		-15/13.5		-15/13.5					
Output Voltage Swing	V _O	V _S = ±15V	R _L = 10kΩ	±13.5 ±14		±13.5 ±14		±13.5 ±13.7				V	
			R _L = 2kΩ	±10.5 ±11.8		±10.5 ±11.8		±10.5 ±11.5					
	V _{OH}	V ₊ = 5V, V ₋ = 0 R _L = 2kΩ		3.9 4.1		3.9 4.1		3.9 4.1				μV	
	V _{OL}	V ₊ = 5V, V ₋ = 0 R _L = 10kΩ		100 500		100 500		100 500					
Common-Mode Rejection Ratio	CMRR	V ₊ = 5V, V ₋ = 0, 0 < V _{CM} < 3.5V		85 110		85 110		80 105				dB	
		V _S = ±15V, -15V < V _{CM} < 13.5V		90 120		90 120		85 115					
Power-Supply Rejection Ratio	PSRR			1.0 12		1.0 12		3.2 15				μV/V	
Supply Current	I _{SY}	V _S = ±1.5V		12 25		13 25		15 25				μA	
		V _S = ±15V		16 30		17 30		19 30					

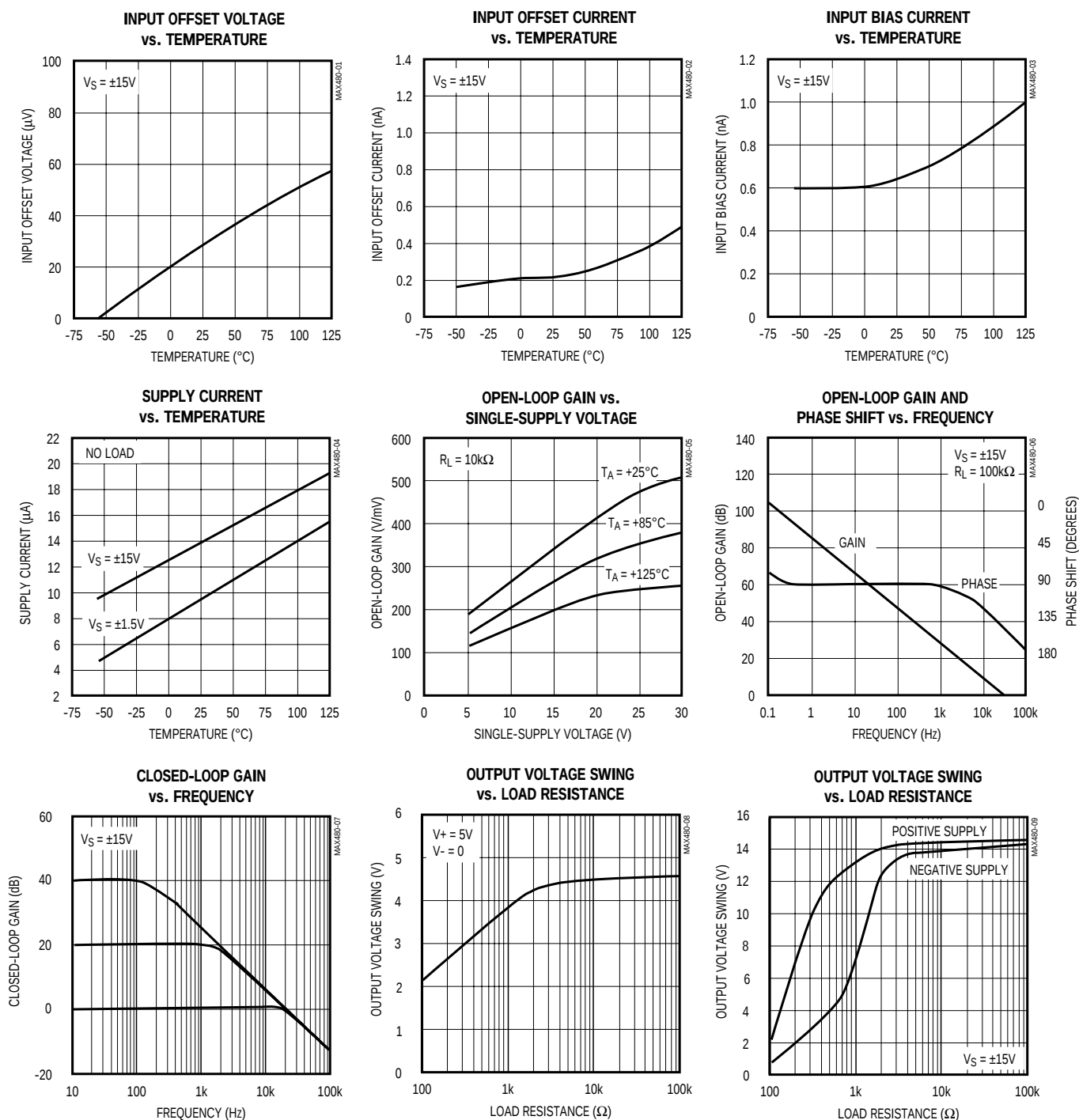
Note 1: Guaranteed by CMRR test.

Note 2: Guaranteed by design.

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Typical Operating Characteristics

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

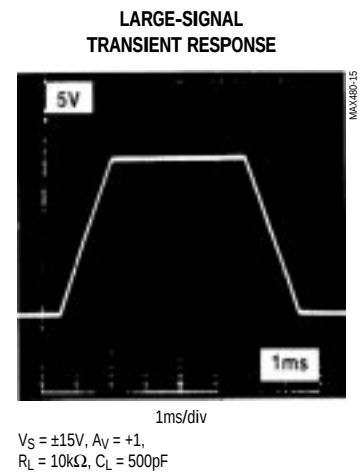
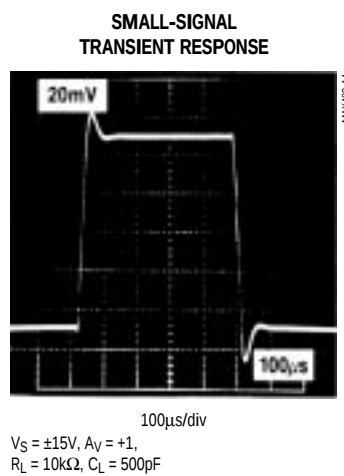
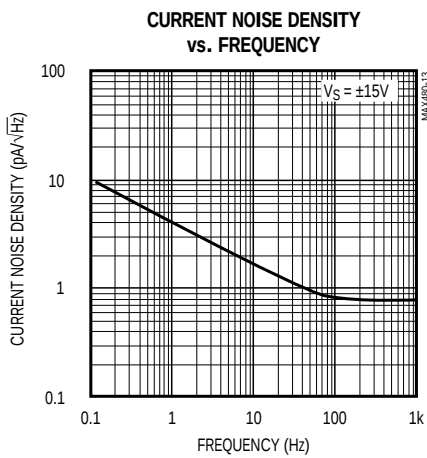
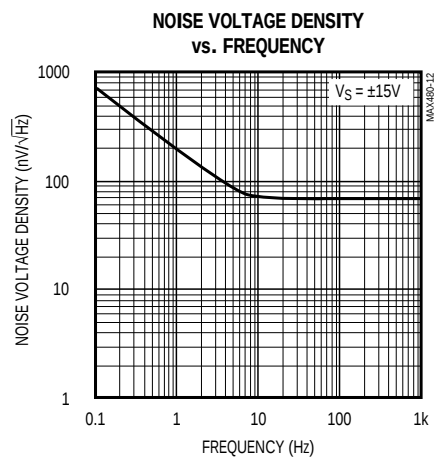
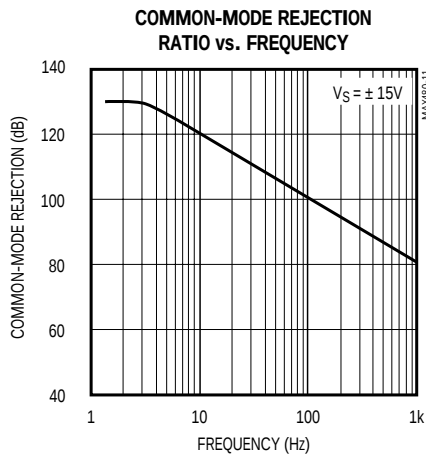
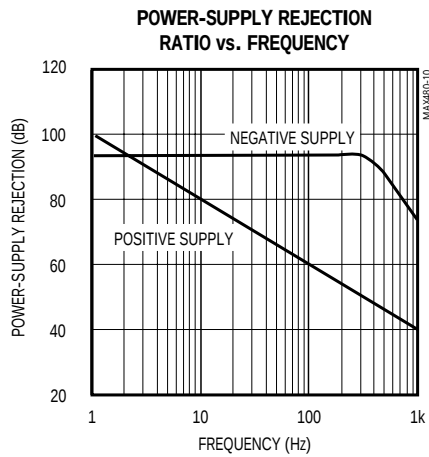


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Typical Operating Characteristics (continued)

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

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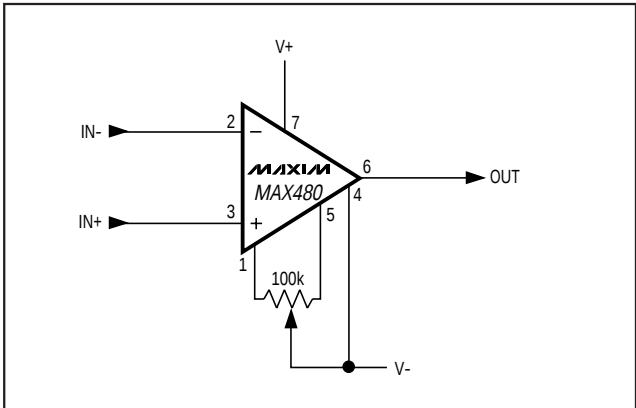


Figure 1. Offset Nulling Circuit

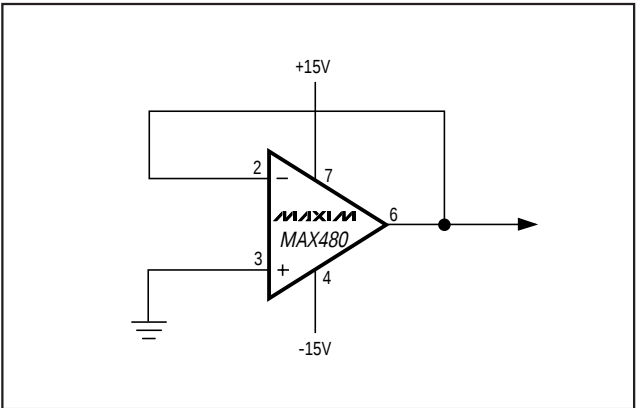
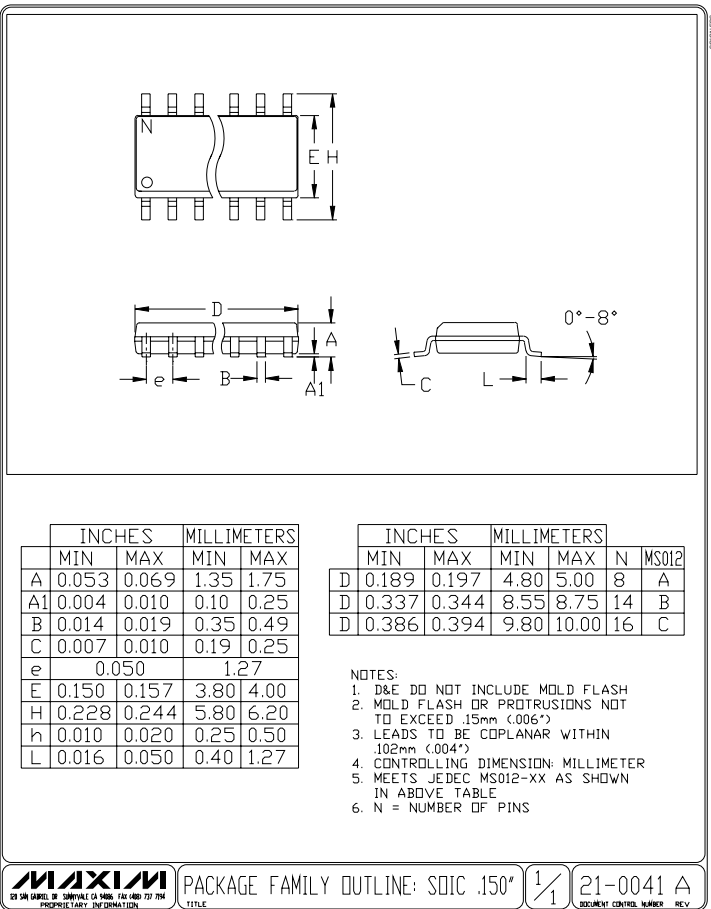


Figure 2. Burn-In Circuit

Package Information



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