# ACR44U



# **Fast Turn-off Asymmetric Thyristor**

Replaces March 1998 version, DS4222-3.4

DS4222-4.0 January 2000

**KEY PARAMETERS** 

### **APPLICATIONS**

- High Frequency Applications
- Regulated Power Supplies
- Capacitor Discharge
- Ultrasonic Generators
- Induction Heating

# $\begin{array}{ccc} V_{_{DRM}} & 1600V \\ I_{_{T(AV)}} & 44A \\ I_{_{TSM}} & 550A \\ dVdt^* & 600V/\mu s \\ dI/dt & 2000A/\mu s \\ t_{_{q}} & 6.0\mu s \\ ^*dV/dt \ Available \ to \ 1000V/\mu s \end{array}$

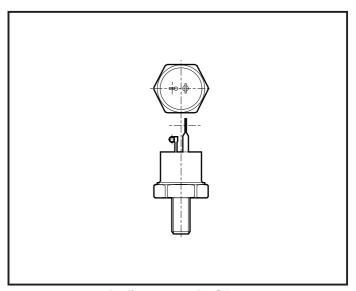
#### **FEATURES**

■ The ACR44U is a glass passivated asymmetric thyristor which has exceptionally fast turn-off capabilities combined with good turn-on characteristics.

#### **VOLTAGE RATINGS**

Type Number	Repetitive Peak Off-state Voltage V	Repetitive Peak Reverse Voltage V <sub>RRM</sub> V
ACR44U 16LE	1600	2
ACR44U 14LE	1400	2
ACR44U 12LE	1200	2
ACR44U 10LE	1000	2
ACR44U 08LE	800	2

Lower voltage grades available.



Outline type code: SO28.
See Package Details for further information.

### **CURRENT RATINGS**

Symbol	Parameter	Conditions		Units
I <sub>T(AV)</sub>	Mean on-state current	Half wave resistive load, T <sub>case</sub> = 80°C		А
I <sub>T(RMS)</sub>	RMS value	$T_{case} = 70^{\circ}C$		А
I <sub>T</sub>	Continuous (direct) on-state current	T <sub>case</sub> = 85°C	57	А

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# **SURGE RATINGS**

Symbol	Parameter	Conditions	Max.	Units
I <sub>TSM</sub>	Surge (non-repetitive) forward current	40   1   1   1   1   1   1   1   1   1	550	А
l <sup>2</sup> t	I <sup>2</sup> t for fusing	10ms half sine; T <sub>case</sub> = 125°C	1500	A²s

# THERMAL AND MECHANICAL DATA

Symbol	Parameter	Conditions	Min.	Max.	Units
R <sub>th(j-c)</sub>	Thermal resistance - junction to case	d.c.	-	0.35	°C/W
R <sub>th(c-h)</sub>	Thermal resistance - case to heatsink	Mounting torque 3.5Nm with mounting compound	-	0.25	°C/W
T <sub>vj</sub>	Virtual junction temperature	On-state (conducting)	-	125	°C
T <sub>stg</sub>	Storage temperature range		-55	125	°C
-	Mounting torque		3.5	4.0	Nm

# **DYNAMIC CHARACTERISTICS**

 $T_{case} = 125$ °C unless otherwise stated.

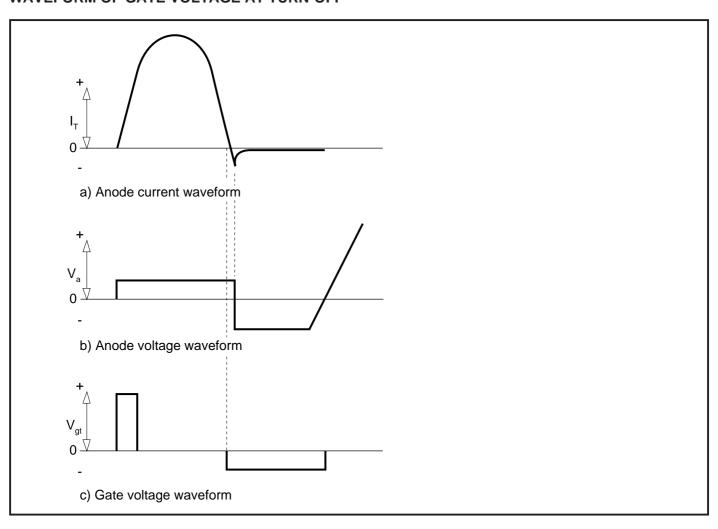
Symbol	Parameter	Conditions	Тур.	Max.	Units
V <sub>TM</sub>	Maximum on-state voltage	At 100A peak, T <sub>case</sub> = 25°C		2.7	V
I <sub>RRM</sub> /I <sub>DRM</sub>	Peak reverse and off-state current	At V <sub>RRM</sub> /V <sub>DRM</sub> , T <sub>case</sub> = 125°C	-	20/10	mA
dV/dt	Maximum linear rate of rise of off-state voltage	To $V_{DRM} T_j = 125$ °C, gate open circuit	-	600 <sup>*</sup>	V/μs
dl/dt	Rate of rise of on-state current	From $V_{DRM}$ to 125A. Gate source 15V, 15 $\Omega$ t <sub>r</sub> = 50ns	-	2000	A/μs
V <sub>T(TO)</sub>	Threshold voltage	-		1.5	V
r <sub>T</sub>	On-state slope resistance	-	-	13.3	mΩ
I <sub>L</sub>	Latching current	-	120	-	mA
I <sub>H</sub>	Holding current	-	25	-	mA
t <sub>d</sub>	Delay time	$V_D = 300V$ , gate source = 15V, 15 $\Omega$	-	250	ns
t <sub>q</sub>	Turn-off time (with antiparallel diode)	$I_T = 50$ A, square wave $t_p = 50$ µs, $T_j = 120$ °C, $dI_R/dt = 50$ A/µs, $dV/dt = 600$ V/µs to $V_{DRM}$ , gate voltage at turn-off 3.5-4.5V. $V_R = -1$ V.	-	6.0	μs

<sup>\*</sup> Available to 1000V/µs.

## **GATE TRIGGER CHARACTERISTICS AND RATINGS**

Symbol	Parameter	Conditions		Тур.	Max.	Units
V <sub>GT</sub>	Gate trigger voltage	$V_{DWM} = 12V, R_L = 30\Omega, T_{case} = 100$	$V_{DWM} = 12V, R_L = 30\Omega, T_{case} = 25^{\circ}C$		3.0	V
I <sub>GT</sub>	Gate trigger current	$V_{DWM} = 12V, R_{L} = 30\Omega, T_{case} = 25^{\circ}C$		60	200	mA
$V_{FGM}$	Peak forward gate voltage	-		-	40	V
$V_{RGM}$	Peak reverse gate voltage	-		-	10	V
I <sub>FGM</sub>	Peak forward gate current	-		-	10	Α
P <sub>GM</sub>	Peak gate power	-		-	40	W
$P_{G(AV)}$	Average gate power	Average time 10ms max	Forward	-	10	W
			Reverse	-	6	W

## **WAVEFORM OF GATE VOLTAGE AT TURN-OFF**



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# **CURVES**

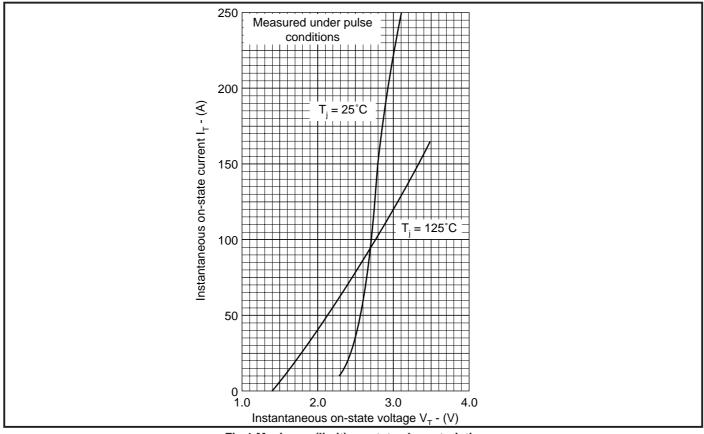


Fig.1 Maximum (limit) on-state characteristics

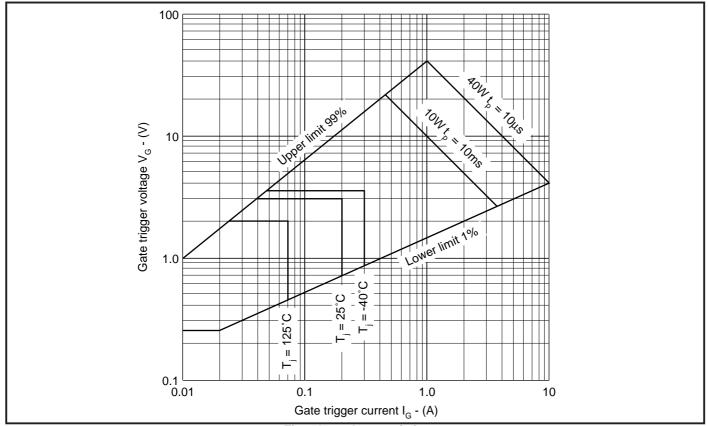


Fig.2 Gate characteristics

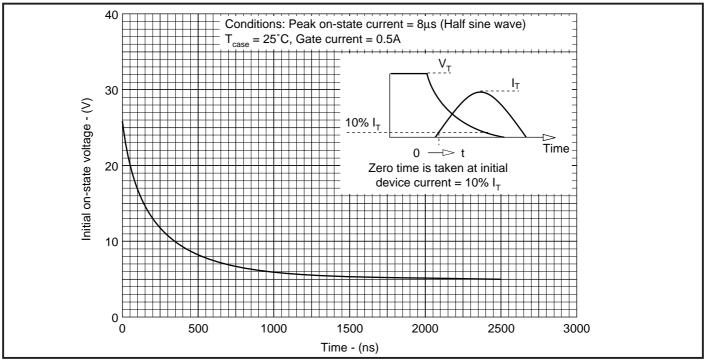


Fig.3 Typical initial on-state voltage vs time

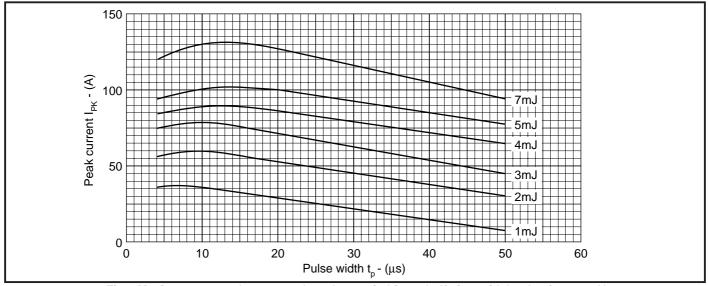


Fig.4 Maximum energy loss per pulse when switching a half sinusoidal pulse from 600V

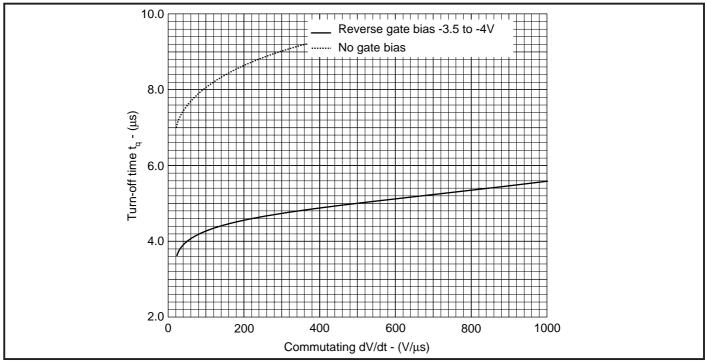


Fig.5 Variation of turn-off time with commutating dV/dt

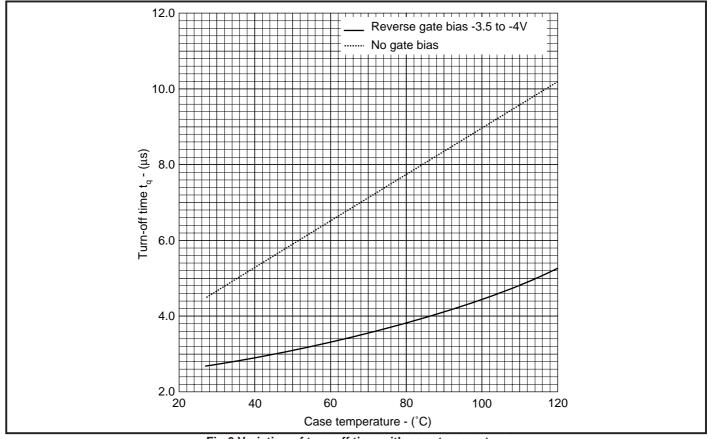
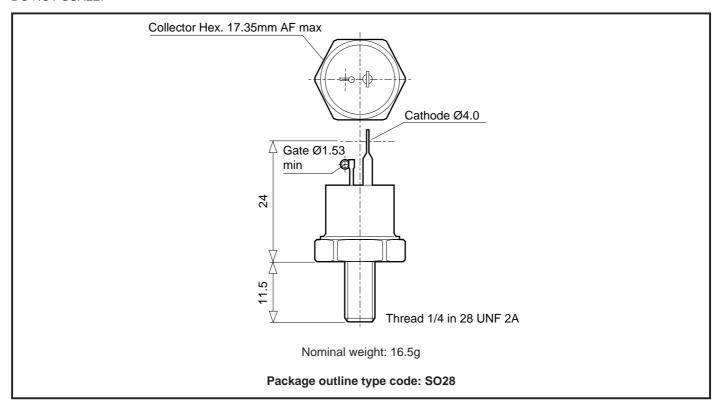


Fig.6 Variation of turn-off time with case temperature

#### **PACKAGE DETAILS**

For further package information, please contact your local Customer Service Centre. All dimensions in mm, unless stated otherwise. DO NOT SCALE.





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Preliminary Information: The product is in design and development. The datasheet represents the product as it is understood but details may change

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No Annotation: The product parameters are fixed and the product is available to datasheet specification.

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