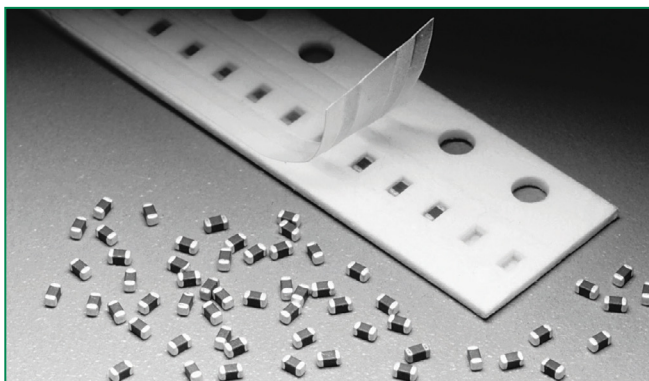


### MHS Varistor Series



#### Size Table

Metric	EIA
1005	0402
1608	0603

#### Additional Information



**Datasheet**



**Resources**



**Samples**

#### Applications

- Data, Diagnostic I/O Ports
- Universal Serial Bus (USB)
- Video & Audio Ports
- Portable/Hand-Held Products
- Mobile Communications
- Computer/DSP Products
- Industrial Instruments Including Medical

#### Description

The Multilayer High-Speed MHS Series is a very-low capacitance extension to the Littelfuse ML family of transient voltage surge suppression devices available in an 0402 and 0603-size surface mount chip.

The MHS Series provides protection from ESD and EFT in high-speed data line and other high frequency applications. The low capacitance of the MHS Series permits usage in analog or digital circuits where it will not attenuate or distort the desired signal or data.

Their small size is ideal for high-density printed circuit boards, being typically applied to protect integrated circuits and other sensitive components. They are particularly well suited to suppress ESD events including those specified in IEC 61000-4-2 or other standards used for Electromagnetic Compliance (EMC) testing.

The MHS Series is manufactured from semiconducting ceramics and is supplied in a leadless, surface mount package. The MHS Series is also compatible with modern reflow and wave soldering processes.

Littelfuse Inc. manufactures other multilayer varistor series products, see the ML, MLE, MLN and AUML Series data sheets.

#### Features

- Halogen-Free and RoHS compliant
- 3pF, 12pF, and 22pF capacitance versions suitable for high-speed data rate lines
- ESD rated to IEC
- 61000-4-2 (Level 4)
- EFT/B rated to IEC 61000-4-4 (Level 4)
- Low leakage currents
- -55°C to +125°C operating temp. range
- Inherently bi-directional

#### Absolute Maximum Ratings

• For ratings of individual members of a series, see device ratings and specifications table.

Continuous	MHS Series	Units
Steady State Applied Voltage:		
DC Voltage Range ( $V_{MDC}$ ) :	V0402/0603MHS03	$\leq 42$ V
	V0402/0603MHS12	$\leq 18$ V
	V0402/0603MHS22	$\leq 09$ V
Operating Ambient Temperature Range ( $T_A$ )	-55 to +125	°C
Storage Temperature Range ( $T_{STG}$ )	-55 to +150	°C

### Device Ratings and Specifications

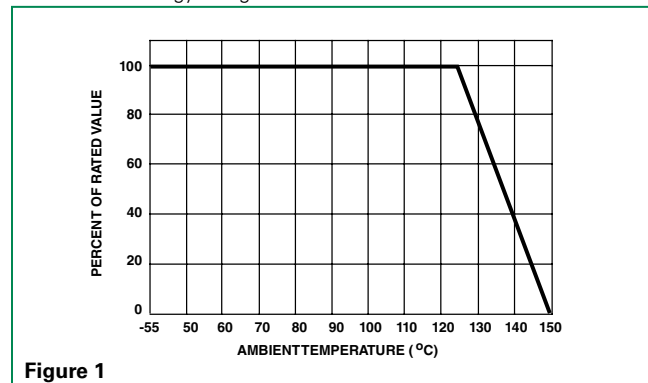
Part Number	Performance Specifications (25 °C)							
	Maximum Clamping Voltage At 1A (8X20μs)	Maximum ESD Clamp Voltage (Note 1)		Typical Leakage Current at Specified DC Voltage		Typical Capacitance at 1MHz (1V p-p)		Typical Inductance (from Impedance Analysis)
		8kV Contact (Note 2)	15kV AIR (Note 3)	3.5V	5.5V	C (Note 4)		
		Clamp	Clamp	P	I <sub>L</sub>	MIN	MAX	
	(V <sub>c</sub> )	(V)	(V)	(μA)	(μA)	(pF)	(pF)	(nH)
V0402MHS03N (Note 5)	135	<300	<400	0.5	1.00	2	5	<1.0
V0402MHS03F (Note 7)	135	<300	<400	0.5	1.00	2	5	<1.0
V0603MHS03N (Note 5)	135	<300	<400	0.5	1.00	1	6	<1.0
V0603MHS03F (Note 7)	135	<300	<400	0.5	1.00	1	6	<1.0
V0402MHS12N (Note 5)	55	<125	<160	0.5	1.00	8	16	<1.0
V0402MHS12F (Note 7)	55	<125	<160	0.5	1.00	8	16	<1.0
V0603MHS12N (Note 5)	55	<125	<160	0.5	1.00	8	16	<1.0
V0603MHS12F (Note 7)	55	<125	<160	0.5	1.00	8	16	<1.0
V0402MHS22N (Note 5)	30	<125	<160	0.5	1.00	15	29	<1.0
V0402MHS22F (Note 7)	30	<125	<160	0.5	1.00	15	29	<1.0
V0603MHS22N (Note 5)	30	<65	<100	0.5	1.00	15	29	<1.0
V0603MHS22F (Note 7)	30	<65	<100	0.5	1.00	15	29	<1.0

#### NOTES:

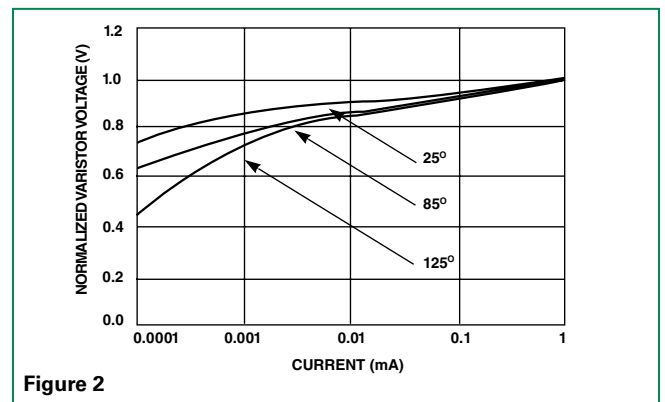
1. Tested to IEC-61000-4-2 Human Body Model (HBM) discharge test circuit.
2. Direct discharge to device terminals (IEC preferred test method).
3. Corona discharge through air (represents actual ESD event).
4. Capacitance may be customized, contact your Littelfuse Sales Representative.
5. V0402MHSxxx (0402 size devices) available as "R" packaging option only. Example: V0402MHS03NR. See Packaging and Tape and Reel sections (last page) for additional information.
6. The typical capacitance rating is discrete component test result.
7. Items are lead free and antimony free, available as "R" packing option only.

### Peak Current and Energy Derating Curve

For applications exceeding 125°C ambient temperature, the peak surge current and energy ratings must be reduced as shown below.



### Standby Current at Normalized Varistor Voltage and Temperature



**Disclaimer Notice - Information furnished is believed to be accurate and reliable. However, users should independently evaluate the suitability of and test each product selected for their own applications. Littelfuse products are not designed for, and may not be used in, all applications. Read complete Disclaimer Notice at [www.littelfuse.com/disclaimer-electronics](http://www.littelfuse.com/disclaimer-electronics).**

### Nominal Voltage Stability to Multiple ESD Impulses (8kV Contact Discharges per IEC 61000-4-2)

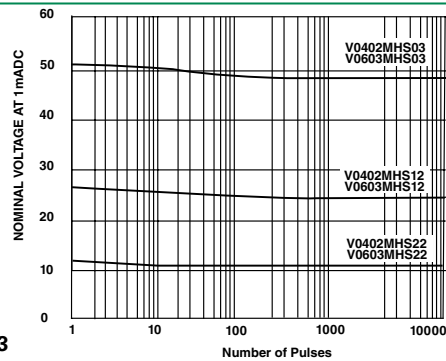


Figure 3

### Insertion Loss (S21) Characteristics

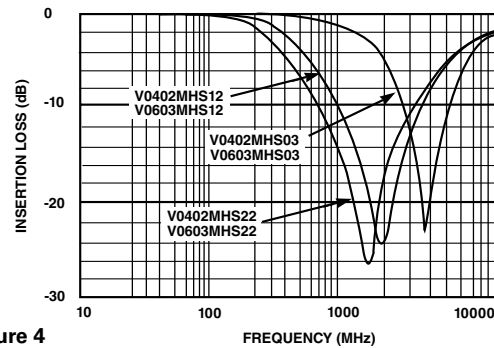


Figure 4

### Device Characteristics

At low current levels, the V-I curve of the multilayer transient voltage suppressor approaches a linear (ohmic) relationship and shows a temperature dependent effect. At or below the maximum working voltage, the suppressor is in a high resistance model (approaching  $10^9\Omega$  at its maximum rated working voltage). Leakage currents at maximum rated voltage are below  $100\mu A$ , typically  $25\mu A$ ; for 0402 size below  $20\mu A$ , typically  $5\mu A$ .

Typical Temperature Dependence of the Characteristic Curve in the Leakage Region

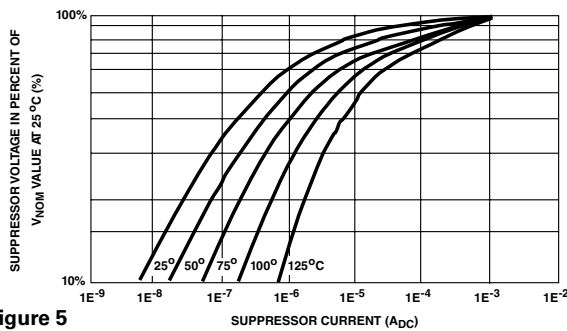


Figure 5

### Speed of Response

The Multilayer Suppressor is a leadless device. Its response time is not limited by the parasitic lead inductances found in other surface mount packages. The response time of the  $Z_{nO}$  dielectric material is less than 1ns and the MLE can clamp very fast  $dV/dT$  events such as ESD. Additionally, in "real world" applications, the associated circuit wiring is often the greatest factor effecting speed of response. Therefore, transient suppressor placement within a circuit can be considered important in certain instances.

Multilayer Internal Construction

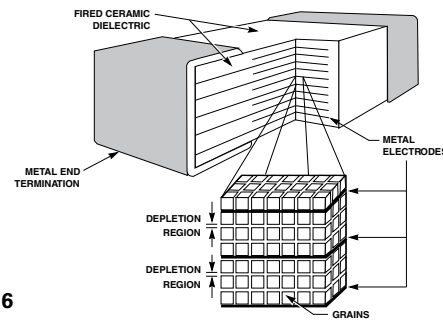


Figure 6

### Lead (Pb) Soldering Recommendations

The principal techniques used for the soldering of components in surface mount technology are IR Re-flow and Wave soldering. Typical profiles are shown on the right.

The recommended solder for the MHS suppressor is a 62/36/2 (Sn/Pb/Ag), 60/40 (Sn/Pb) or 63/37 (Sn/Pb). Littelfuse also recommends an RMA solder flux.

Wave soldering is the most strenuous of the processes. To avoid the possibility of generating stresses due to thermal shock, a preheat stage in the soldering process is recommended, and the peak temperature of the solder process should be rigidly controlled.

### Reflow Solder Profile

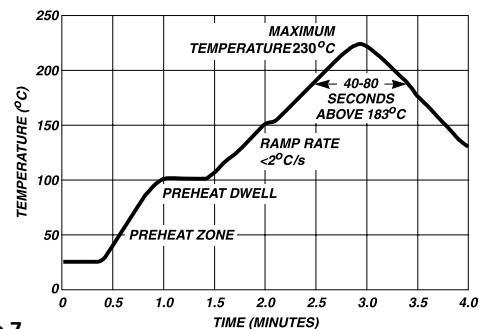


Figure 7

### Lead-free (Pb-free) Soldering Recommendations

When using a reflow process, care should be taken to ensure that the MHS chip is not subjected to a thermal gradient steeper than 4 degrees per second; the ideal gradient being 2 degrees per second. During the soldering process, preheating to within 100 degrees of the solder's peak temperature is essential to minimize thermal shock.

Once the soldering process has been completed, it is still necessary to ensure that any further thermal shocks are avoided. One possible cause of thermal shock is hot printed circuit boards being removed from the solder process and subjected to cleaning solvents at room temperature. The boards must be allowed to cool gradually to less than 50°C before cleaning.

Littelfuse offers the Nickel Barrier Termination finish for the optimum Lead-free solder performance.

The preferred solder is 96.5/3.0/0.5 (SnAgCu) with an RMA flux, but there is a wide selection of pastes and fluxes available with which the Nickel Barrier parts should be compatible.

The reflow profile must be constrained by the maximums in the Lead-free Reflow Profile. For Lead-free wave soldering, the Wave Solder Profile still applies.

Note: the Lead-free paste, flux and profile were used for evaluation purposes by Littelfuse, based upon industry standards and practices. There are multiple choices of all three available, it is advised that the customer explores the optimum combination for their process as processes vary considerably from site to site.

### Wave Solder Profile

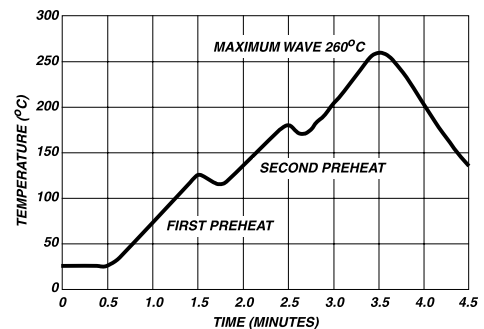


Figure 8

### Lead-free Re-flow Profile

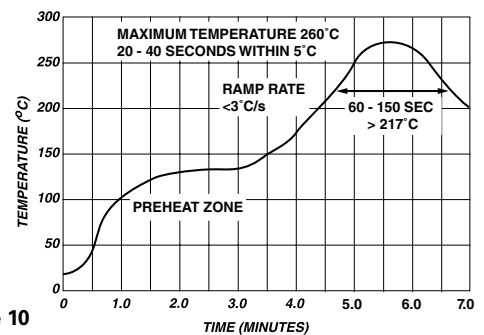
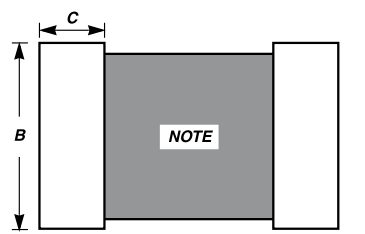


Figure 10

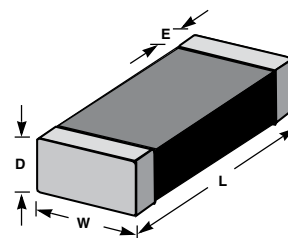
### Product Dimensions (mm)

#### PAD LAYOUT DIMENSIONS



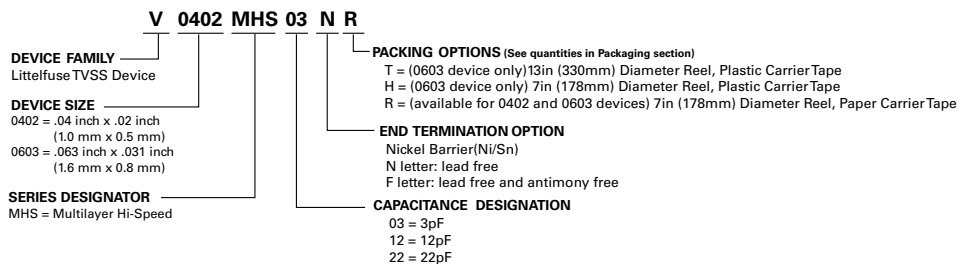
Note: Avoid metal runs in this area, parts are not recommended for use in applications using Silver (Ag) epoxy paste.

#### CHIP LAYOUT DIMENSIONS



Dimension	0402 Size		0603 Size	
	IN	MM	IN	MM
<b>A</b>	0.067	1.700	0.100	2.540
<b>B</b>	0.020	0.510	0.030	0.760
<b>C</b>	0.024	0.610	0.035	0.890
<b>D (max.)</b>	0.024	0.600	0.040	1.000
<b>E</b>	0.01 +/- 0.006	0.25 +/- 0.15	0.015 +/- 0.008	0.4 +/- 0.2
<b>L</b>	0.039 +/- 0.004	1.00 +/- 0.10	0.063 +/- 0.006	1.6 +/- 0.15
<b>W</b>	0.020 +/- 0.004	0.50 +/- 0.10	0.032 +/- 0.006	0.8 +/- 0.15

### Part Numbering System

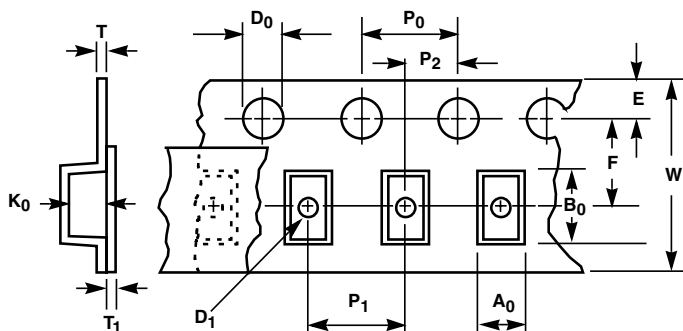


### Packaging\*

Device Size	Quantity		
	13 Inch Reel ("T" Option)	7 Inch Reel ("H" Option)	7 Inch Reel ("R" Option)
0603	10,000	2,500	4,000
0402	not available	not available	10,000

\*(Packaging) It is recommended that parts be kept in the sealed bag provided and that parts be used as soon as possible when removed from bags.

### Tape and Reel Specifications



Symbol	Description	Dimensions in Millimeters	
		0402 Size	0603 Size
$A_0$	Width of Cavity	Dependent on Chip Size to Minimize Rotation.	
$B_0$	Length of Cavity	Dependent on Chip Size to Minimize Rotation.	
$K_0$	Depth of Cavity	Dependent on Chip Size to Minimize Rotation.	
$W$	Width of Tape	8 +/- 0.2	8 +/- 0.3
$F$	Distance Between Drive Hole Centers and Cavity Centers	3.5 +/- .05	3.5 +/- .05
$E$	Distance Between Drive Hole Centers and Tape Edge	1.75 +/- 0.1	1.75 +/- 0.1
$P_1$	Distance Between Cavity Centers	2 +/- 0.05	4 +/- 0.1
$P_2$	Axial Drive Distance Between Drive Hole Centers & Cavity Centers	2 +/- 0.1	2 +/- 0.1
$P_0$	Axial Drive Distance Between Drive Hole Centers	4 +/- 0.1	4 +/- 0.1
$D_0$	Drive Hole Diameter	1.55 +/- 0.05	1.55 +/- 0.05
$D_1$	Diameter of Cavity Piercing	N/A	1.05 +/- 0.05
$T_1$	Top Tape Thickness	0.1 Max	0.1 Max
$T$	Nominal Carrier Tape Thickness	1.1	1.1

**Notes:**

- Conforms to EIA-481-1, Revision A
- Can be supplied to IEC publication 286-3