



[2 YEAR WARRANTY]

ΤÜV

NFC25 SERIES

Triple output



- Low profile 0.43 inches
- Remote ON/OFF
- BTR2511 conformance
- UL, CSA and TÜV approvals
- EN55022 conducted emissions level B
- Industry standard footprint
- Fixed operating frequency
- Load sharing facility
- Available RoHS compliant

The NFC25 series of dc-dc converters provide 25 Watts of power in a low profile, 11 mm, industry standard package. Key design specifications are tight line and load regulation, 80% efficiency, load sharing facility, conducted emissions to EN55022 level B, and the BTR2511 requirements (hold-up time, reverse polarity protection etc.). These specifications have been enhanced by adding such standard features as remote ON/OFF, short circuit protection and overvoltage protection. The input voltage ranges encompass 9 Vdc to18 Vdc, 18 Vdc to 36 Vdc and 36 Vdc to 72 Vdc. The NFC25 Series is ideal for applications such as telecommunications, process control, factory automation, mobile and test equipment.

SPECIFICATION All specifications are typical at nominal input, full load at 25 °C unless otherwise stated

OUTPUT SPECIFICATION	ONS				
Voltage accuracy	5 V Auxiliary outputs	±1.0% ±3.0%			
Voltage adjustability	All Outputs	±10%			
Line regulation	LL to HL, +5 V ou LL to HL, auxiliari				
Load regulation	FL to NL, +5 V ou (See Note 6) Auxiliary output	utput ±1%, max. See diagram			
Cross regulation		See graphs			
Ripple and noise (5 Hz to 20 MHz)	5 V output Auxiliaries	80 mV pk-pk, max. 150 mV pk-pk, max.			
Temperature coefficient	All outputs	±0.02%/°C, max.			
Overvoltage protection	5 V output Auxiliary	6.8 V, typical None			
Short circuit protection	5 V output Auxiliary	Continuous 10 seconds, max.			
INPUT SPECIFICATION	INPUT SPECIFICATIONS				
Input voltage range	12 Vdc 24 Vdc 48 Vdc	9-18 Vdc 18-36 Vdc 36-72 Vdc			
Continuous input overvoltage	ETS300-132 (48 Vdc version)	75 Vdc			
No load input current	12 Vdc 24 Vdc 48 Vdc	120 mA 60 mA 50 mA			
Input filter		Pi network			
Remote ON/OFF Logic compatibility On Off		(See Note 9) CMOS and TTL >1.5 V or open <0.4 V			

EMC CHARACTERISTICS				
Radiated noise Conducted noise	EN55022/11, FCC part 15 EN55022/11, FCC part 15 48 V input models	Class B		
	12 V and 24 V input models	Class A		
GENERAL SPECIFICA	TIONS			
Efficiency		% typical 78%, min.		
Isolation voltage	Input/output	500 Vdc		
Switching frequency	Fixed	150 kHz		
Approvals and standards	EN60950, IEC950 CSA C22.			
Case material	Aluminum w non-condu			
Material flammability		UL94V-0		
Weight	75	g (2.6 oz)		
MTBF	MIL-HDBK-217E 330,	000 hours		
ENVIRONMENTAL SPECIFICATIONS				
Thermal performance		to +70 °C o +100 °C on cooled None		
Relative humidity	Non-condensing 5% to	95% RH		
Altitude		feet max. feet max.		
Vibration	5-500 Hz 2.5 G rms	(approx.)		

International Safety Standard Approvals

TÜV EN60950/IEC950 Reg. File No. 92 05 2584 533

CSA C22.2 No. 950 File No. LR41062C



UL1950 File No. E136005



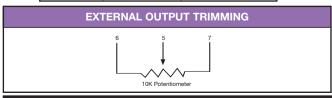
25 Watt Wide input DC-DC converters

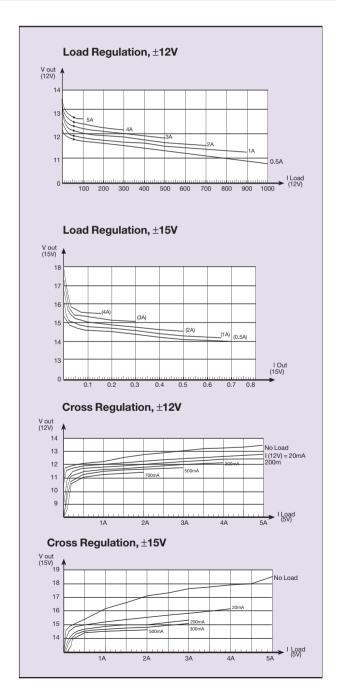
INPUT	OUTPUT	OUTPUT	JTPUT INPUT CURRENT		ATION	(44.40)
VOLTAGE (1)	VOLTAGE	CURRENT (MAX.)	FULL LOAD	LINE (2)	LOAD (3,4)	MODEL NUMBER (11,12)
9-18 Vdc	5/±12 Vdc	5/±1 A	2.75 A	±0.2%/±1.5%	±1.0%/graph	NFC25-12T05-12J
9-18 Vdc ⁽⁵⁾	5/±15 Vdc	5/±0.8 A	2.75 A	±0.2%/±1.5%	±1.0%/graph	NFC25-12T05-15-AJ
18-36 Vdc ⁽⁵⁾	5/±12 Vdc	5/±1 A	1.35 A	±0.2%/±1.5%	±1.0%/graph	NFC25-24T05-12J
18-36 Vdc ⁽⁵⁾	5/±15 Vdc	5/±0.8 A	1.35 A	±0.2%/±1.5%	±1.0%/graph	NFC25-24T05-15J
36-72 Vdc	5/±12 Vdc	5/±1 A	0.68 A	±0.2%/±1.5%	±1.0%/graph	NFC25-48T05-12J
36-72 Vdc	5/±15 Vdc	5/±0.8 A	0.68 A	±0.2%/±1.5%	±1.0%/graph	NFC25-48T05-15J

Notes

- Nominal input voltages are 12 Vdc, 24 Vdc and 48 Vdc. Measured from high line to low line at nominal loads.
- Measured from full load to no load (auxiliary outputs at no load).
- Auxiliary ±12 V load regulation, 15% full load to 100% full load. Initial: 5 V @ 2.1 A, ±12 V @ 0.6 A. Final: $5 \lor @ 2.1 A$, $\pm 12 \lor @ 0.09 A$. Auxiliary $\pm 15 \lor$ load regulation, 20% full load to 100% full load. Initial: $5 \lor @ 2.1 A$, $\pm 15 \lor @ 0.5 A$. Final: 5 V @ 2.1 A, ±15 V @ 0.1 A.
- To order a model with reverse polarity inputs, add the suffix '-A' to the model number, e.g. NFC25-24T05-12-A.
- A minimum load of 1 Amp is required on the 5 V output in order to maintain regulation of the auxiliary outputs.
- A load sharing facility is available, whereby the 25 Watts of power is available to any single output or any combination of outputs. There are, however, two prerequisites: the maximum output current may not be exceeded and the regulation is maintained as per Note 6.
- Nominal load conditions:
- 5 V @ 2.1 A, ±12 V @ 0.6 A and 5 V @ 2.1 A, ±15 V @ 0.5 A. The unit will be damaged by the application of a low impedance voltage source to the remote on/off pin.
- 10 Maximum case temperature must not be exceeded. Derating may be
- extended or restricted depending on available cooling. The 'J' suffix indicates that these parts are Pb-free (RoHS 6/6) compliant. TSE RoHS 5/6 (non Pb-free) compliant versions may be available on special request, please contact your local sales representative for details.
- 12 NOTICE: Some models do not support all options. Please contact your local Artesyn representative or use the on-line model number search tool at http://www.artesyn.com/powergroup/products.htm to find a suitable alternative.

PIN CONNECTIONS				
PIN	STANDARD	PIN OUT		
NUMBER	PIN OUT	OPTION A (5)		
1	- Input	+24V		
2	+ Input	-24V		
3	N/C	N/C		
4	Remote	Remote		
5	Adjust	Adjust		
6	+5V	+5V		
7	Return	Return		
8	+12V or +15V +12V or +15V			
9	-12V or -15V	-12V or -15V		

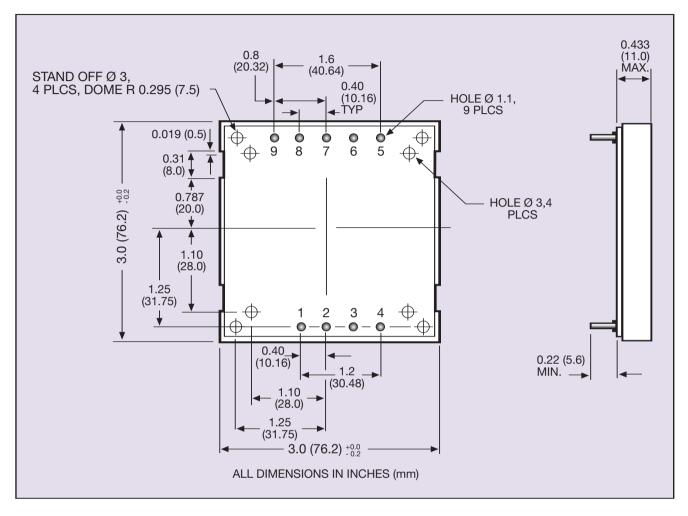


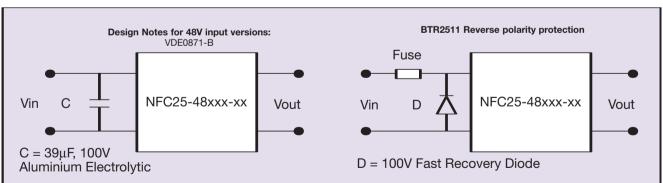




http://www.artesyn.com PAGE 2

25 Watt Wide input DC-DC converters





Datasheet © Artesyn Technologies® 2006

The information and specifications contained in this datasheet are believed to be correct at time of publication. However, Artesyn Technologies accepts no responsibility for consequences arising from printing errors or inaccuracies. Specifications are subject to change without notice. No rights under any patent accompany the sale of any such product(s) or information contained herein.