

# PMEG2005CT

500 mA low  $V_F$  dual MEGA Schottky barrier rectifier

Rev. 01 — 4 June 2009

Product data sheet

## 1. Product profile

### 1.1 General description

Planar Maximum Efficiency General Application (MEGA) Schottky barrier rectifier in common cathode configuration with an integrated guard ring for stress protection, encapsulated in a SOT23 (TO-236AB) small Surface-Mounted Device (SMD) plastic package.

### 1.2 Features

- Average forward current:  $I_{F(AV)} \leq 0.5$  A
- Reverse voltage:  $V_R \leq 20$  V
- Low forward voltage
- AEC-Q101 qualified
- Small SMD plastic package

### 1.3 Applications

- Low voltage rectification
- High efficiency DC-to-DC conversion
- Switch Mode Power Supply (SMPS)
- Reverse polarity protection
- High-speed switching
- Low power consumption applications

### 1.4 Quick reference data

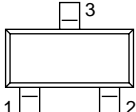
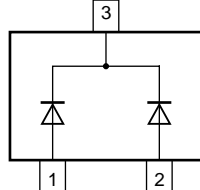
**Table 1. Quick reference data**  
 $T_j = 25^\circ\text{C}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Per diode</b>						
$I_{F(AV)}$	average forward current	square wave; $\delta = 0.5$ ; $f = 20$ kHz				
		$T_{amb} \leq 100^\circ\text{C}$	[1]	-	0.5	A
		$T_{sp} \leq 130^\circ\text{C}$	-	-	0.5	A
$V_R$	reverse voltage		-	-	20	V
$V_F$	forward voltage	$I_F = 0.5$ A	-	360	390	mV
$I_R$	reverse current	$V_R = 20$ V	-	30	200	$\mu\text{A}$

[1] Device mounted on a ceramic Printed-Circuit Board (PCB),  $\text{Al}_2\text{O}_3$ , standard footprint.

## 2. Pinning information

Table 2. Pinning

Pin	Description	Simplified outline	Graphic symbol
1	anode (diode 1)		
2	anode (diode 2)		
3	common cathode		

006aab034

## 3. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PMEG2005CT	-	plastic surface-mounted package; 3 leads	SOT23

## 4. Marking

Table 4. Marking codes

Type number	Marking code <sup>[1]</sup>
PMEG2005CT	P8*

- [1] \* = -: made in Hong Kong  
 \* = p: made in Hong Kong  
 \* = t: made in Malaysia  
 \* = W: made in China

## 5. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
<b>Per diode</b>					
$V_R$	reverse voltage	$T_j = 25\text{ °C}$	-	20	V
$I_{F(AV)}$	average forward current	square wave; $\delta = 0.5$ ; $f = 20\text{ kHz}$			
		$T_{amb} \leq 100\text{ °C}$	<sup>[1]</sup> -	0.5	A
		$T_{sp} \leq 130\text{ °C}$	-	0.5	A
$I_{FRM}$	repetitive peak forward current	$t_p \leq 1\text{ ms}$ ; $\delta \leq 0.25$	-	3.9	A
$I_{FSM}$	non-repetitive peak forward current	square wave; $t_p = 8\text{ ms}$	<sup>[2]</sup> -	10	A

**Table 5. Limiting values ...continued**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
<b>Per device; one diode loaded</b>					
$P_{tot}$	total power dissipation	$T_{amb} \leq 25\text{ °C}$	[3] -	330	mW
			[4] -	400	mW
			[1] -	460	mW
$T_j$	junction temperature		-	150	°C
$T_{amb}$	ambient temperature		-55	+150	°C
$T_{stg}$	storage temperature		-65	+150	°C

[1] Device mounted on a ceramic PCB,  $Al_2O_3$ , standard footprint.[2]  $T_j = 25\text{ °C}$  prior to surge.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[4] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.

## 6. Thermal characteristics

**Table 6. Thermal characteristics**

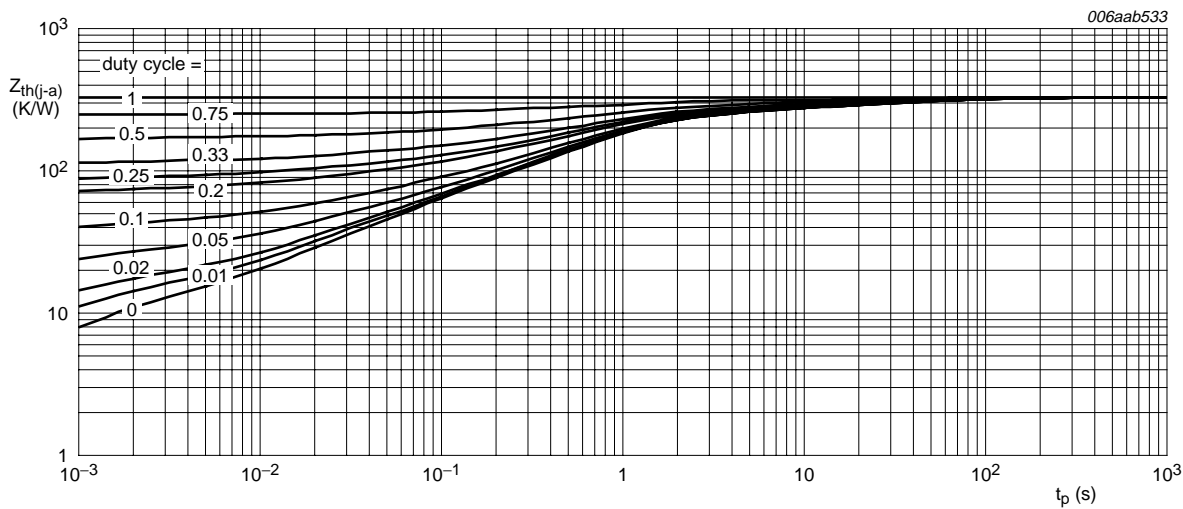
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
<b>Per diode; one diode loaded</b>						
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]			
			[2] -	-	375	K/W
			[3] -	-	310	K/W
			[4] -	-	270	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point		[5] -	-	60	K/W

[1] For Schottky barrier diodes thermal runaway has to be considered, as in some applications the reverse power losses  $P_R$  are a significant part of the total power losses.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

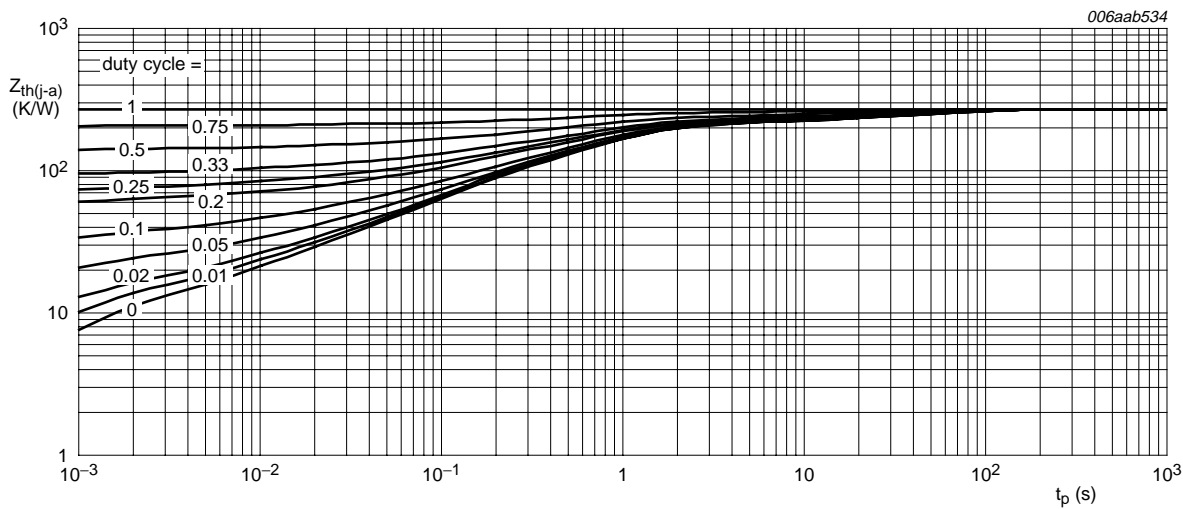
[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for cathode 1 cm<sup>2</sup>.[4] Device mounted on a ceramic PCB,  $Al_2O_3$ , standard footprint.

[5] Soldering point of cathode tab.



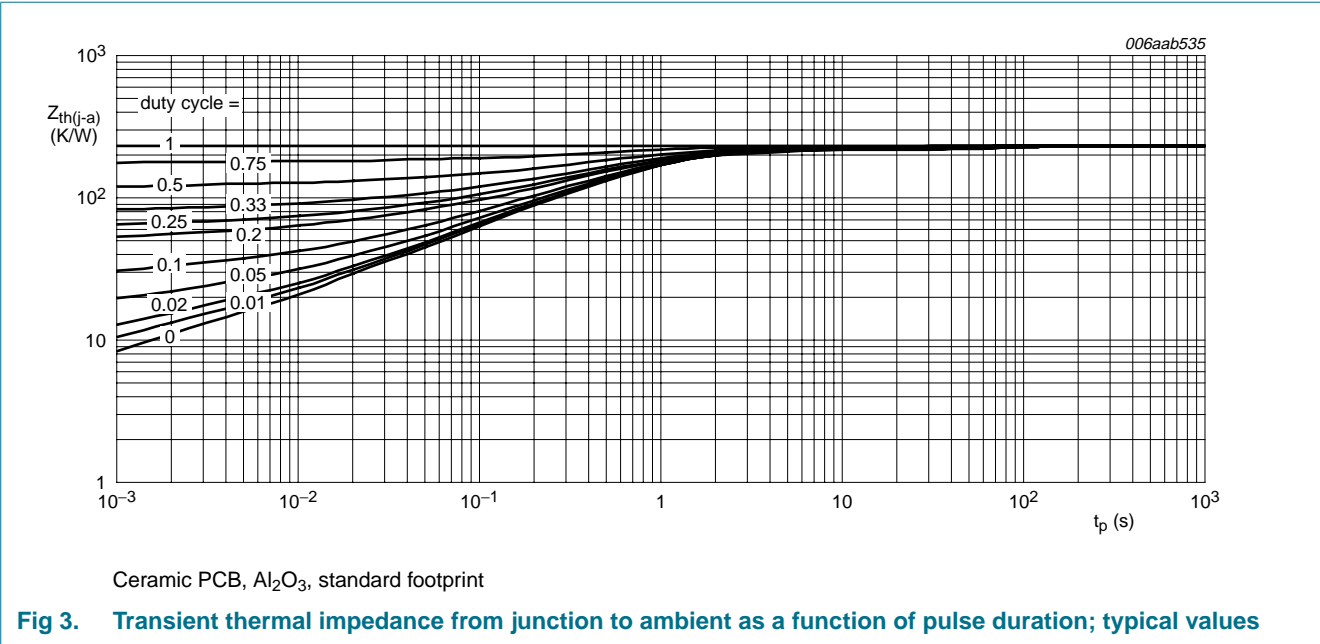
FR4 PCB, standard footprint

Fig 1. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for cathode  $1\text{ cm}^2$

Fig 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



7. Characteristics

Table 7. Characteristics  
 $T_j = 25\text{ }^{\circ}\text{C}$  unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per diode						
$V_F$	forward voltage	$I_F = 0.1\text{ mA}$	-	95	130	mV
		$I_F = 1\text{ mA}$	-	155	190	mV
		$I_F = 10\text{ mA}$	-	215	240	mV
		$I_F = 100\text{ mA}$	-	285	330	mV
		$I_F = 500\text{ mA}$	-	360	390	mV
$I_R$	reverse current	$V_R = 10\text{ V}$	-	11	40	$\mu\text{A}$
		$V_R = 20\text{ V}$	-	30	200	$\mu\text{A}$
$C_d$	diode capacitance	$V_R = 1\text{ V}; f = 1\text{ MHz}$	-	66	80	pF
$t_{rr}$	reverse recovery time	[1]	-	22	-	ns

[1] When switched from  $I_F = 10\text{ mA}$  to  $I_R = 10\text{ mA}$ ;  $R_L = 100\text{ }\Omega$ ; measured at  $I_R = 1\text{ mA}$ .

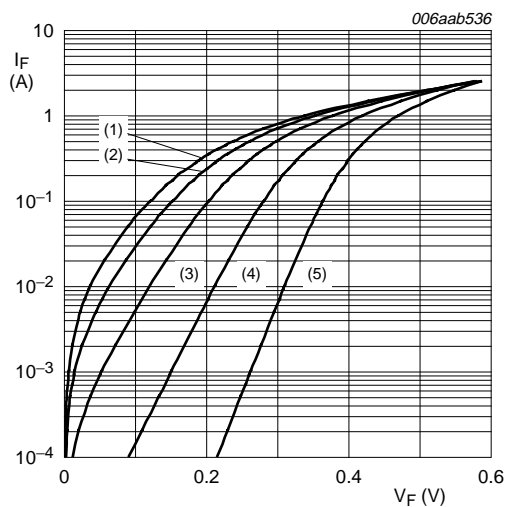


Fig 4. Forward current as a function of forward voltage; typical values

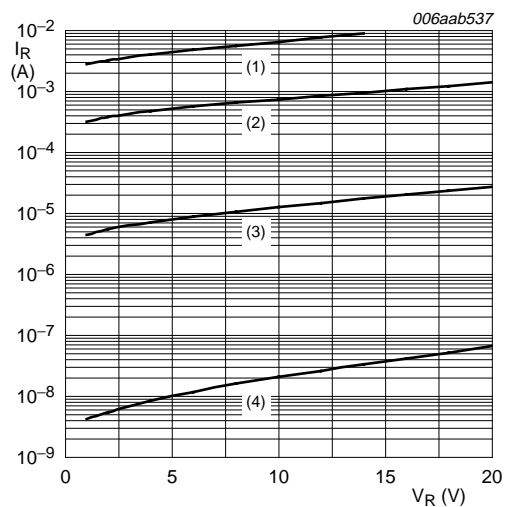
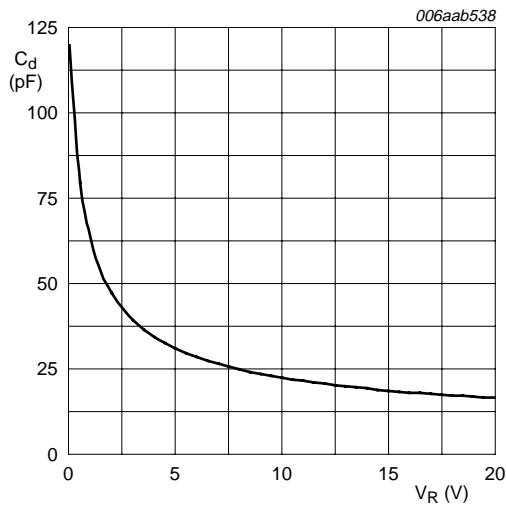
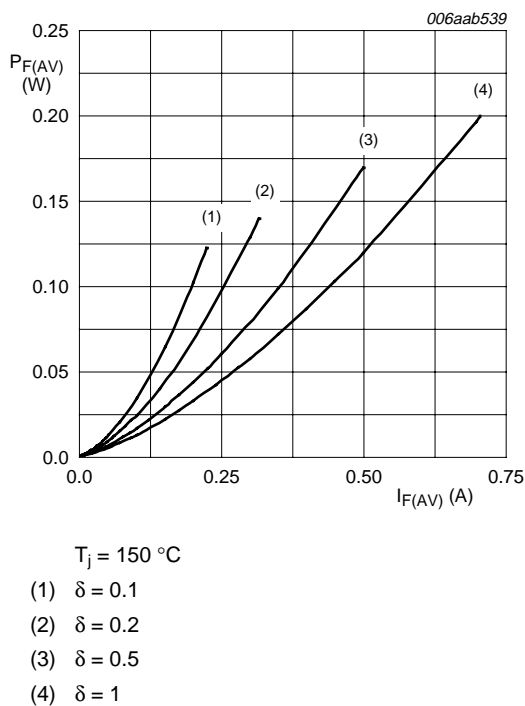


Fig 5. Reverse current as a function of reverse voltage; typical values

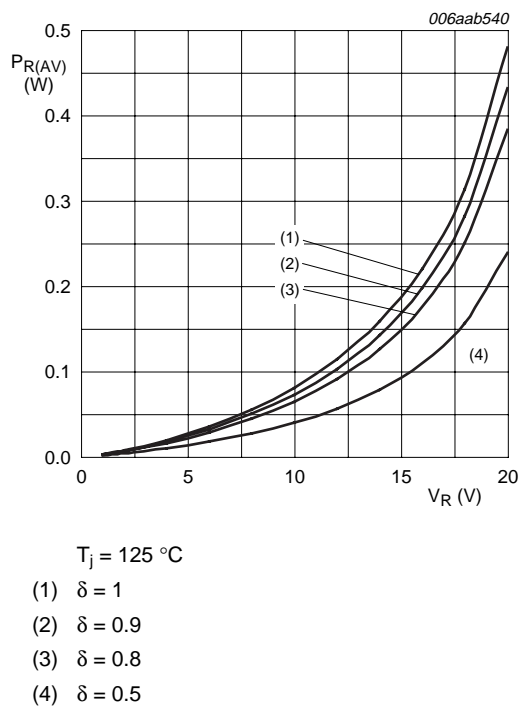


$f = 1\text{ MHz}; T_{amb} = 25\text{ }^{\circ}\text{C}$

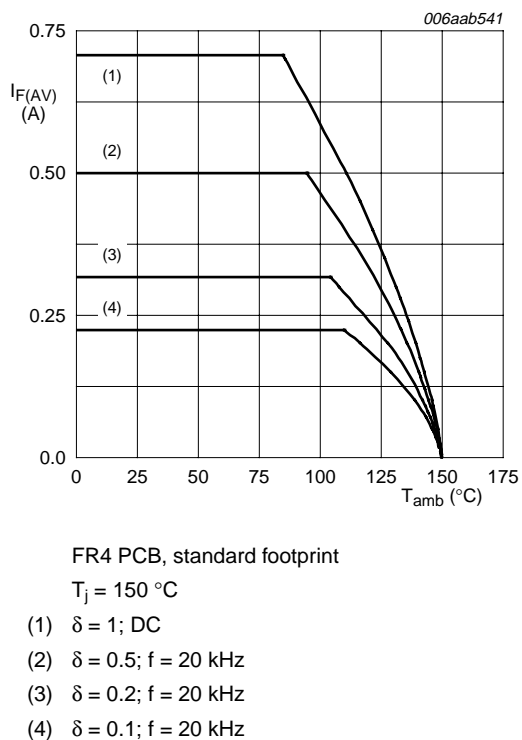
Fig 6. Diode capacitance as a function of reverse voltage; typical values



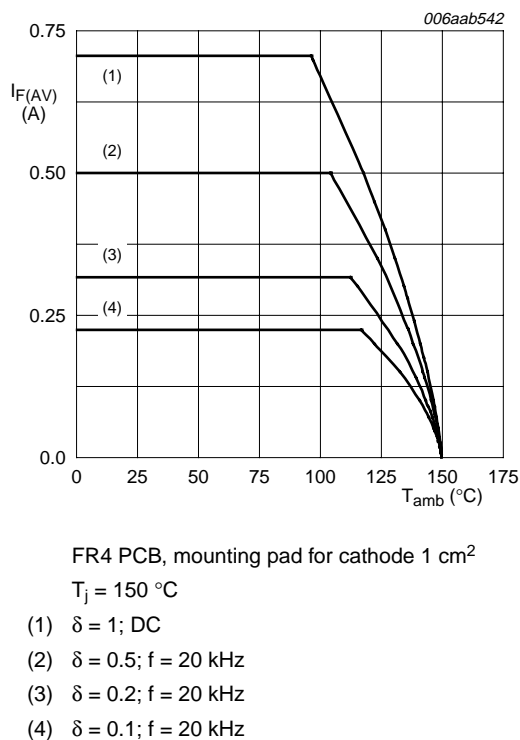
**Fig 7.** Average forward power dissipation as a function of average forward current; typical values



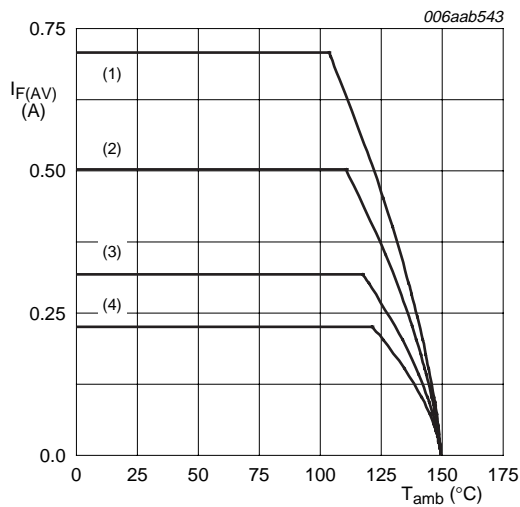
**Fig 8.** Average reverse power dissipation as a function of reverse voltage; typical values



**Fig 9.** Average forward current as a function of ambient temperature; typical values



**Fig 10.** Average forward current as a function of ambient temperature; typical values

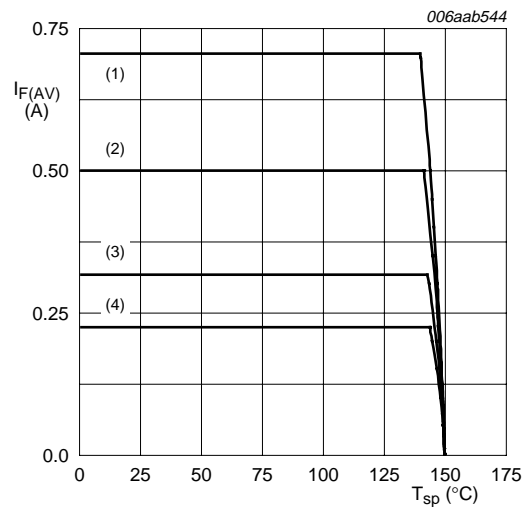


Ceramic PCB,  $Al_2O_3$ , standard footprint

$T_j = 150\text{ °C}$

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ;  $f = 20\text{ kHz}$
- (3)  $\delta = 0.2$ ;  $f = 20\text{ kHz}$
- (4)  $\delta = 0.1$ ;  $f = 20\text{ kHz}$

**Fig 11. Average forward current as a function of ambient temperature; typical values**

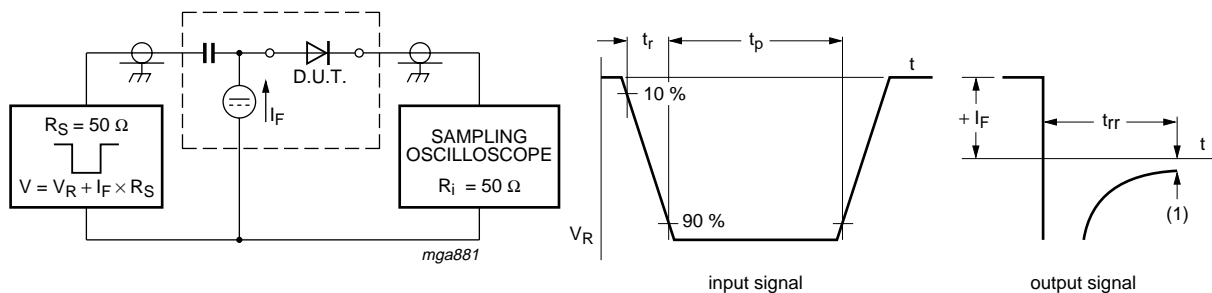


$T_j = 150\text{ °C}$

- (1)  $\delta = 1$ ; DC
- (2)  $\delta = 0.5$ ;  $f = 20\text{ kHz}$
- (3)  $\delta = 0.2$ ;  $f = 20\text{ kHz}$
- (4)  $\delta = 0.1$ ;  $f = 20\text{ kHz}$

**Fig 12. Average forward current as a function of solder point temperature; typical values**

## 8. Test information



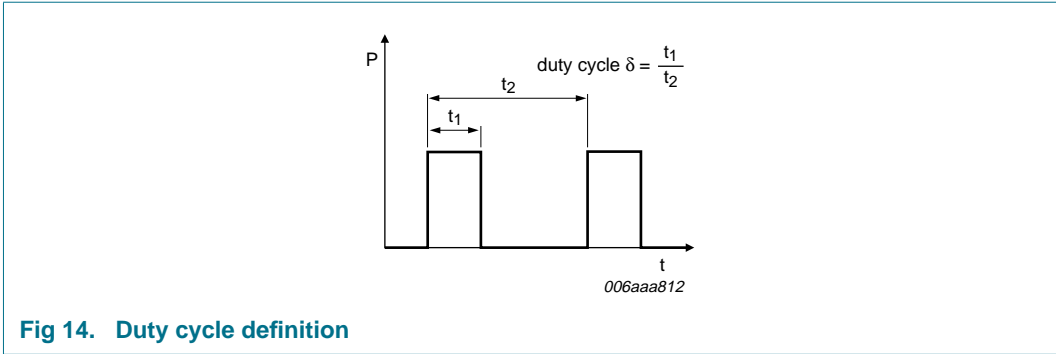
- (1)  $I_R = 1\text{ mA}$

Input signal: reverse pulse rise time  $t_r = 0.6\text{ ns}$ ; reverse voltage pulse duration  $t_p = 100\text{ ns}$ ; duty cycle  $\delta = 0.05$

Oscilloscope: rise time  $t_r = 0.35\text{ ns}$

**Fig 13. Reverse recovery time test circuit and waveforms**



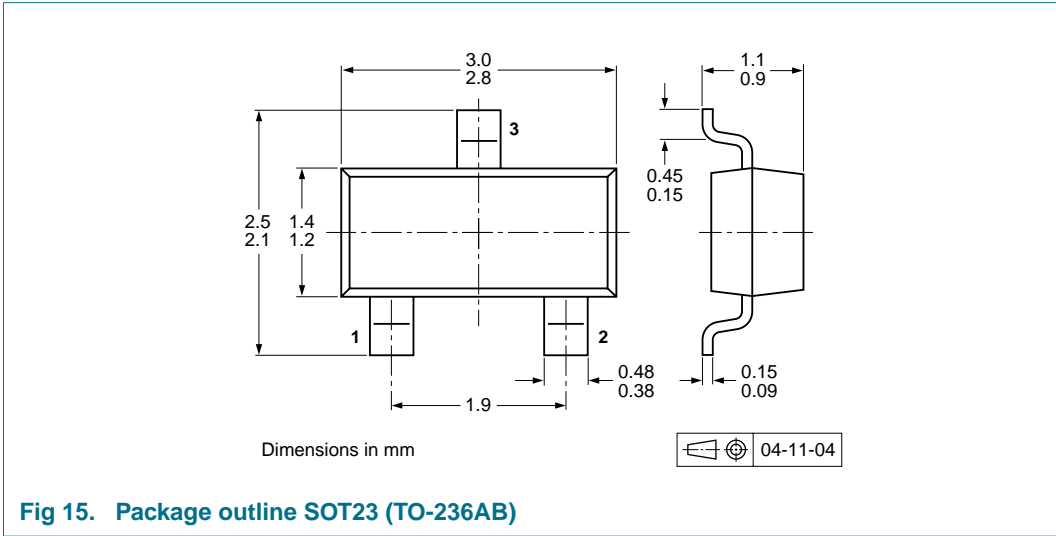


The current ratings for the typical waveforms as shown in [Figure 9](#), [10](#), [11](#) and [12](#) are calculated according to the equations:  $I_{F(AV)} = I_M \times \delta$  with  $I_M$  defined as peak current,  $I_{RMS} = I_{F(AV)}$  at DC, and  $I_{RMS} = I_M \times \sqrt{\delta}$  with  $I_{RMS}$  defined as RMS current.

8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



10. Packing information

Table 8. Packing methods  
The indicated -xxx are the last three digits of the 12NC ordering code.[\[1\]](#)

Type number	Package	Description	Packing quantity	
			3000	10000
PMEG2005CT	SOT23	4 mm pitch, 8 mm tape and reel	-215	-235

[1] For further information and the availability of packing methods, see [Section 14](#).

11. Soldering

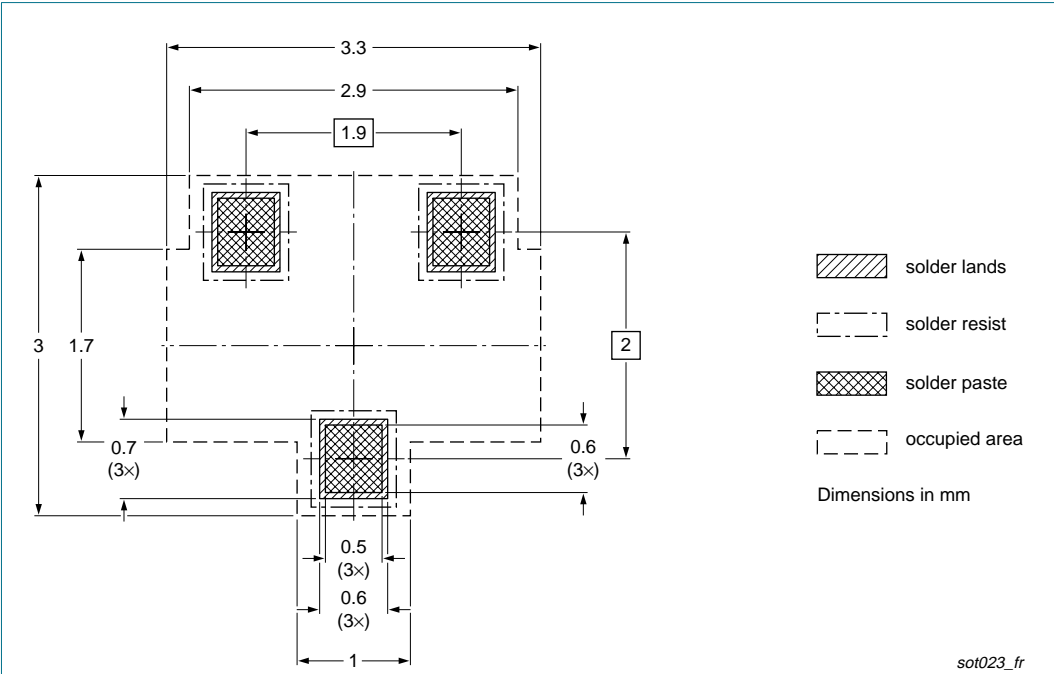


Fig 16. Reflow soldering footprint SOT23 (TO-236AB)

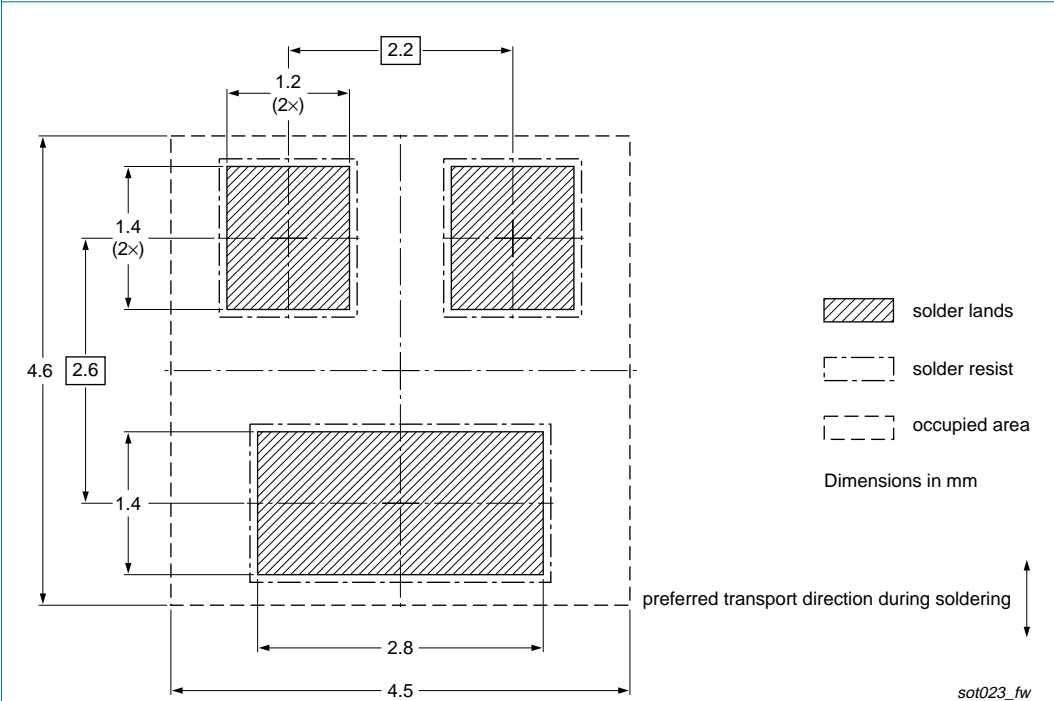


Fig 17. Wave soldering footprint SOT23 (TO-236AB)

12. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
PMEG2005CT_1	20090604	Product data sheet	-	-

## 13. Legal information

### 13.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 13.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

### 13.3 Disclaimers

**General** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in medical, military, aircraft, space or life support equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental

damage. NXP Semiconductors accepts no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) may cause permanent damage to the device. Limiting values are stress ratings only and operation of the device at these or any other conditions above those given in the Characteristics sections of this document is not implied. Exposure to limiting values for extended periods may affect device reliability.

**Terms and conditions of sale** — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, including those pertaining to warranty, intellectual property rights infringement and limitation of liability, unless explicitly otherwise agreed to in writing by NXP Semiconductors. In case of any inconsistency or conflict between information in this document and such terms and conditions, the latter will prevail.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from national authorities.

**Quick reference data** — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

### 13.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## 14. Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

## 15. Contents

<b>1</b>	<b>Product profile</b> .....	<b>1</b>
1.1	General description .....	1
1.2	Features .....	1
1.3	Applications .....	1
1.4	Quick reference data .....	1
<b>2</b>	<b>Pinning information</b> .....	<b>2</b>
<b>3</b>	<b>Ordering information</b> .....	<b>2</b>
<b>4</b>	<b>Marking</b> .....	<b>2</b>
<b>5</b>	<b>Limiting values</b> .....	<b>2</b>
<b>6</b>	<b>Thermal characteristics</b> .....	<b>3</b>
<b>7</b>	<b>Characteristics</b> .....	<b>5</b>
<b>8</b>	<b>Test information</b> .....	<b>8</b>
8.1	Quality information .....	9
<b>9</b>	<b>Package outline</b> .....	<b>9</b>
<b>10</b>	<b>Packing information</b> .....	<b>9</b>
<b>11</b>	<b>Soldering</b> .....	<b>10</b>
<b>12</b>	<b>Revision history</b> .....	<b>11</b>
<b>13</b>	<b>Legal information</b> .....	<b>12</b>
13.1	Data sheet status .....	12
13.2	Definitions .....	12
13.3	Disclaimers .....	12
13.4	Trademarks .....	12
<b>14</b>	<b>Contact information</b> .....	<b>12</b>
<b>15</b>	<b>Contents</b> .....	<b>13</b>

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

founded by

**PHILIPS**

© NXP B.V. 2009.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 4 June 2009

Document identifier: PMEG2005CT\_1