

2SK3228

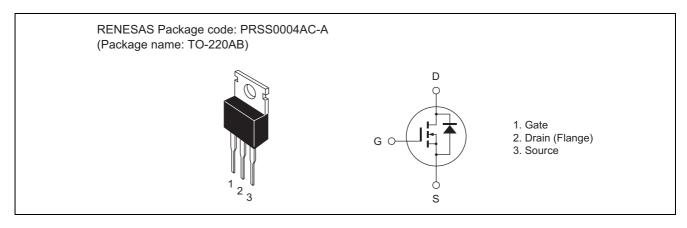
Silicon N Channel MOS FET High Speed Power Switching

REJ03G1094-0400 Rev.4.00 May 15, 2006

Features

- Low on-resistance $R_{DS \; (on)} = 6 \; m\Omega \; typ.$
- Low drive current
- 4 V gate drive device can be driven from 5 V source

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	80	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	75	A
Drain peak current	I _{D (pulse)} Note 1	300	A
Body-drain diode reverse drain current	I _{DR}	75	A
Avalanche current	I _{AP} Note 3	50	A
Avalanche energy	E _{AR} Note 3	181	mJ
Channel dissipation	Pch Note 2	100	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at $Tc = 25^{\circ}C$

3. Value at Tch \leq 25°C, Rg \geq 50 Ω

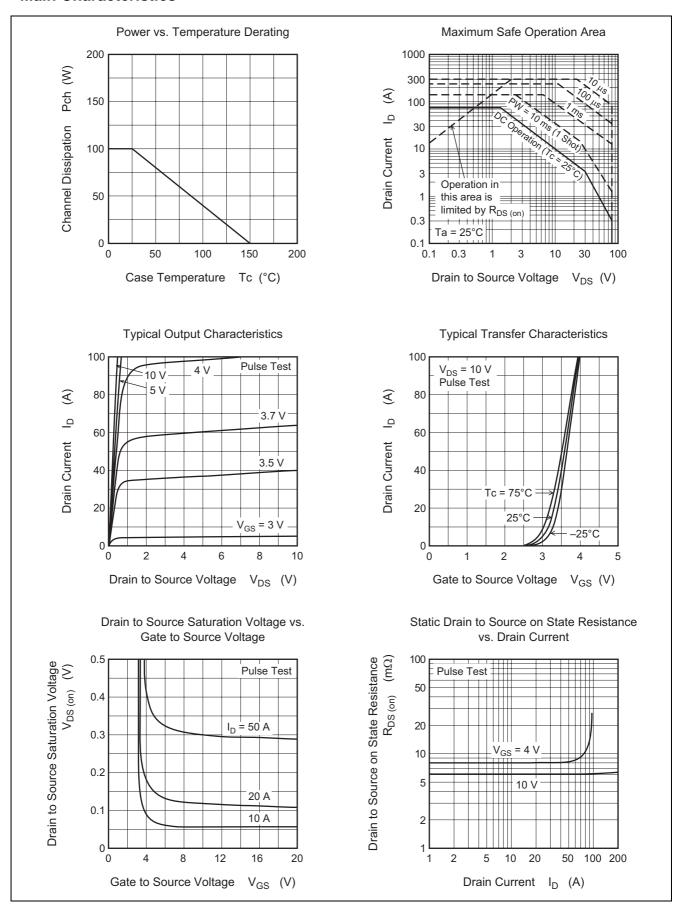
Electrical Characteristics

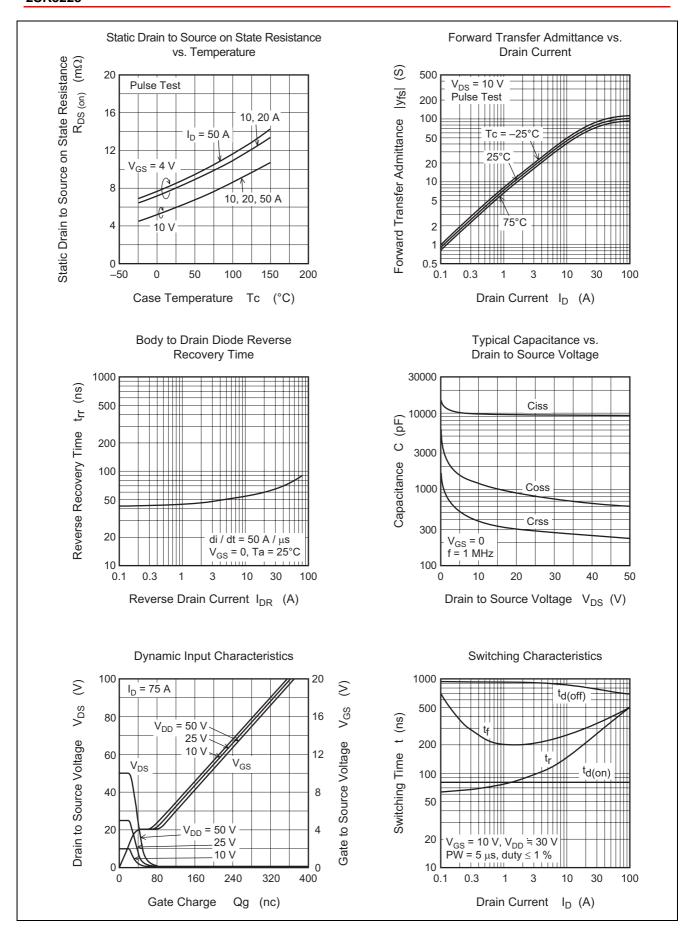
 $(Ta = 25^{\circ}C)$

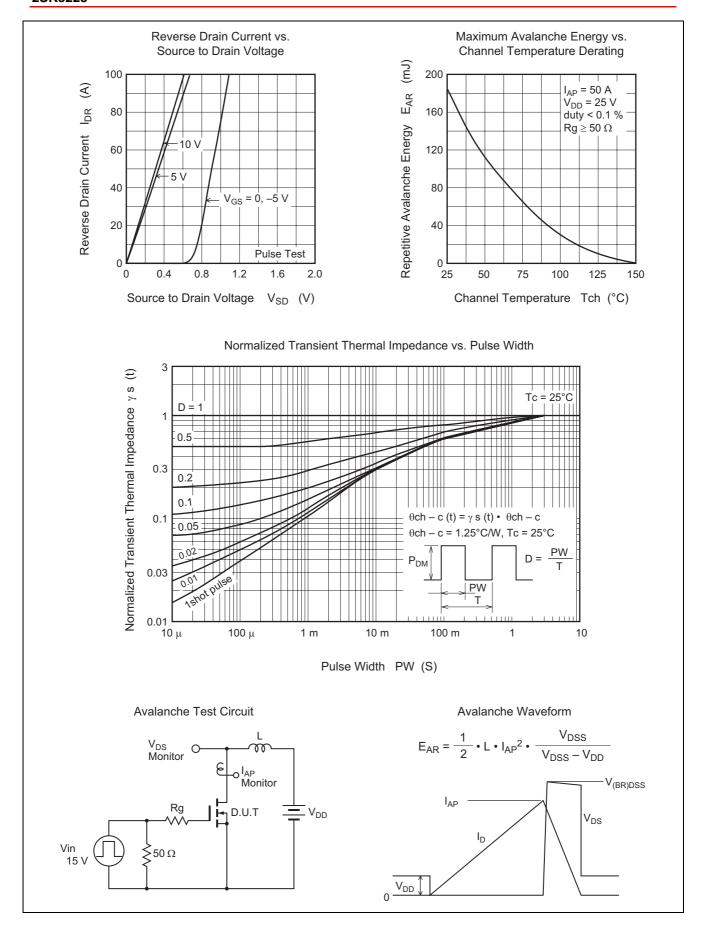
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR) DSS}	80	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	10	μΑ	V _{DS} = 80 V, V _{GS} = 0
Gate to source cutoff voltage	V _{GS (off)}	1.0	_	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static drain to source on state resistance	R _{DS (on)}	_	6.0	7.5	mΩ	$I_D = 40 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note 4}}$
	R _{DS (on)}	_	8.0	12	mΩ	$I_D = 40 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y _{fs}	55	90	_	S	$I_D = 40 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note 4}}$
Input capacitance	Ciss	_	9700	_	pF	I _D = 10 V
Output capacitance	Coss	_	1250	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	290	_	pF	f = 1 MHz
Total gate charge	Qg	_	150	_	nC	V _{DD} = 25 V
Gate to source charge	Qgs	_	30	_	nC	V _{GS} = 25 V
Gate to drain charge	Qgd	_	30	_	nC	I _D = 75 A
Turn-on delay time	t _{d (on)}	_	80	_	ns	I _D = 10 A
Rise time	t _r	_	300	_	ns	V _{GS} = 40 V
Turn-off delay time	t _{d (off)}	_	770	_	ns	$R_L = 0.75 \Omega$
Fall time	t _f	_	370	_	ns	
Body-drain diode forward voltage	V_{DF}	_	1.05	_	V	I _F = 75 A, V _{GS} = 0
Body-drain diode reverse recovery time	t _{rr}	_	90	_	ns	I _F = 75 A, V _{GS} = 0
						di _F /dt = 50 A/μs

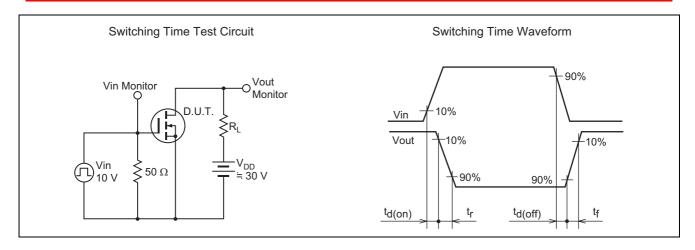
Note: 4. Pulse test

Main Characteristics

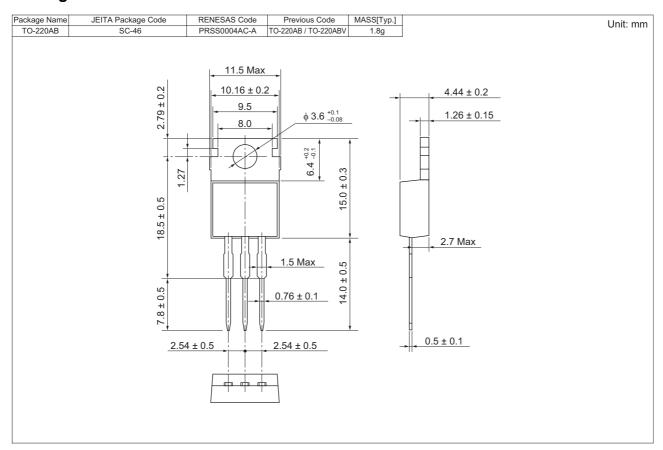








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
2SK3228-E	500 pcs	Box (Sack)

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