

TC74VHCT14AF, TC74VHCT14AFT, TC74VHCT14AFK

Hex Schmitt Inverter

The TC74VHCT14A is an advanced high speed CMOS SCHMITT INVERTER fabricated with silicon gate C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

Pin configuration and function are the same as the TC74VHC04 but the inputs have hysteresis and with its schmitt trigger function, the TC74VHC14 can be used as a line receivers which will receive slow input signals.

The input voltage are compatible with TTL output voltage.

This device may be used as a level converter for interfacing 3.3 to 5 V system.

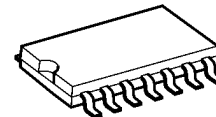
Input protection and output circuit ensure that 0 to 5.5 V can be applied to the input and output ^(Note) pins without regard to the supply voltage. These structure prevents device destruction due to mismatched supply and input/output voltages such as battery back up, hot board insertion, etc.

Note: $V_{CC} = 0\text{ V}$

Features

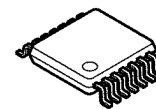
- High speed: $t_{pd} = 5.0\text{ ns}$ (typ.) at $V_{CC} = 5\text{ V}$
- Low power dissipation: $I_{CC} = 2\text{ }\mu\text{A}$ (max) at $T_a = 25^\circ\text{C}$
- Compatible with TTL inputs
: $V_{IL} = 0.5\text{ V}$ (max)
 $V_{IH} = 2.1\text{ V}$ (min)
- Power down protection is provided on all inputs and outputs.
- Balanced propagation delays: $t_{pLH} \approx t_{pHL}$
- Low noise: $V_{OLP} = 0.8\text{ V}$ (max)
- Pin and function compatible with the 74 series (74AC/HC/F/ALS/LS etc.) 14 type.

TC74VHCT14AF



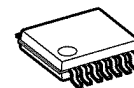
SOP14-P-300-1.27A

TC74VHCT14AFT



TSSOP14-P-0044-0.65A

TC74VHCT14AFK

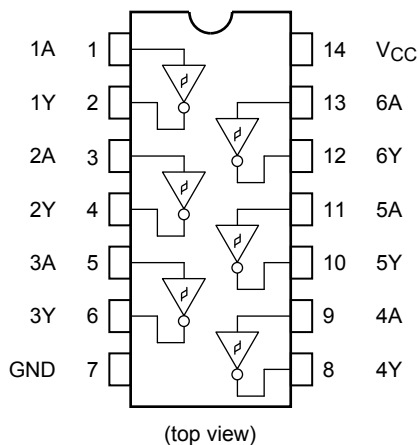


VSSOP14-P-0030-0.50

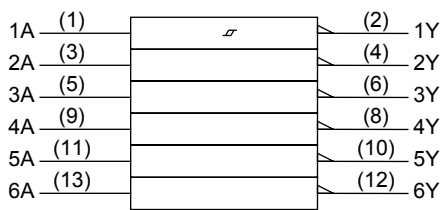
Weight

SOP14-P-300-1.27A:	0.18 g (typ.)
TSSOP14-P-0044-0.65A:	0.06 g (typ.)
VSSOP14-P-0030-0.50:	0.02 g (typ.)

Pin Assignment



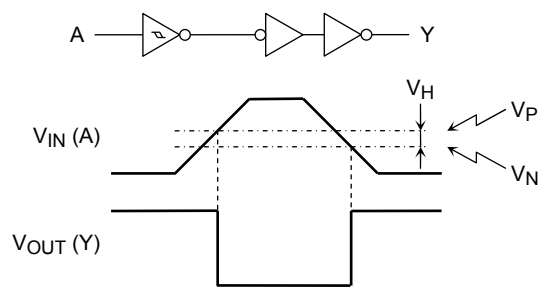
IEC Logic Symbol



Truth Table

A	Y
L	H
H	L

System Diagram, Waveform



Absolute Maximum Ratings (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	-0.5 to 7.0	V
DC input voltage	V_{IN}	-0.5 to 7.0	V
DC output voltage	V_{OUT}	-0.5 to 7.0 (Note 2)	V
		-0.5 to $V_{CC} + 0.5$ (Note 3)	
Input diode current	I_{IK}	-20	mA
Output diode current	I_{OK}	± 20 (Note 4)	mA
DC output current	I_{OUT}	± 25	mA
DC V_{CC} /ground current	I_{CC}	± 50	mA
Power dissipation	P_D	180	mW
Storage temperature	T_{stg}	-65 to 150	°C

Note 1: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 2: $V_{CC} = 0$ V

Note 3: High or low state. I_{OUT} absolute maximum rating must be observed.

Note 4: $V_{OUT} < GND$, $V_{OUT} > V_{CC}$

Operating Ranges (Note 1)

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	4.5 to 5.5	V
Input voltage	V_{IN}	0 to 5.5	V
Output voltage	V_{OUT}	0 to 5.5 (Note 2)	V
		0 to V_{CC} (Note 3)	
Operating temperature	T_{opr}	-40 to 85	°C

Note 1: The operating ranges must be maintained to ensure the normal operation of the device.
Unused inputs must be tied to either V_{CC} or GND.

Note 2: $V_{CC} = 0$ V

Note 3: High or low state

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
				V _{CC} (V)	Min	Typ.	Max	Min	Max
Positive threshold voltage	V _P	—	4.5 5.5	— —	— —	1.90 2.10	— —	1.90 2.10	V
Negative threshold voltage	V _N	—	4.5 5.5	0.50 0.60	— —	— —	0.50 0.60	— —	V
Hysteresis voltage	V _H	—	4.5 5.5	0.40 0.40	— —	1.40 1.50	0.40 0.40	1.40 1.50	V
High-level output voltage	V _{OH}	V _{IN} = V _{IL}	I _{OH} = -50 µA I _{OH} = -8 mA	4.5 4.5	4.4 3.94	4.5 —	— —	4.4 3.80	V
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 50 µA I _{OL} = 8 mA	4.5 4.5	— —	0.0 —	0.1 0.36	— 0.44	V
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND	0 to 5.5	—	—	±0.1	—	±1.0	µA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND	5.5	—	—	2.0	—	20.0	µA
	I _{CCT}	Per input: V _{IN} = 3.4 V Other input: V _{CC} or GND	5.5	—	—	1.35	—	1.50	mA
Output leakage current	I _{OPD}	V _{OUT} = 5.5 V	0	—	—	0.5	—	5.0	µA

AC Characteristics (input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
					Min	Typ.	Max	Min	Max	
Propagation delay time	t _{pLH}	—	5.0 ± 0.5	15	—	5.0	7.6	1.0	9.0	ns
	t _{pHL}	—	5.0 ± 0.5	50	—	6.5	9.6	1.0	11.0	
Input capacitance	C _{IN}	—	—	—	—	4	10	—	10	pF
Power dissipation capacitance	C _{PD}	(Note)	—	—	—	18	—	—	—	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC} / 6 \text{ (per gate)}$$

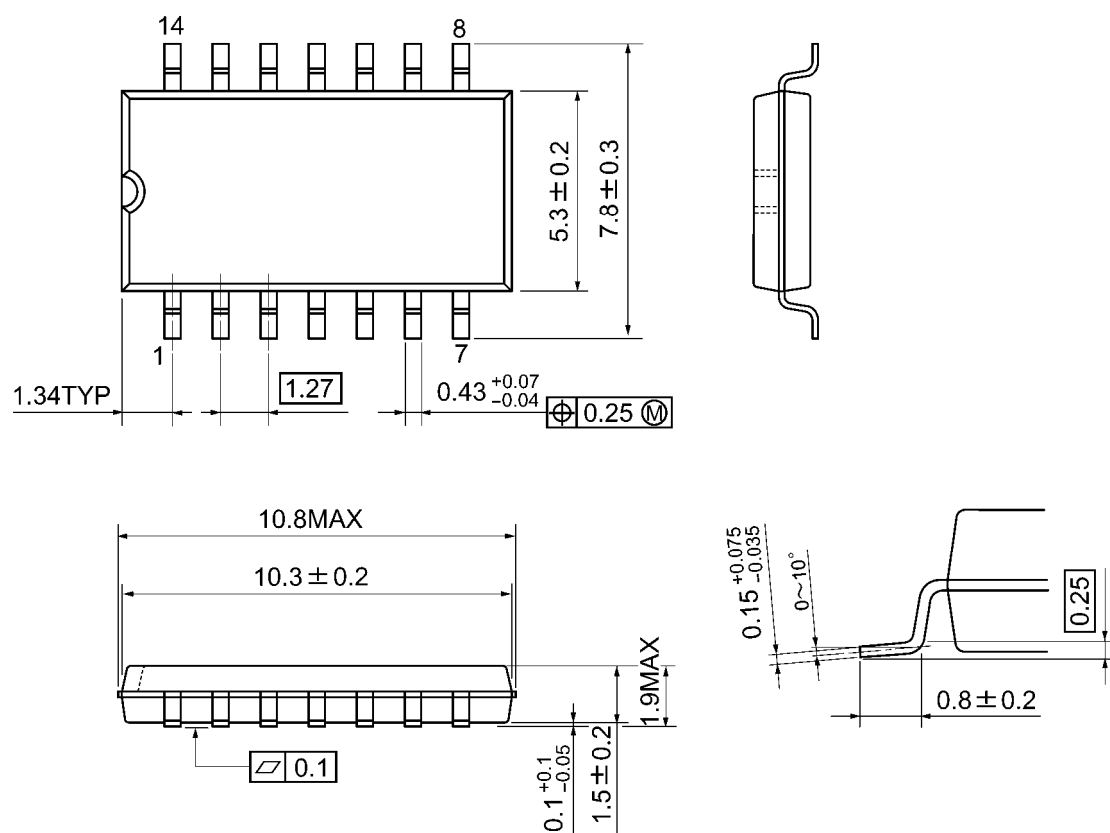
Noise Characteristics (input: t_r = t_f = 3 ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Unit
				V _{CC} (V)	Typ. Limit	
Quiet output maximum dynamic V _{OL}	V _{OLP}	C _L = 50 pF	5.0	0.5	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.5	-0.8	V
Minimum high level dynamic input voltage	V _{IHD}	C _L = 50 pF	5.0	—	2.1	V
Maximum low level dynamic input voltage	V _{ILD}	C _L = 50 pF	5.0	—	0.5	V

Package Dimensions

SOP14-P-300-1.27A

Unit: mm

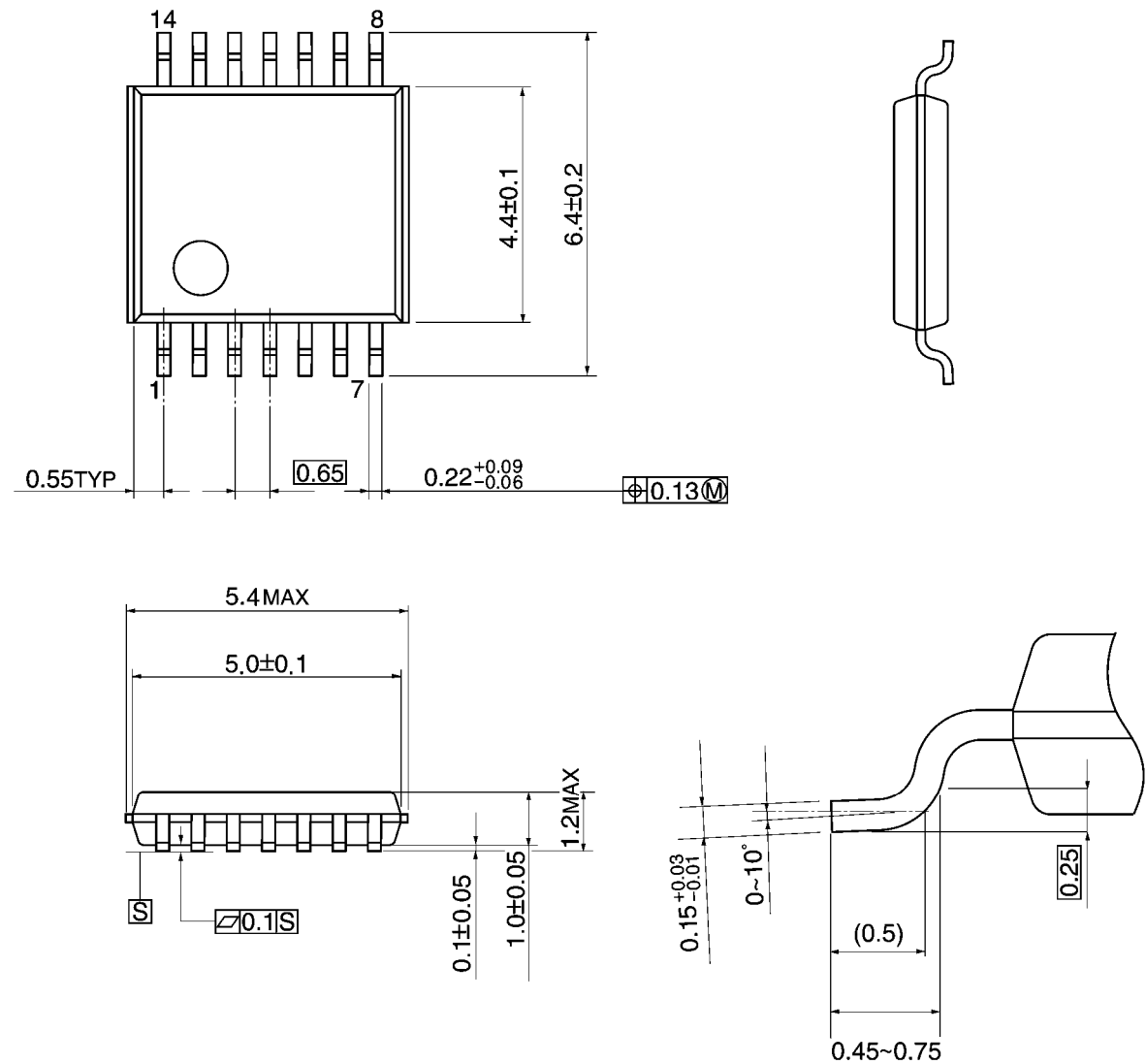


Weight: 0.18 g (typ.)

Package Dimensions

TSSOP14-P-0044-0.65A

Unit: mm

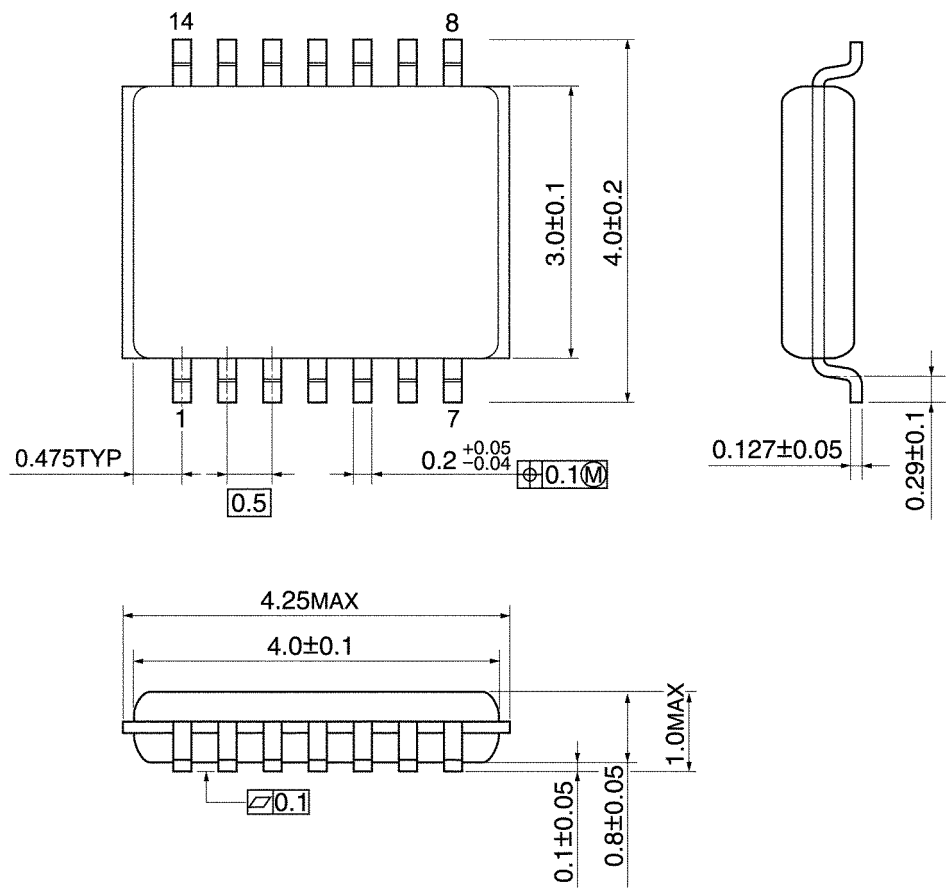


Weight: 0.06 g (typ.)

Package Dimensions

VSSOP14-P-0030-0.50

Unit: mm



Weight: 0.02 g (typ.)

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