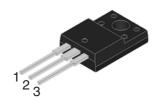
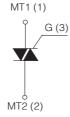


### TO-220F

(FULLY ISOLATED CASE)





#### **On-State Current**

#### **Gate Trigger Current**

12 Amp

 $\leq$  100 mA

**Off-State Voltage** 

400 V ÷ 800 V

#### **FEATURES**

- Glass/passivated die junctions
- Medium current Triac
- Ideal for automated placement
- Low thermal resistance
- High surge current capability
- Low forward voltage drop
- Solder dip 260°C. 10s
- Component in accordance to RoHS 2011/65/EU and WEEE 2002/96/EC
- Meets MSL level 3, per J-STD-020, LF maximum peak of 260° C

#### **MECHANICAL DATA**

- Case: TO-220F. Epoxy meets UL 94V-0 flammability rating.
- Polarity: As marked on the body.
- **Terminals:** Matte tin plated leads, solderable per MIL-STD-750 Method 2026, J-STD-002 and JESD22-B102. Consumer grade, meets JESD 201 class 1A whisker test.

#### TYPICAL APPLICATIONS

Suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, induction motor starting circuits... or for phase control operation in light dimmers, motor speed controllers, ....

## Maximun Ratings and Electrical Characteristics at 25°C

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SYMBOL	PARAMETER	CONDITIONS	Value	Unit	
I <sub>T(RMS)</sub>	RMS On-state Current (full sine wave)	All Conduction Angle, T <sub>c</sub> = 56 °C	12	А	
I <sub>TSM</sub>	Non-repetitive On-State Current	Full Cycle, 60 Hz (t = 16.7 ms)	110	А	
I <sub>TSM</sub>	Non-repetitive On-State Current	Full Cycle, 50 Hz (t = 20 ms)	100	А	
I <sup>2</sup> t	Fusing Current	tp = 10 ms, Half Cycle	50	A <sup>2</sup> s	
I <sub>GM</sub>	Peak Gate Current	20 μs max. Tj = 125 °C	4	А	
P <sub>G(AV)</sub>	Average Gate Power Dissipation	Tj = 125 °C	1	W	
dl/dt	Critical rate of rise of on-state current	$I_G = 2x I_{GT}, t_r \le 100 \text{ns}$	50	A/µs	
		f = 120 Hz, T <sub>j</sub> = 125 °C			
Tj	Operating Temperature		(-40 +125)	°C	
T <sub>stg</sub>	Storage Temperature		(-40 +150)	°C	
T <sub>sld</sub>	Soldering Temperature	10s max	260	°C	
V <sub>iso</sub>	R.M.S. isolation voltage 50/60 Hz sinusoidal waveform		2.500	Vac	

SYMBOL	PARAMETER		Unit		
	711711121211	D	M	N	Oiiit
$V_{DRM}/V_{RRM}$	Repetitive Peak Off State Voltage	400	600	800	V

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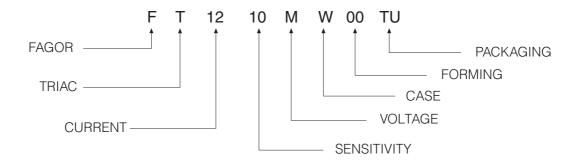


### Electrical Characteristics at Tamb = 25 °C

SYMBOL	PARAMETER	CONDITIONS	Quadrant		SEN	ISITIV	ITY	Unit	
STINIBUL	FARAMETER	CONDITIONS	Quaurani		10	18	17	Ollit	
I <sub>GT</sub> <sup>(1)</sup>	Gate Trigger Current	$V_D = 12 V_{DC}, R_L = 33\Omega, T_j = 25  ^{\circ}C$	Q1÷Q3	MAX	25	25	50	mA	
			Q4	MAX	25	50	100		
V <sub>GT</sub>	Gate Trigger Voltage	$V_D = 12 V_{DC}, R_L = 33\Omega, T_j = 25  ^{\circ}C$	Q1÷Q4	MAX		1.3		V	
$V_{GD}$	Gate Non Trigger Voltage	$V_D = V_{DRM}, R_L = 3.3 \text{ K}\Omega, T_j = 125 \text{ °C}$	Q1÷Q4	MIN		0.2		V	
I <sub>H</sub> <sup>(2)</sup>	Holding Current	$I_T = 100 \text{ mA,Gate open, } T_j = 25 ^{\circ}\text{C}$		MAX	25	25	50	mA	
IL	Latching Current	I <sub>G</sub> = 1.2 I <sub>GT</sub> , T <sub>j</sub> = 25 °C	Q1, Q3, Q4	MAX	40	40	70	mA	
			Q2	MAX	60	80	100		
dV/dt (2)	Critical Rate of Voltage Rise	$V_D = 0.67 \times V_{DRM}$ , Gate open		MIN	200	200	400	V/µs	
		T <sub>j</sub> = 125 °C							
(dV/dt)c <sup>(2)</sup>	Critical Rate of Commutating off-state voltage	$(dI/dt)c = 2.7 \text{ A/ms} T_j = 125 °C$		MIN	3	5	10	V/µs	
V <sub>TM</sub> <sup>(2)</sup>	On-state Voltage	$I_T = 17 \text{ Amp, tp} = 380 \mu\text{s},  T_j = 25 ^{\circ}\text{C}$		MAX		1.55		V	
V <sub>t (o)</sub> (2)	Threshold Voltage	T <sub>j</sub> = 125 °C		MAX		0.85		V	
r <sub>d</sub> <sup>(2)</sup>	Dynamic resistance	T <sub>j</sub> = 125 °C		MAX		35		mΩ	
I <sub>DRM</sub> /I <sub>RRM</sub>	Off-State Leakage Current	$V_D = V_{DRM}$ , $T_j = 125 °C$		MAX		1		mA	
		$V_R = V_{RRM},$ $T_j = 25  ^{\circ}C$		MAX		5		μΑ	
R <sub>th(j-c)</sub>	Thermal Resistance Junction-Case	for AC 360° conduction angle				4		°C/W	
R <sub>th(j-a)</sub>	Thermal Resistance Junction-Ambient					55		°C/W	

<sup>(1)</sup> Minimum  $I_{GT}$  is guaranted at 5% of  $I_{GT}$  max.

### **Part Number Information**



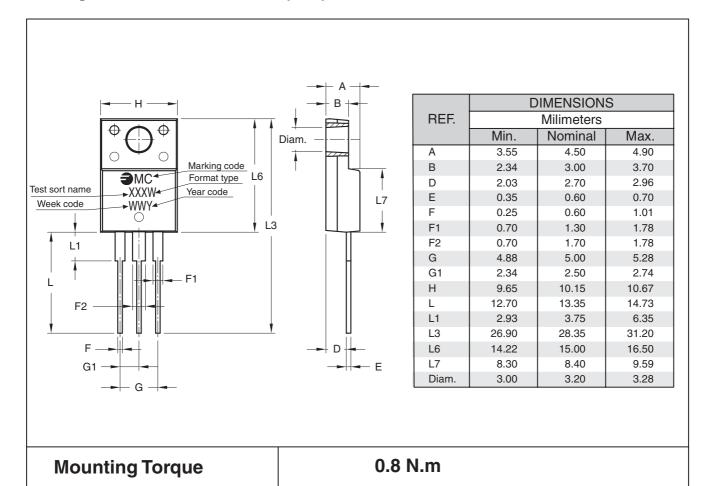
<sup>(2)</sup> For either polarity of electrode MT2 voltage with reference to electrode MT1.



# **Ordering information**

PREFERRED P/N	PREFERRED P/N PACKAGE CODE DELIVERY MODE BASE QUAN		BASE QUANTITY	UNIT WEIGHT (g)
FT1210MW 00TU	FT1210MW 00TU TU TUBE		1,000	2.00

## Package Outline Dimensions: (mm) TO-220F





# Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 1: Maximum power dissipation versus RMS on-state current (full cycle)

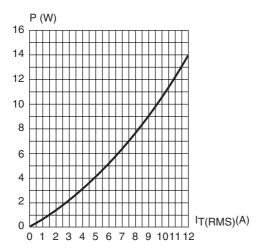


Fig. 3: Relative variation of thermal impedance versus pulse duration.

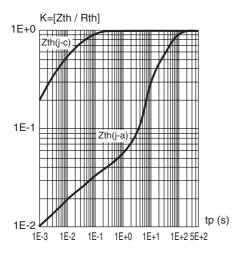


Fig. 5: Surge peak on-state current versus number of cycles

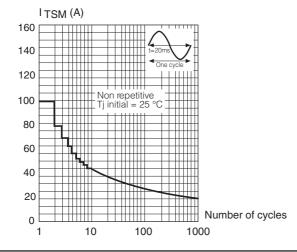


Fig. 2: RMS on-state current versus case temperature (full cycle).

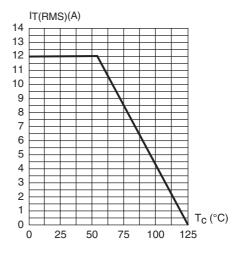


Fig. 4: On-state characteristics (maximum values)

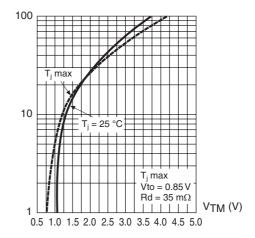
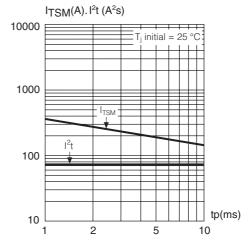


Fig. 6: Non repetitive surge peak on-state current for a sinusoidal pulse with width: tp < 10 ms, and corresponding value of I<sup>2</sup>t.





# Ratings and Characteristics (Ta 25 °C unless otherwise noted)

Fig. 7: Relative variation of gate trigger current, holding current and latching versus junction temperature (typical values)

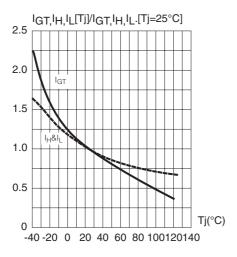


Fig. 9: Relative variation of critical rate of decrease of main current versus

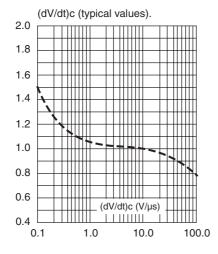
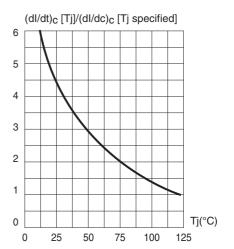


Fig. 8: Relative variation of critical rate of decrease of main current versus junction temperature





## **Revision History**

Date Revision		Description of Changes		
14-Apr-2005	0	Original Data Sheet		
11-Jun-2013	1	Change values of: $I_{T(RMS)}$ / $I_{TSM}$ / $I^2t$ / $V_{t(o)}$ / $r_d$ / $R_{th(j-c)}$ / $R_{th(j-a)}$		

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