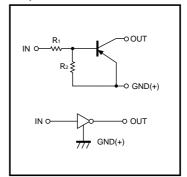
## Digital transistors (built-in resistors)

# DTA124EM / DTA124EE / DTA124EUA / DTA124EKA / DTA124ESA

### Features

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see the equivalent circuit).
- 2) The bias resistors consist of thin-film resistors with complete isolation to allow positive biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 3) Only the on / off conditions need to be set for operation, making device design easy.

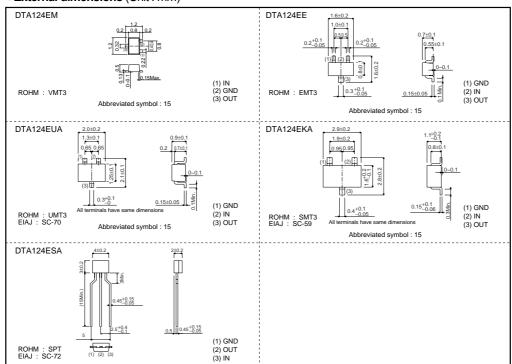
## ●Equivalent circuit



#### Structure

PNP digital transistor (Built-in resistor type)

## ●External dimensions (Unit: mm)



## ●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits(DTA124E □ )					
		М	Е	UA	KA	SA	Unit
Supply voltage	Vcc	-50					٧
Input voltage	Vin	<b>−40~+10</b>					V
Output current	lo	-30					
	IC(Max.)	-100					mA
Power dissipation	Pd	15	50	200		300	mW
Junction temperature	Tj	150					°C
Storage temperature	Tstg	-55~+150					°C

## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions		
lanut valta sa	VI(off)	-	-	-0.5	.,	Vcc=-5V, Io=-100μA		
Input voltage	VI(on)	-3	-	-	V	Vo=-0.2V, Io=-5mA		
Output voltage	Vo(on)	-	-0.1	-0.3	V	Io/I:=-10mA/-0.5mA		
Input current	lı	-	-	-0.36	mA	V=-5V		
Output current	IO(off)	-	-	-0.5	μΑ	Vcc=-50V, Vi=0V		
DC current gain	Gı	56	_	-	-	Vo=-5V, Io=-5mA		
Input resistance	R <sub>1</sub>	15.4	22	28.6	kΩ	_		
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>	0.8	1	1.2	-	_		
Transition frequency	f⊤	_	250	_	MHz	Vce=-10V, Ie=5mA, f=100MHz *		

<sup>\*</sup>Transition frequency of the device

## Packaging specifications

	Package		EMT3	UMT3	SMT3	SPT
Packaging type  Code		Taping	Taping	Taping	Taping	Taping
		T2L	TL	T106	T146	TP
Туре	Basic ordering unit (pieces)	8000	3000	3000	3000	5000
DTA124EM		0				-
DTA124EE		-	0	-	-	-
DTA124EUA		-	-	0	-	-
DTA124EKA		-	-	-	0	_
DTA124ESA		-	-	-		0

### •Electrical characteristic curves

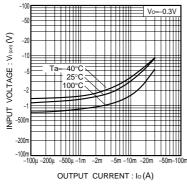


Fig.1 Input voltage vs. output current (ON characteristics)

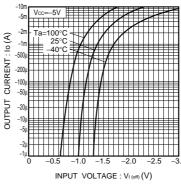


Fig.2 Output current vs. input voltage (OFF characteristics)

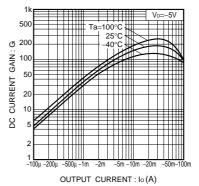


Fig.3 DC current gain vs. output current

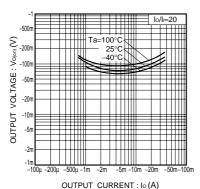


Fig.4 Output voltage vs. output current

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