

NTE1782 Integrated Circuit TV Vertical Deflection Output Circuit

Description:

The NTE1782 is an integrated circuit in a 7-Lead SIP type package designed for use in TV vertical deflection output circuits.

Features:

- High Breakdown Voltage
- Low Power Consumption

Absolute Maximum Ratings: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Supply Voltage, V_{CC}	30V
Circuit Voltage, V_{3-1}	0 to 60V
Circuit Voltage, V_{4-1}	-1V to 6V
Circuit Voltage, V_{5-1}	-1V to 3V
Supply Current, I_{CC}	360mA
Circuit Current, I_2, I_6	-1800 to 1800mA _{O-P}
Power Dissipation, P_D	8W
Operating Ambient Temperature Range, T_{opr}	-20° to +70°C
Storage Temperature Range, T_{stg}	-55° to +150°C
Thermal Resistance, Junction-to-Case, R_{thJC}	4°C/W

Electrical Characteristics: ($T_A = +25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Deflection Current	$I_{y(P-P)}$		1700	1800	1900	mA _{P-P}
Deflection Current Linearity	$\Delta I_{y(+)}$		59	—	175	mA _{P-P}
	$\Delta I_{y(-)}$		54	—	162	mA _{P-P}
Deflection Current Change with Ambient Temperature	$\Delta I_y/T_A$	$T_A = -20^\circ \text{ to } +70^\circ\text{C}$	-1.5	—	+1.5	%
Center Voltage	V_{MID}		13.2	13.8	14.4	V
Flyback Pulse Amplitude	$V_{(FBP)}$		47	—	—	V
Static Circuit Current	I_{CQ}	$V_{3-1} = 24V, V_{7-1} = 24V, V_{5-1} = 0$	7	15	30	mA

Electrical Characteristics (Cont'd): ($T_A = +25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Output Transistor Saturation Voltage	V_{3-2}	$V_{3-1} = V_{7-1} = 24\text{V}$, $\text{Pin}2-1 = 33\Omega$, $V_{4-1} = 0.3\text{V}$, $V_{5-1} = 0$	–	3.0	4.0	V
	V_{2-1}	$V_{3-1} = V_{7-1} = 24\text{V}$, $\text{Pin}2-3 = 33\Omega$, $V_{4-1} = 1.3\text{V}$, $V_{5-1} = 0$	–	1.3	2.0	V
Q_{21} Saturation Voltage	V_{6-1}	$V_{7-1} = 24\text{V}$, $\text{Pin}7-6 = 1.2\text{k}\Omega$, $V_{5-1} = 0$	–	–	0.5	V

Pin Connection Diagram
(Front View)

