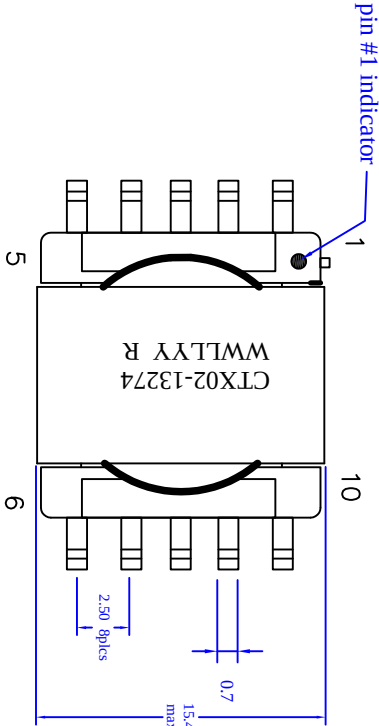
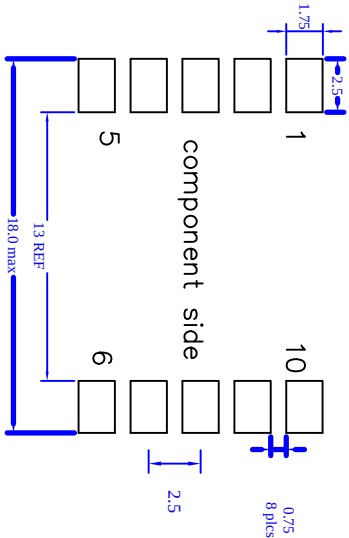


TOP VIEW



FRONT VIEW

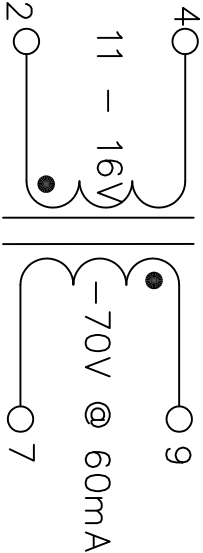
RECOMMENDED PCB LAYOUT



ELECTRICAL CHARACTERISTICS

OCL @ 100kHz, 0.10Vrms & 0.0Adc. Pins (2 - 4). 43.5 - 58.8 uH.
OCL @ 100kHz, 0.10Vrms & 1.0Adc. Pins (2 - 4). 40 uH minimum.
DCR @ 20°C, pins (2 - 4). 0.15 O maximum.
DCR @ 20°C, pins (9 - 7). 7.5 O maximum
Turns ratio; pins (2 - 4):(9 - 7). 1:6.0 +/-2%
Hipot @ 500Vdc for 1 second. Pin 2 to pin 9 (1mA leakage current).

SCHEMATIC



Notes:

- 1) All Dimensions are in millimeters unless otherwise specified.
- 2) Tolerances are +/- 0.25mm unless stated otherwise.
- 3) All soldering surfaces to be coplanar within 0.102 millimeters.
- 4) wlllyy = (Date Code) R= (Revision Level)
- 5) PCB tolerances are +/- 0.1 unless stated otherwise.
- 6) RoHS compliant.

Cooper Bussmann

1225 Broken Sound Parkway NW, Suite F, Boca Raton, Flor

Transformer, Flyback

11 - 60VDC (in): 70V @ 60mA

250KHz, 50uH @ 1.0Adc peak
ER14.5/6, 10 pin, Vertical, SMT

Size Drawing Number: CTX02-13274-R

A Revision Level: E Sheet 3 of 1

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