

LOW NOISE BOARD STACKER FTW SERIES

Mates with:
CLT, SQT, ESQT, TLE,
SMM, MMS, TCSD

For prototype quantities only. Call Samtec for production volume Ferrite shielding.

Features:

- Low or high impedance ceramic insulators on each pin
- Wide range of board spacing achievable
- Through-hole and Surface Mount
- Single and double row

Specifications:

FTW

Insulator Material:

Black LCP and Gray Ferrite

Terminal Material:

Phosphor Bronze

Current Rating:

3A @ 80°C ambient

Operating Temp Range:

-65°C to 125°C with Gold

-65°C to 105°C with Tin

Plating:

Au or Sn over

50µ" (1,27µm) Ni

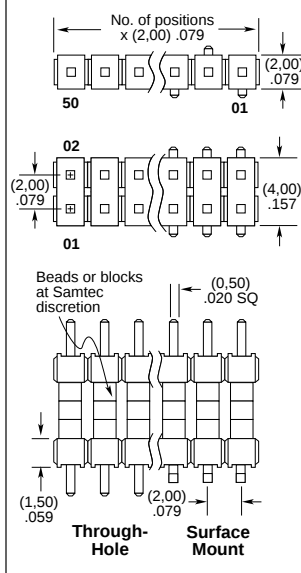
SMT Lead Coplanarity:

(0,15mm) .006" max

Note:

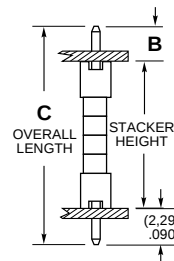
This Series is non-standard, non-returnable.

FTW	NO. PINS PER ROW	LEAD STYLE	PLATING OPTION	ROW OPTION	FREQUENCY OPTION	TAIL OPTION
02 thru 50 =Through-Hole	02 thru 36 =Surface Mount	- G = 20µ" (0,51µm) Gold post area of longer tail Gold flash on tail	- S = 30µ" (0,76µm) Gold post 150µ" (3,81µm) Tin on tail Style -02 not available.	- S = Single Row	- L = Low Frequency (EMI suppression < 30 MHz)	- SM = Surface Mount
		- T = 150µ" (3,81µm) Tin				
APPLICATION SPECIFIC OPTIONS Alignment pins and locking clips available. Call Samtec.						

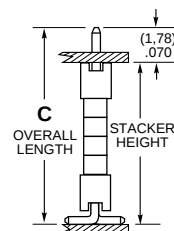


Specify LEAD STYLE from chart

THROUGH-HOLE									
LEAD STYLE	C	B	STACKER HEIGHT	IMPEDANCE					
				FREQ	10MHz	25MHz	100MHz		
-02	(9,58) .377	(2,72) .107	(4,57) .180	LOW HIGH	4.4Ω min —	7.5Ω ±20% 4.4Ω min	— 14Ω ±20%		
-03	(13,60) .535	(2,16) .085	(9,14) .360	LOW HIGH	17.6Ω min —	30Ω ±20% 17.6Ω min	— 56Ω ±20%		
-04	(15,10) .594	(2,13) .084	(10,67) .420	LOW HIGH	22Ω min —	37.5Ω ±20% 22Ω min	— 70Ω ±20%		



SURFACE MOUNT						
LEAD STYLE	C	STACKER HEIGHT	IMPEDANCE			
			FREQ	10MHz	25MHz	100MHz
- 02	(8,08) .318	(6,30) .248	LOW HIGH	4.4Ω min	7.5Ω ±20% 4.4Ω min	— 14Ω ±20%
- 03	(11,13) .438	(9,35) .368	LOW HIGH	13.2Ω min	22.5Ω ±20% 13.2Ω min	— 42Ω ±20%
- 04	(12,65) .498	(10,87) .428	LOW HIGH	17.6Ω min	30Ω ±20% 17.6Ω min	— 56Ω ±20%
-05	(15,70) .618	(13,92) .548	LOW HIGH	26.4Ω min	45Ω ±20% 26.4Ω min	— 84Ω ±20%



SUGGESTED PCB LAYOUTS (Your specific layout may vary)

