TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SH34F,TC7SH34FU

#### NON-INVERT-BUFFER

#### **Features**

• High speed ......  $t_{pd} = 5.0 \text{ ns (typ.)}$ 

at  $V_{CC} = 5 V$ 

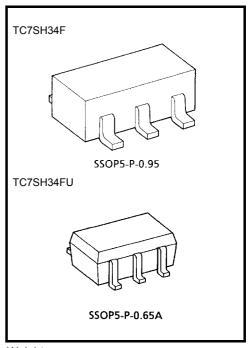
• Low power dissipation ......  $I_{CC}$  = 2  $\mu A$  (max)

at Ta = 25°C

• Compatible with TTL outputs...VIL = 0.8 V (max.)

VIH = 2.0 V (min.)

- 5.5V tolerant input.
- Wide operation voltage range :  $V_{CC}$  (opr) = 2~5.5 V



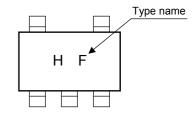
Weight

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

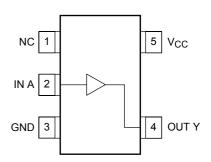
## **Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Supply voltage range	V <sub>CC</sub>	-0.5~7.0	V
DC input voltage	V <sub>IN</sub>	-0.5~7.0	٧
DC output voltage	V <sub>OUT</sub>	-0.5~V <sub>CC</sub> + 0.5	٧
Input diode current	l <sub>IK</sub>	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /ground current	I <sub>CC</sub>	±50	mA
Power dissipation	P <sub>D</sub>	200	mW
Storage temperature	T <sub>stg</sub>	<b>−65~150</b>	°C
Lead temperature (10 s)	TL	260	°C

### Marking



## Pin Assignment (top view)



# Logic Diagram



## **Truth Table**

INPUT	OUTPUT
Α	Υ
L	L
Н	Н

## **Recommended Operating Conditions**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2~5.5	V	
Input voltage	V <sub>IN</sub>	0~5.5	V	
		0~ V <sub>CC</sub>		
Operating temperature	T <sub>opr</sub>	-40~85	°C	
Input rise and fall time	dt/dv	0~100 ( $V_{CC}$ = 3.3 V $\pm$ 0.3 V )	ns/V	
input rise and rail tille	ui/uv	0~20 ( $V_{CC}$ = 5 $V$ $\pm$ 0.5 $V$ )	113/ V	

## **DC Electrical Characteristics**

Characteristics Symbol Test Circuit		Test				Ta = 25°C		Ta = -40~85°C			
		Test Condition		V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit	
High-level input VIH —			_		1.50	_	_	1.50	_	V	
					V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7			
Low-level input					2.0	_	_	0.50	_	0.50	
voltage	V <sub>IL</sub>	_		_	3.0~ 5.5	_	_	V <sub>CC</sub> × 0.3	_	V <sub>CC</sub> × 0.3	V
			VIN = VIH		2.0	1.9	2.0	_	1.9	_	V
				$I_{OH} = -50 \mu A$	3.0	2.9	3.0	_	2.9	_	
High-level output voltage	V <sub>OH</sub>				4.5	4.4	4.5	_	4.4		
				I <sub>OH</sub> = -4 mA	3.0	2.58	_	_	2.48	_	
				$I_{OH} = -8 \text{ mA}$	4.5	3.94		_	3.80	_	
					2.0	_	0.0	0.1	_	0.1	
Low-level output voltage V <sub>OL</sub> —		$I_{OL} = 50~\mu A$	3.0	_	0.0	0.1	_	0.1			
		V <sub>IN</sub> = V <sub>IL</sub>		4.5	_	0.0	0.1	_	0.1	V	
			I <sub>OL</sub> = 4 mA	3.0	_	_	0.36	_	0.44		
				I <sub>OL</sub> = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I <sub>IN</sub>	_	V <sub>IN</sub> = 5.5 V or GND		0~ 5.5	_		±0.1	_	±1.0	μА
Quiescent supply current	Icc	_	V <sub>IN</sub> = V <sub>C</sub>	5.5	_	_	2.0	_	20.0	μА	

## AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$ )

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit	
			V <sub>CC</sub> (V)	C <sub>L (</sub> pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t <sub>PLH</sub>	3.3 ± 0.3 - 5.0 ± 0.5 -	15	_	5.0	7.1	1.0	8.5	- ns	
			50	_	7.5	10.6	1.0	12.0		
			15	_	3.8	5.5	1.0	6.5		
			5.0 ± 0.5	50	_	5.3	7.5	1.0	8.5	
Input capacitance	C <sub>IN</sub>				_	4	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>		(Note)		_	13	_	_	_	pF

Note: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

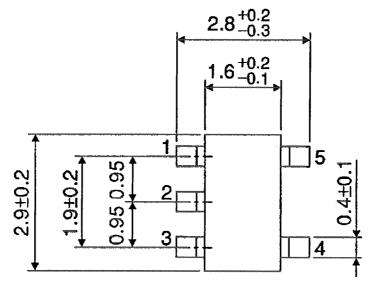
Average operating current can be obtained by the equation:

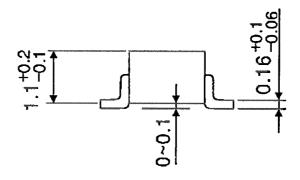
$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

## **Package Dimensions**

**TOSHIBA** 

SSOP5-P-0.95 Unit: mm

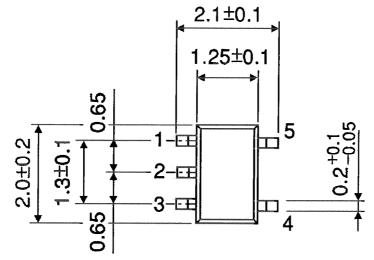


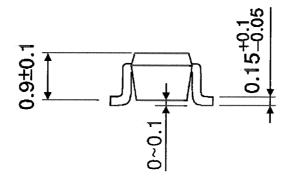


Weight: 0.016 g (typ.)

## **Package Dimensions**

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

5 2004-02-09

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Handbook" etc..

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