DEVELOPMENT TOOLS QuickWorks Software Download System General QL9600 Sockets QuickWorks QuickLogic provides a complete design environment for Field Programmable Gate Array (FPGA) designs. QuickLogic development software helps you increase productivity, shorten design cycles, achieve design performance and power requirements quickly and efficiently. QuickLogic provides tools to guide you from concept to silicon. QuickWorks® supports Windows operating systems, and provides a comprehensive design environment ranging from schematic and HDL-base design entry, HDL language editors and tutorials, logic synthesis, place and route, timing analysis, and simulation support. QuickLogic has partnered with Mentor Graphics and Aldec Inc. to provide industry leading synthesis

DRC and Logic Optimization

Place and Route

Stimulus Generation

Design Synthesis and Optimization

Fix Placement

Window-based placer

Configuration Editor

Timing & Delay Setting

Other Constraints

Physical Viewer

Design Flow

My Verilog design works in behavioral simulation, but I cannot get it to run at speed. What should I do?

When creating a Verilog or VHDL file from a .rom file, why does the RAM/ROM/FIFO Wizard give a

Why can't I fix the pin assignments for some of the pins in my HDL design?

What is the input hysteresis on pASIC 3, QuickRAM and QuickPCI families?

Updated

January

19, 2011

October 4,

February

5,2008

October

13, 2011

Desktop programming solution designed for accelerated programming times.

System General QL9600 Programmer Tool Kit provides a high-speed, portable programming solution for designers using QuickLogic's FPGAs. QL9600 supports the pASIC® 3, QuickRAM®, QuickPC®, Eclipse™, Eclipse Plus, Eclipse II,

Programming Adapter Part Number

Programming Adapter Part Number

QT-SG-PMN121-T

QT-SG-PTN196-T

QT-SG-PFN144-S

QT-SG-PSN256-S

QT-SG-PTN196-S

QT-SG-PUN132-BR-S

QT-SG-PUN132-CR-S

QT-SG-PBN516

QT-SG-PSN484

QT-SG-PTN280

QT-SG-PQ208

QT-SG-PQN208

QT-SG-PUN196-R

QT-SG-PFN144

QT-SG-PFN100

Programming Adapter Part Number

QT-SG-PTN196-R, QT-SG-PTN196

Prototyping Socket

QK-PE-PMN121

QK-PE-PTN196

Prototyping Socket

QK-YI-PF144

QK-ET-PS256

QK-ET-PT196

QK-PE-PUN132

Prototyping Socket

QK-ET-PB516

QK-ET-PS484

QK-ET-PT280

QK-ET-PQ208

QK-TI-PQ208

QK-ET-PT196

QK-ET-PU196

QK-YI-PF144

QK-YI-PF100

Prototyping Socket

QK-YI-PF100

QK-YI-PF144 QK-YI-PQ208

QK-ET-PB256

QK-YI-PB456, QK-ET-PB456

QK-YI-PS484, QK-ET-PS484

•

QK-PE-PUN1

2010

Notes

separately.

versions installed on your system.

Download from www.mentor.com

directory path is C:\pasic\spde\data\license.dat.

QL9600 Software for System General Programmers

This version doesn't include Mentor Graphics Precision Synthesis. Please

download Mentor Graphics Precision Synthesis and Synthesis Library

This patch is applicable only on QuickWorks2010.3.1 Release build.

For EOL devices. Please make sure you don't have any other QuickWorks

License installation instructions:Please copy the license date license file to the data directory of your SpDE installation. The default installation

What is the QuickLogic-recommended rinsing solution for devices?

Constraint Editor

I/O Flip-Flop, RAM Assigmnents

Floorplanning

Select I/O Standards

Timing Reloted Constraints

Placer, Router Constraints

Design Physical Layout View

You are using an outdated browser. Please upgrade your browser to improve your experience.

Documentation

and simulation tools, as well as provide an interface to other industry standard EDA tools. In addition, QuickLogic provides various reference design kits and software development kits to shorten your verification cycles.

Design Entry/Verification

Stimulus Generation Stimulus Generation Stimulus Generation Design Implementation

Timing Driven Place and Route Power Analysis Programming System General

Static Timing Analysis

and Constraint Editor

QuickWorks Synthesis Part Number Included Software QS-QWK-SYN · Schematic Capture Place & Route · Mentor Graphics Precision Synthesis

Frequently Asked Questions

The Message, "VHDL compiler exit on status xxxxxxxx" appeared during source files compilation under Synplify-Lite. How can I find out the problem with the source code? Sometimes, Synplify-Lite reports a mapper failure. The error is not very helpful, since it only says, "Mapper Fails." Is there a "next step" after encountering this error?

syntax error?

Software Download Please read the software license agreement. Version Name Size QuickWorks 2010.4.1 2010.4.1 207 MΒ

QuickWorks 2010.3.1.1

QuickWorks 9.7.4

license.dat for v9.7.4

(Right click the link to

save the file)

QL9600 Software

Patch

How can I contact your sales department?

2010.3.1.1 13.4

9.7.4

1.34a

MB

301.4

MΒ

4.9

MΒ QuickPro 9.4.1 RC3 64 MB IBIS Model Visual Editor/Viewer

System General QL9600

USB connectivity enables shorter download times.

PolarPro®, and ArcticLink® device families.

System General Kit List

Part Number

ArcticLink

System General PMN121 Adapter

System General PTN196 Adapter

System General PFN144 Adapter

System General PSN256 Adapter

System General PTN196 Adapter

System General QL1P100 Adapter

System General PB516 Adapter

System General PS484 Adapter

System General PT280 Adapter

System General PF144 Adapter

System General PV100 Adapter

Part Number

PolarPro

Part Number

QT-SG-T9600-A System General QL9600 Programming Tool Kit including EPD (for 110V power supply) QT-SG-T9600-B System General QL9600 Programming Tool Kit including EPD (for 220V power supply) System General QL9600 Programming Tool Kit including EPD (for 240V power supply) QT-SG-T9600-C QT-SG-T9600-EPD System General EPD

System General Adapter List

System General QL1P300 Adapter Eclipse II, Eclipse, Eclipse Plus Part Number

System General PQ208 Adapter System General PT196 Adapter System General PUN196 Adapter

QuickRAM, pASIC 3, QuickPCI Part Number Programming Adapter Part Number System General PF100 Adapter QT-SG-PFN100 System General PF144 Adapter QT-SG-PFN144

System General PQ208 Adapter QT-SG-PQN208 System General PN256 Adapter QT-SG-PBN256 456 PBGA QA-PB456 QA-PS484 484 BGA

Please contact us for information about prototyping and development sockets (BGA, QFP, and HiLo). Documentation Quick Note ping ModelSim for Simulations with QuickLogic Devices » Corporate » Support » Development Tools

HAVE A QUESTION?

Sockets

If you have questions for our solution experts, please contact us! First Name Last Name Email

Company Your region Your message Contact Us

ABOUT US History

Contact Us

Management Team

Working at QuickLogic **Current Openings** SOCIAL SUPPORT

Customer Login Mature Products Development Tools

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