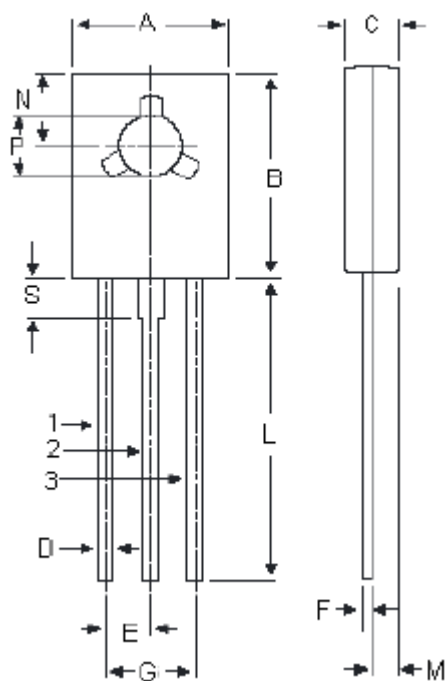


TBD676, 678, 680, 680A, 682

PNP Power Darlington Transistors



TO-126 Plastic Package

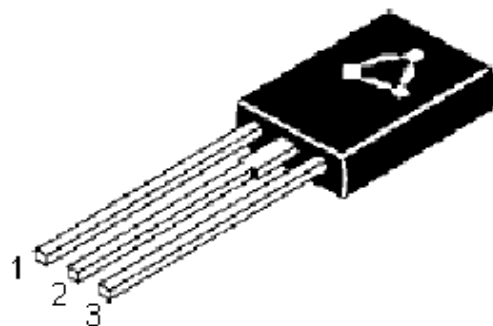


Dimensions	Minimum	Maximum
A	7.4	7.8
B	10.5	10.8
C	2.4	2.7
D	0.7	0.9
E	2.25 (Typical)	
F	0.49	0.75
G	4.5 (Typical)	
L	15.7 (Typical)	
M	1.27 (Typical)	
N	3.75 (Typical)	
P	3.0	3.2
S	2.5 (Typical)	

Dimensions : Millimetres

Pin Configuration:

1. Emitter
2. Collector
3. Base



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Absolute Maximum Ratings

Parameter	Symbol	TBD676	TBD678	TBD680 TBD680A	TBD682	Unit
Collector Base Voltage	V _{CBO}	45	60	80	100	V
Collector Emitter Voltage	V _{CEO}					
Emitter Base Voltage	V _{EBO}	5.0				
Collector Current	I _C	4.0				A
Base Current	I _B	0.1				
Total Power Dissipation at T _a = 25°C Derate above 25°C	P _D	1.25 10				W mW/°C
Total Power Dissipation at T _C = 25°C Derate above 25°C		40 0.32				W W/°C
Operating and Storage Junction Temperature Range	T _j , T _{stg}	-55 to +150				°C
Thermal Resistance						
Junction to Case	R _{th (j-c)}	3.13				°C/W
Junction to Ambient in Free Air	R _{th (j-a)}	100				

Electrical Characteristics ($T_C = 25^\circ\text{C}$ unless specified otherwise)

Parameter	Symbol	Test Condition	Minimum	Maximum	Unit
Collector Emitter Voltage	V_{CEO}^*	$I_C = 50\text{mA}, I_B = 0$ TBD676 TBD678 TBD680/TBD680A TBD682	45 60 80 100	-	V
Collector Cut off Current	I_{CEO} I_{CBO}	$V_{CE} = \text{Half Rated } V_{CEO}, I_B = 0$ $V_{CB} = \text{Rated } V_{CBO}, I_E = 0$	-	500 0.2	μA mA
	I_{CBO}	$V_{CB} = \text{Rated } V_{CBO}, I_E = 0$ $T_C = 100^\circ\text{C}$		2.0	mA
Emitter Cut off Current	I_{EBO}	$V_{EB} = 5\text{V}, I_C = 0$	-	2.0	mA
Collector Emitter Saturation Voltage NON A A	$V_{CE(sat)}^*$	$I_C = 1.5\text{A}, I_B = 6\text{mA}$ $I_C = 2.0\text{A}, I_B = 8\text{mA}$	-	2.5 2.8	V
Base Emitter On Voltage NON A A	$V_{EB(on)}^*$	$I_C = 1.5\text{A}, V_{CE} = 3\text{V}$ $I_C = 2\text{A}, V_{CE} = 3\text{V}$	-	2.5 2.5	
DC Current Gain NON A A	h_{FE}^*	$I_C = 1.5\text{A}, V_{CE} = 3\text{V}$ $I_C = 2\text{A}, V_{CE} = 3\text{V}$	750 750	-	-
Small Signal Current Gain	$ h_{fe} $	$I_C = 1.5\text{A}, V_{CE} = 3\text{V}$ $f = 1\text{MHz}$	1.0	-	-

*Pulse Test : Pulse Width = $\leq 300\mu\text{s}$, Duty Cycle = $\leq 2\%$.



TBD676, 678, 680, 680A, 682



PNP Power Darlington Transistors

Specifications

V _{CEO} maximum (V)	V _{CBO} maximum (V)	I _{C (av)} maximum (A)	h _{FE} minimum	Package	Type	Part Number
45		4	750 at I _C = 1.5A	TO-126	PNP	TBD676
60	TBD678					
80	TBD680					
	TBD680A					
100	TBD682					

TBD676, 678, 680, 680A, 682

PNP Power Darlington Transistors



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