



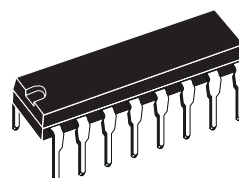
TEF3718 TEF3718S

STEPPER MOTOR DRIVER

- HALF AND FULL-STEP MODES
- BIPOLAR DRIVE OF STEPPER MOTOR FOR MAXIMUM MOTOR PERFORMANCE
- BUILT-IN PROTECTION DIODES
- WIDE RANGE OF CURRENT CONTROL : 5 TO 1500 mA
- WIDE VOLTAGE RANGE : 10 TO 50 V
- DESIGNED FOR UNSTABILIZED MOTOR SUPPLY VOLTAGE
- CURRENT LEVELS CAN BE SELECTED IN STEPS OR VARIED CONTINUOUSLY
- THERMAL OVERLOAD PROTECTION
- ALARM OUTPUT (TEF3718SP) OR PRE-ALARM OUTPUT (TEF3718SSP)

DESCRIPTION

The TEF3718 and TEF3718S are bipolar monolithic integrated circuits intended to control and drive the current in one winding of a bipolar stepper motor. The circuits consists of an LS-TTL - compatible logic input, a current sensor, a monostable and an output stage with built-in protection diodes. Two TEF3718 or TEF3718S and a few external components form a complete control and drive unit for LS-TTL or microprocessor controlled stepper motor systems.

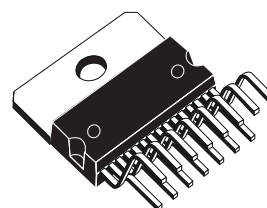


POWERDIP 12 + 2 + 2

ORDER CODE :

TEF3718DP

TEF3718SDP



MULTIWATT 15

ORDER CODE :

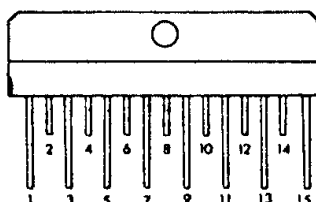
TEF3718SP

TEF3718SSP

PIN CONNECTON

TEF3718

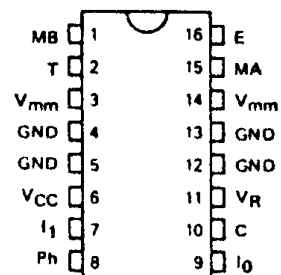
- 1 - V_{CC}
- 2 - I₁
- 3 - Ph
- 4 - I₀
- 5 - C
- 6 - V_{ref}
- 7 - ALARM OUT
- 8 - GND
- 9 - NC
- 10 - V_{mm}
- 11 - T
- 12 - MA
- 13 - E
- 14 - MB
- 15 - V_{mm}



TEF3718S

- 1 - V_{CC}
- 2 - I₁
- 3 - Ph
- 4 - I₀
- 5 - C
- 6 - V_{ref}
- 7 - PRE-ALARM OUT
- 8 - GND
- 9 - NC
- 10 - V_{mm}
- 11 - T
- 12 - MA
- 13 - E
- 14 - MB
- 15 - V_{mm}

TEF3718 TEF3718S



ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
V _{CC} V _{MM}	Supply Voltage	7 50	V
V _I	Input Voltage : Logic Inputs Analog Inputs Reference Input	6 V _{CC} 15	V
I _I	Input Current : Logic Inputs Analog Inputs	10 10	mA
I _O	Output Current	± 1.5	A
T _j	Junction Temperature	+ 150	°C
T _{oper}	Operating Ambient Temperature Range	– 40 to + 85	°C
T _{stg}	Storage Temperature Range	– 55 to 150	°C

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
R _{th(j-c)}	Maximum Junction-case Thermal Resistance POWERDIP MULTIW	11 3	°C/W
R _{th(j-a)}	Maximum Junction-ambient Thermal Resistance POWERDIP MULTIW	45 (*) 40	°C/W

(*) Soldered on a 35 µm thick 20 cm² PC board cooper area.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min.	Typ.	Max.	Unit
V _{CC} V _{MM}	Supply Voltage	4.75 10	5 –	5.25 45	V
I _m	Output Current	0.020	–	1.2	A
T _{amb}	Ambient Temperature	– 40	–	85	°C
t _r	Rise Time Logic Inputs	–	–	2	µs
t _f	Fall Time Logic Inputs	–	–	2	µs

ELECTRICAL CHARACTERISTICS

$V_{CC} = 5\text{ V} \pm 5\%$. $V_{MM} = -10\text{ V}$ to $+45\text{ V}$. $T_{amb} = -40\text{ }^{\circ}\text{C}$ to $+25\text{ }^{\circ}\text{C}$ (Unless otherwise specified)

Symbol	Characteristics	Min.	Typ.	Max.	Unit
I_{CC}	Supply Current			25	mA
V_{IH}	High Level Input Voltage Logic Input	2			V
V_{IL}	Low Level Input Voltage Input			0.7	V
I_{IH}	High Level Input Current Logic Input ($V_I = 2.4\text{ V}$)			20	μA
I_{IL}	Low Level Input Current Logic Input ($V_I = 0.4\text{ V}$)	0.4			μA
V_{CH} V_{CM} V_{CL}	Comparator Threshold Voltage ($V_R = +5\text{ V}$) $I_O = 0\text{ A}$ $I_1 = 0$ $I_O = 1\text{ A}$ $I_1 = 0$ $I_O = 0\text{ A}$ $I_1 = 1$	390 230 65	420 250 80	440 270 90	mV
I_{CO}	Comparator Input Current	— 20		20	μA
I_{off}	Output Leakage Current ($I_O = 1\text{ A}$, $I_1 = 1$)			100	μA
V_{sat}	Total Saturation Voltage Drop ($I_m = 1\text{ A}$,) POWERDIP MULTIWATT			2.9 3.3	V
P_{tot}	Total Power Dissipation ($I_m = 1\text{ A}$, $f_s = 30\text{ kHz}$)		3.1	3.6	W
t_{off}	Cutt off Time (see figures 1 and 2 $V_{mm} = +10\text{ V}$, $V_{ton} \leq 5\text{ }\mu\text{s}$)	25	30	35	μs
t_d	Turn off Delay (see figures 1 and 2, $T_{amb} = +25\text{ }^{\circ}\text{C}$ $dV_C/dt \leq 50\text{ mV}/\mu\text{s}$)		1.6		μs
V_{sat}	Alarm Output Saturation Voltage $I_O = 2\text{ mA}$		0.8		V
I_{ref}	Reference Input Current, $V_R = 5\text{ V}$		0.4	1	mA
V_{sat}	Source Diode Transistor Pair MULTIWATT $I_m = 0.5\text{ A}$ $I_m = 1\text{ A}$			1.35 1.75	mA
V_f	Saturation Voltage POWERDIP $I_m = 0.5\text{ A}$ $I_m = 1\text{ A}$ Diode Forward Voltage $I_f = 0.5\text{ A}$ $I_f = 1\text{ A}$			1.25 1.55 1.5 1.7	
I_{sub}	Substrate Leakage Current $I_f = 1\text{ A}$			10	
V_{sat}	Sink Diode Transistor Pair MULTIWATT $I_m = 0.5\text{ A}$ $I_m = 1\text{ A}$			1.35 1.55	mA
V_f	Saturation Voltage POWERDIP $I_m = 0.5\text{ A}$ $I_m = 1\text{ A}$ Diode Forward Voltage $I_f = 0.5\text{ A}$ $I_f = 1\text{ A}$			1.25 1.35 1.5 1.8	

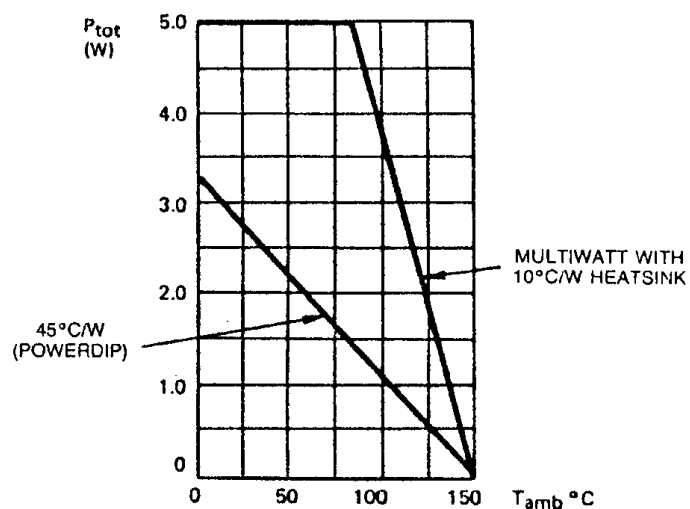
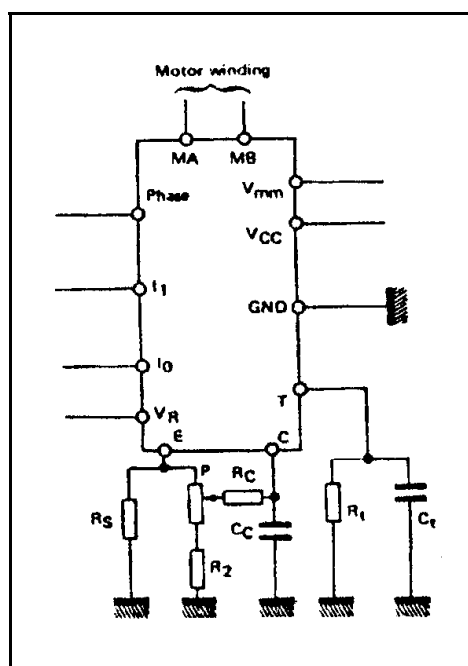
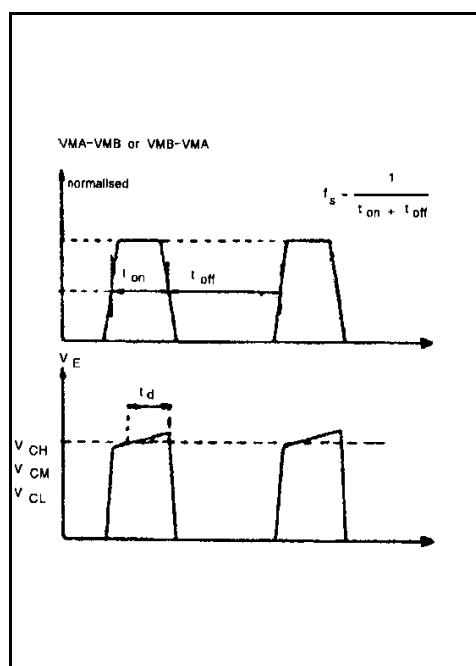


Figure 1.



$R_S = 1\ \Omega$ inductance free
 $R_C = 470\ \Omega$
 $C_C = 820\ \text{pF}$ ceramic
 $R_t = 56\ \text{k}\Omega$
 $C_t = 820\ \text{pF}$ ceramic
 $P = 500\ \Omega$
 $R_2 = 1\ \text{K}$

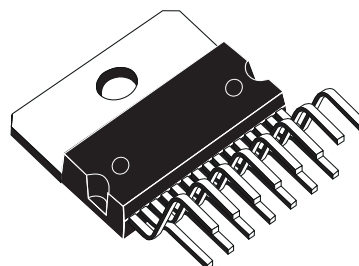
Figure 2.



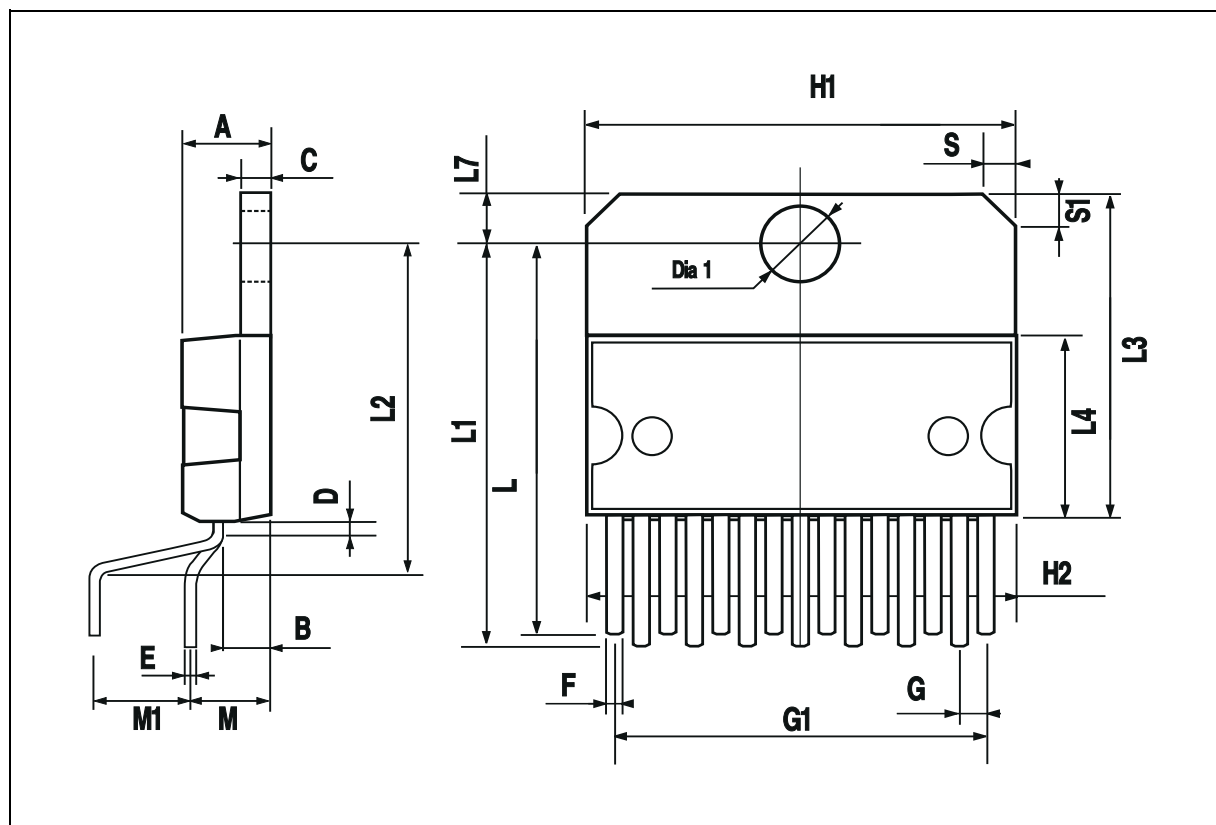
TEF3718-TEF3718S

DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
A			5			0.197
B			2.65			0.104
C			1.6			0.063
D		1			0.039	
E	0.49		0.55	0.019		0.022
F	0.66		0.75	0.026		0.030
G	1.02	1.27	1.52	0.040	0.050	0.060
G1	17.53	17.78	18.03	0.690	0.700	0.710
H1	19.6			0.772		
H2			20.2			0.795
L	21.9	22.2	22.5	0.862	0.874	0.886
L1	21.7	22.1	22.5	0.854	0.870	0.886
L2	17.65		18.1	0.695		0.713
L3	17.25	17.5	17.75	0.679	0.689	0.699
L4	10.3	10.7	10.9	0.406	0.421	0.429
L7	2.65		2.9	0.104		0.114
M	4.25	4.55	4.85	0.167	0.179	0.191
M1	4.63	5.08	5.53	0.182	0.200	0.218
S	1.9		2.6	0.075		0.102
S1	1.9		2.6	0.075		0.102
Dia1	3.65		3.85	0.144		0.152

OUTLINE AND MECHANICAL DATA

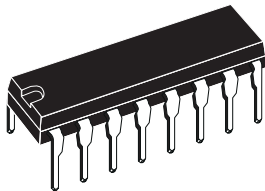


Multiwatt15 V

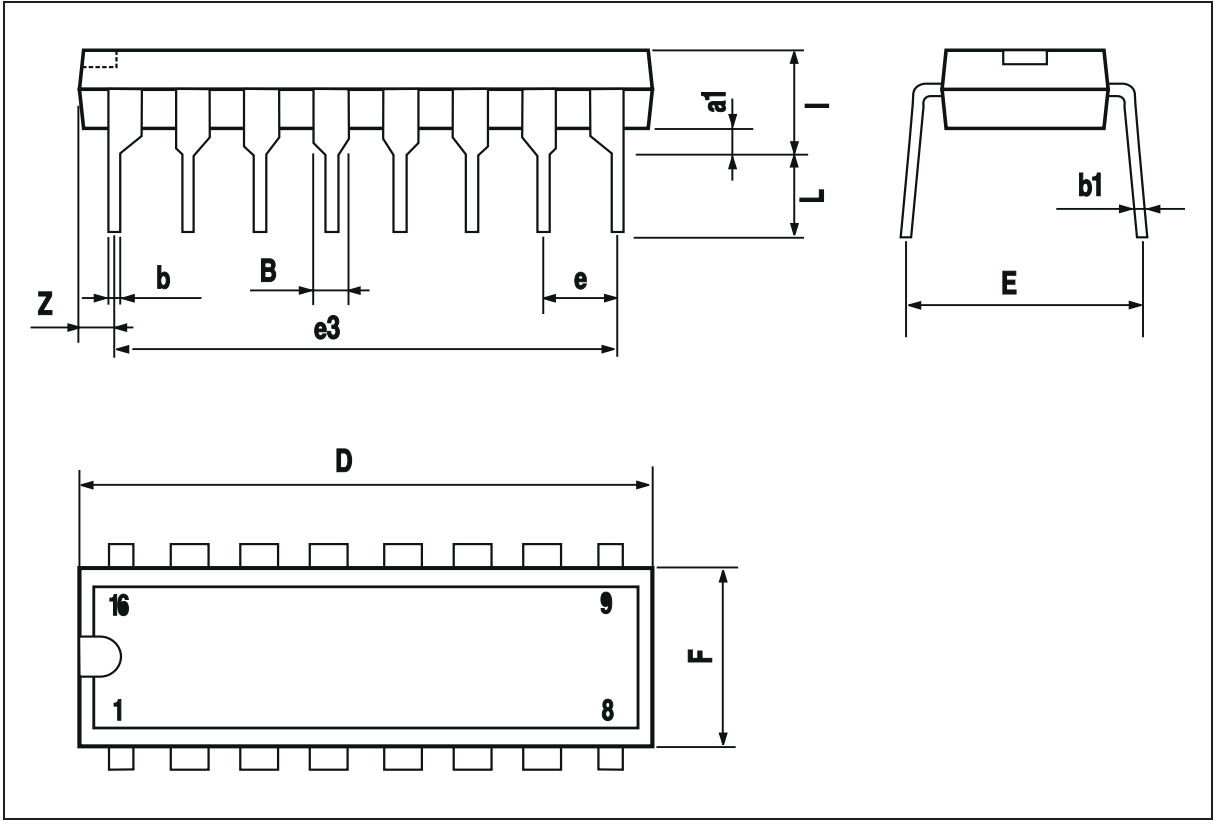


DIM.	mm			inch		
	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
a1	0.51			0.020		
B	0.85		1.40	0.033		0.055
b		0.50			0.020	
b1	0.38		0.50	0.015		0.020
D			20.0			0.787
E		8.80			0.346	
e		2.54			0.100	
e3		17.78			0.700	
F			7.10			0.280
I			5.10			0.201
L		3.30			0.130	
Z			1.27			0.050

OUTLINE AND MECHANICAL DATA



Powerdip 16



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