

REV. Status

PRELIMINARY
02/17/00 MP

HDSL transformer designed for use with Brooktree HDSL IC chips BT8921/BT8970 at 1168Kbps rates

A. Electrical Specifications (@ 25°C)

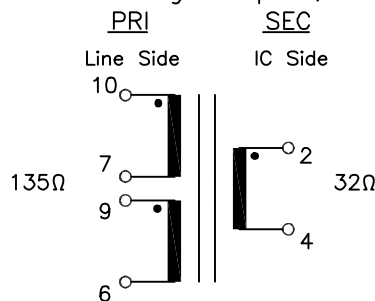
1. Primary (Line side) Impedance; 135Ω
2. Secondary (IC side) Impedance; 32Ω
3. Longitudinal Balance; 50dBm MIN @ 300Hz TO 300KHz, 0dBm (Pin 2 grounded)
4. Frequency Response; ±0.1dB @ 40KHz to 300KHz, 0dBm
5. Insertion Loss; 0.5dB MAX @ 300KHz, 0dBm
6. Total Harmonic Distortion; -70dB MAX @ 40KHz, 14dBm
7. Return Loss; 16.5dB MIN @ 40KHz to 300KHz, 0dBm
8. Primary Open Circuit Inductance; 2.0mH ±6% @ 10KHz, 0.1Vrms Measured (10-6) with 7 and 9 shorted
9. Leakage Inductance; 11μH MAX @ 100KHz, 0.1Vrms Measured (10-6) with 2 & 4, and 7 & 9 shorted
10. DC Current; 160mA
11. DC Resistance; (2-4)=0.785Ω ±10%
(6-9)=1.10Ω ±10%
(7-10)=1.10Ω ±10%
12. Turns Ratio; (10-7):(9-6)=1:1.00±2%
(2-4):(10-6)=1:2.00±2%
7 & 9 shorted

13. Dielectric Strength; 1875Vrms 1 second @ Pri to Sec

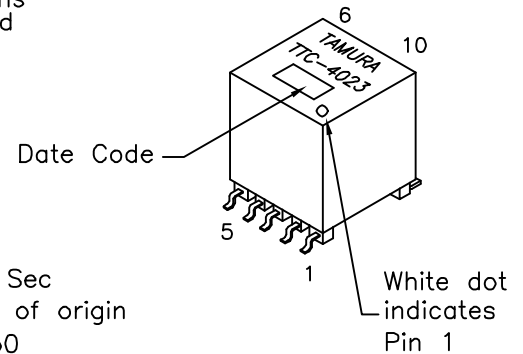
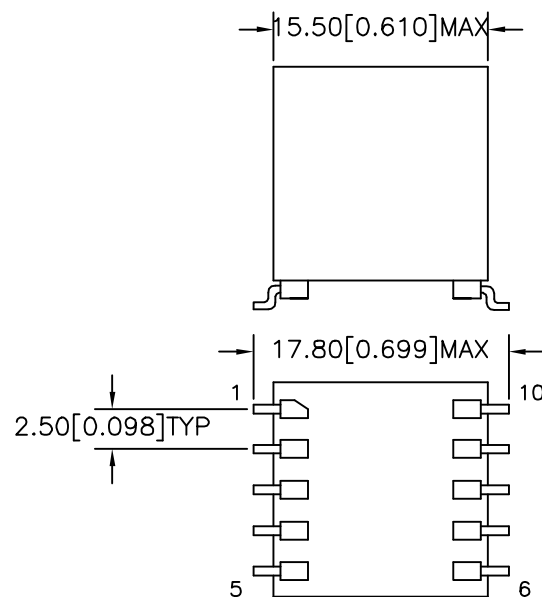
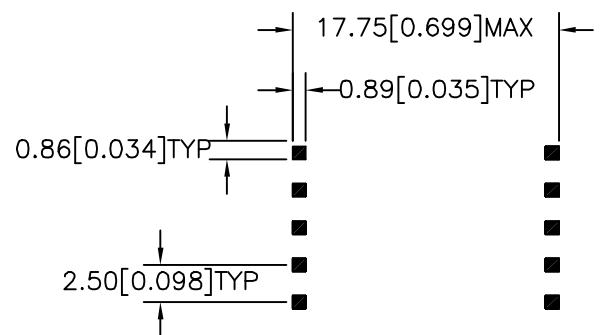
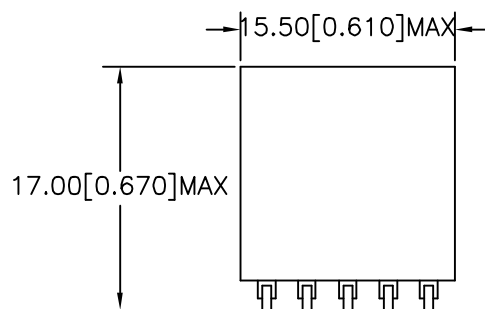
B. Marking; TTC-4023, TAMURA, date code and country of origin

C. Safety; Complies with UL1950 3rd Edition and IEC950

D. Schematic and Mounting Footprint;



E. Mechanical Specifications;



MODEL NUMBER

TTC-4023

PREPARED BY:

D. Rund

ENGINEER:

M. Pitchai

QUALITY CONTROL:

D. Kelley

APPROVED:

J. Coleman

DWG CONTROL NO.

P-A1-12357

ACAD\TTC\A1123571.DWG

REV

PRE

HDSL

TRANSFORMER

TAMURA CORPORATION OF AMERICA43352 BUSINESS PARK DRIVE, TEMECULA, CA. 92590-6624
(909) 699-1270 FAX 9096769482**TTC-4023**

MODEL SPECIFICATION

DIM: mm(in) SCL: 2/1 SH: 1 OF 1

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