



CAN Bus Driver and Receiver

DESCRIPTION

The Si9200EY is designed to interface between the Intel 82526 CAN controller and the physical bus to provide drive capability to the bus and differential receive capability to the controller. It is designed to absorb typical electrical transients on the bus which may occur in an automotive or industrial application, and protect itself against any abnormal bus conditions. The transmitter will be disabled during these conditions and will be re-enabled when the abnormal condition is cleared.

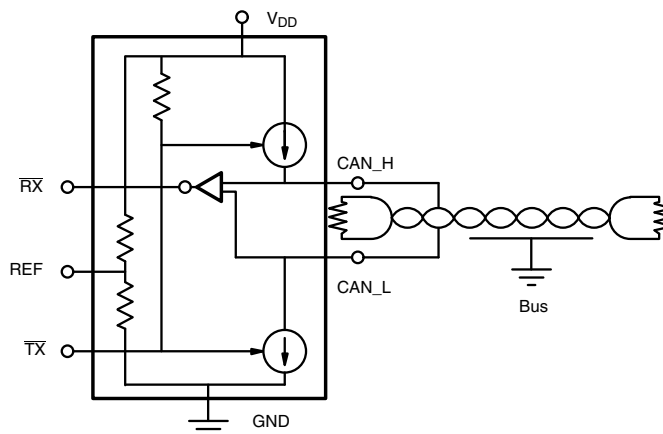
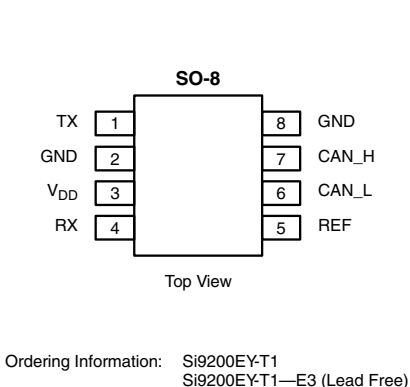
The Si9200EY is built using the Siliconix BiC/DMOS process. This process supports CMOS, DMOS, and isolated bipolar transistors and uses an epitaxial layer to prevent latchup. The bus line pins are diode protected and can be driven beyond the V_{DD} to ground range.

The Si9200EY is offered in the space efficient 8-pin high-density surface-mount plastic package and is specified over the automotive temperature range (- 40 °C to 125 °C). The Si9200EY is available in lead free.

FEATURES

- Survives Ground Shorts and Transients on Multiplexed Bus in Automotive and Industrial Applications
- Single Power Supply
- Compatible with Intel 82526 CAN Controller
- Direct Interface - No External Components Required
- Automotive Temperature Range (- 40 °C to 125 °C)

PIN CONFIGURATION AND FUNCTIONAL BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS^a

Parameter	Limit	Unit
Operating Temperature (T _A)	- 40 to 125	°C
Junction and Storage Temperature	- 55 to 150	
Voltage On Any Pin (except CAN_H and CAN_L) with Respect to Ground	- 0.3 to V _{DD} + 0.3	V
Voltage On CAN_H and CAN_L with Respect to Ground	- 3 to 16	
Supply Voltage, V _{DD}	- 0.3 to 12	
Continuous Output Current	± 100	mA
Thermal Ratings ^b : R _{thJA}	62.5 (no airflow)	°C

Notes:

a. Extended exposure to the absolute maximum ratings or stresses beyond these ratings may affect device reliability or may cause permanent damage to the device. Functional operation at conditions other than the recommended operating conditions is not implied.

b. Mounted on 1-IN², FR4 PC Board.

RECOMMENDED OPERATING RANGE

Parameter	Limit	Unit
V _{DD}	4.75 to 5.25	V
Bus Load Resistance	60	Ω

SPECIFICATIONS

Parameter	Symbol	Test Conditions Unless Otherwise Specified V _{DD} = 4.75 V to 5.25 V	Limits T _A = - 40 V to 125 °C			Unit	
			Min. ^b	Typ. ^c	Max. ^b		
Input							
$\overline{\text{TX}}$ Input Voltage High	V _{INH}		4			V	
$\overline{\text{TX}}$ Input Voltage Low	V _{INL}				1		
$\overline{\text{TX}}$ Input Current Low	I _{IL}	$\overline{\text{TX}} = 0 \text{ V}$	- 50		- 2	μA	
$\overline{\text{TX}}$ Input Current High	I _{IH}	$\overline{\text{TX}} = \text{V}_{\text{DD}}$	- 1		1		
Output							
Bus Recessive	V _{CAN_HR} , V _{CAN_LR}	$\overline{\text{TX}} = \text{V}_{\text{INH}}$, R _L = ∞	2	2.5	3	V	
	V _{DIF} = V _{CAN_HR} - V _{CAN_LR}		- 0.5	0	0.05		
Bus Dominant	V _{CAN_HD}	$\overline{\text{TX}} = \text{V}_{\text{INL}}$, R _L = 60 Ω	2.75	3.5	4.5		
	V _{CAN_LD}		0.5	1.5	2.25		
	V _{DIF} = V _{CAN_HD} - V _{CAN_LD}		1.5	2	3		
Reference Output	V _{REF}	- 25 μA ≤ I _{REF} ≤ 25 μA	0.5 V _{DD} - 0.2	0.5 V _{DD}	0.5 V _{DD} + 0.2		
Receive Output (Bus Recessive Conditions)	V _{RXH}	$\overline{\text{TX}} = \text{V}_{\text{INH}}$ - 2 ≤ V _{CAN_H} , V _{CAN_L} ≤ 7 - 1 ≤ V _{CAN_H} - V _{CAN_L} ≤ 0.5 (Bus Recessive)	I _{OUT} = - 10 μA	V _{DD} - 0.3	V _{DD} - 0.05		
			I _{OUT} = - 100 μA	V _{DD} - 1	V _{DD} - 0.2		
			I _{OUT} = - 2 mA	V _{DD} - 1.75	V _{DD} - 1		
Receive Output (Bus Dominant Conditions)	V _{RXL}	$\overline{\text{TX}} = \text{V}_{\text{INH}}$ - 0.8 ≤ V _{CAN_H} ≤ 7 - 2 ≤ V _{CAN_L} ≤ 5.8 0.9 ≤ V _{CAN_H} - V _{CAN_L} ≤ 5 (Bus Dominant)	I _{OUT} = 10 μA		0.05		
			I _{OUT} = 100 μA		0.2		1
			I _{OUT} = 2 mA		1		1.75
Internal Resistance from Bus Pins	R _{IN} , BUS_L	$\overline{\text{TX}} = \text{V}_{\text{INH}}$ (Recessive)	5		50	kΩ	
	R _{IN} , BUS_H		5		50		
	R _{DIFF}		10		100		
Internal Capacitance from Bus Pins ^c	C _{IN} (CAN_H, CAN_L)				50	pF	

**SPECIFICATIONS**

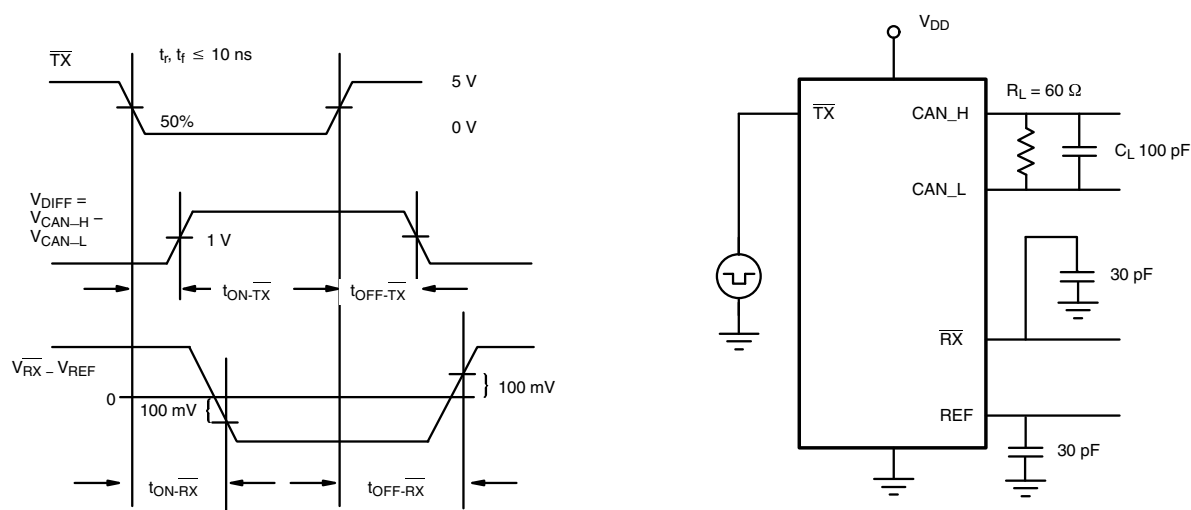
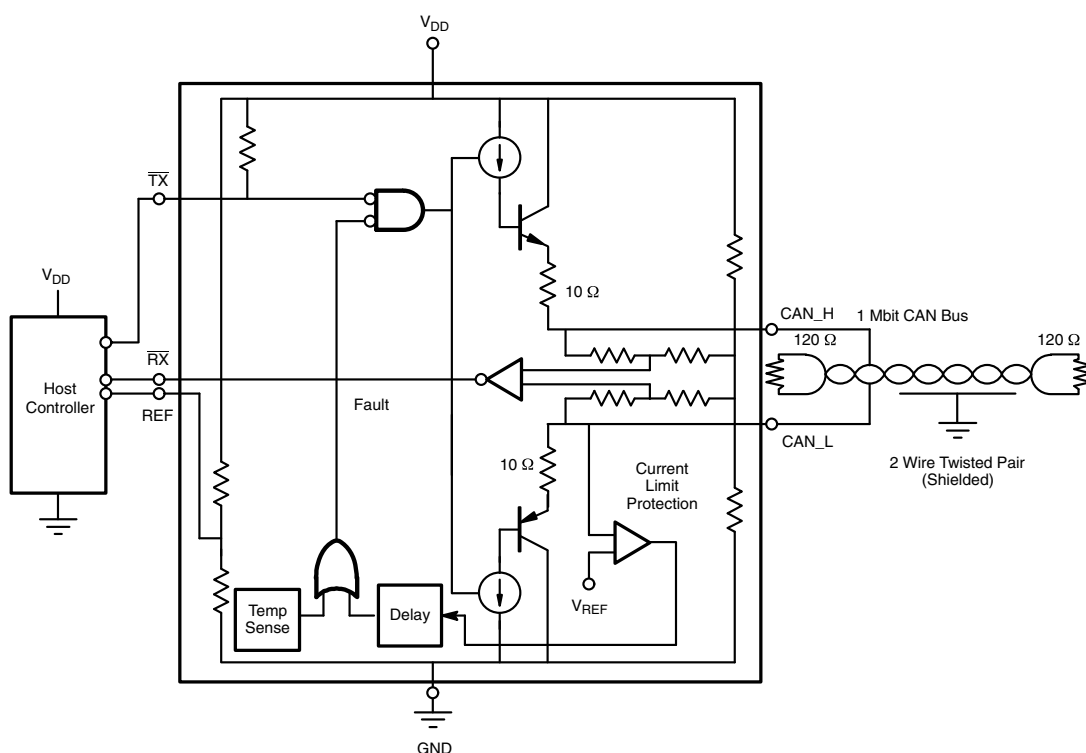
Parameter	Symbol	Test Conditions Unless Otherwise Specified V _{DD} = 4.75 V to 5.25 V	Limits T _A = - 40 V to 125 °C			Unit
			Min. ^b	Typ. ^c	Max. ^b	
Dynamic						
Propagation Delay - TX to V _{DIFF} High	t _{ON-TX}				50	ns
Propagation Delay - TX to V _{DIFF} Low	t _{OFF-TX}				50	
Propagation Delay - TX to Receive Low	t _{ON-RX}				120	
Propagation Delay - TX to Receive High	t _{OFF-RX}				120	
Supply						
Supply Current	I _{DD}	$\overline{TX} = V_{INH}, V_{DD} = 5.25 \text{ V}, R_L = 60 \text{ } \Omega \text{ (Recessive)}$			25	mA
		$\overline{TX} = V_{INL}, V_{DD} = 5.25 \text{ V}, R_L = 60 \text{ } \Omega \text{ (Dominant)}$	40		75	
Transient ^c						
Electrostatic Discharge Human Body Model	V _{ESD}	C _L = 100 pF, R _L = 1500 Ω MIL-STD-883D, Method 3015		2000		V
Bus Transient Voltage	V _{TRANS}	R _S = 1000 Ω, 1 ms	- 60		60	
Protection						
Thermal Trip Point ^c	T _{TRP}		150	165	180	°C
Thermal Hysteresis ^c	T _{HYS}		10	20	30	

Notes:

- a. Typical values are for DESIGN AID ONLY at $T_A = 25 \text{ °C}$, not guaranteed nor subject to production testing.
b. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum.
c. Guaranteed by design, not subject to production test.

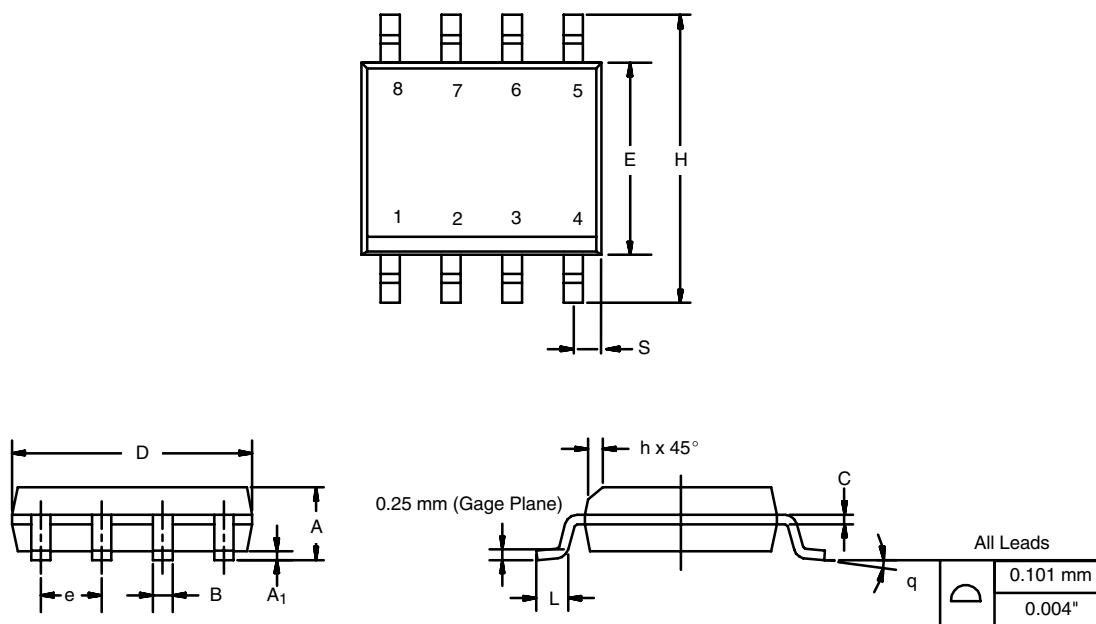
Truth Table

$\overline{TX}$	Mode	Bus State	CAN_H	CAN_L	RX
Low	Transmit	Dominant	High	Low	Low
High (or Floating)	Transmit and Receive	Recessive	Floating	Floating	High
High (or Floating)	Receive	Recessive	High	Low	Low

SWITCHING TIME TEST CIRCUIT**CIRCUIT SCHEMATIC**

SOIC (NARROW): 8-LEAD

JEDEC Part Number: MS-012



DIM	MILLIMETERS		INCHES	
	Min	Max	Min	Max
A	1.35	1.75	0.053	0.069
A ₁	0.10	0.20	0.004	0.008
B	0.35	0.51	0.014	0.020
C	0.19	0.25	0.0075	0.010
D	4.80	5.00	0.189	0.196
E	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
H	5.80	6.20	0.228	0.244
h	0.25	0.50	0.010	0.020
L	0.50	0.93	0.020	0.037
q	0°	8°	0°	8°
S	0.44	0.64	0.018	0.026
ECN: C-06527-Rev. I, 11-Sep-06				
DWG: 5498				



Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and/or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

Material Category Policy

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as RoHS-Compliant fulfill the definitions and restrictions defined under Directive 2011/65/EU of The European Parliament and of the Council of June 8, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment (EEE) - recast, unless otherwise specified as non-compliant.

Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

Vishay Intertechnology, Inc. hereby certifies that all its products that are identified as Halogen-Free follow Halogen-Free requirements as per JEDEC JS709A standards. Please note that some Vishay documentation may still make reference to the IEC 61249-2-21 definition. We confirm that all the products identified as being compliant to IEC 61249-2-21 conform to JEDEC JS709A standards.