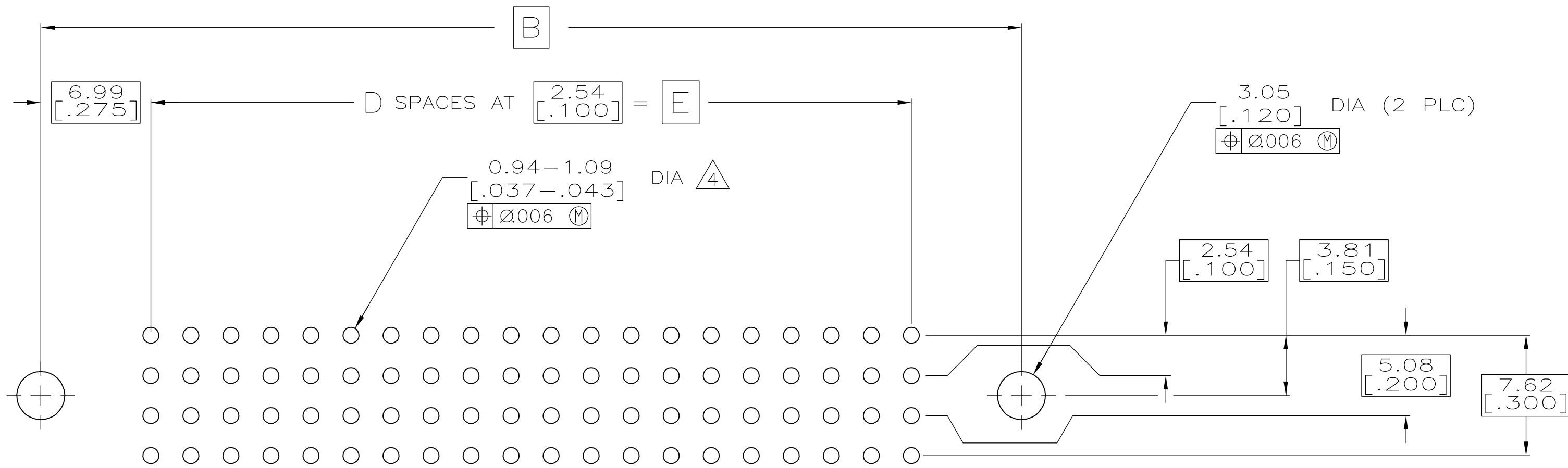
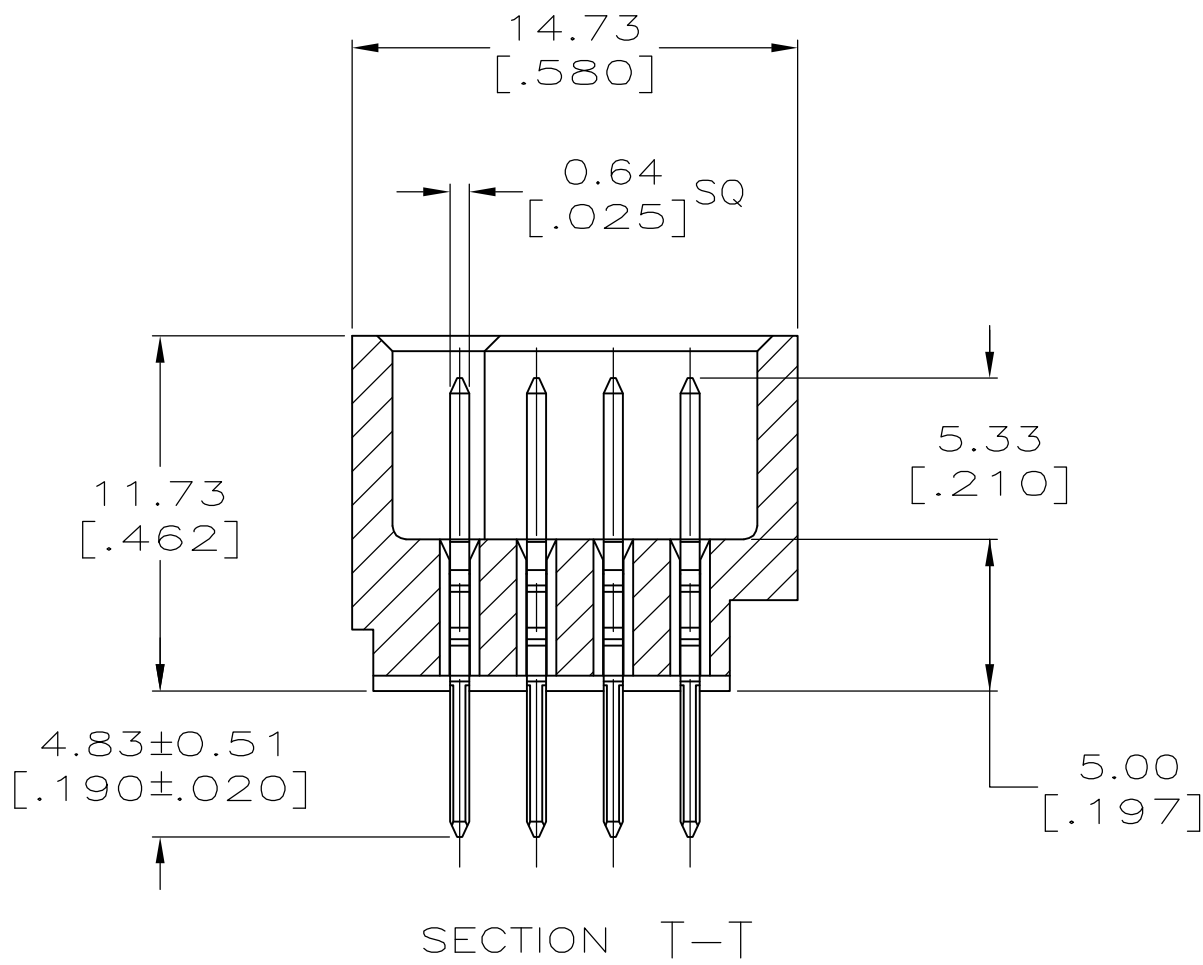
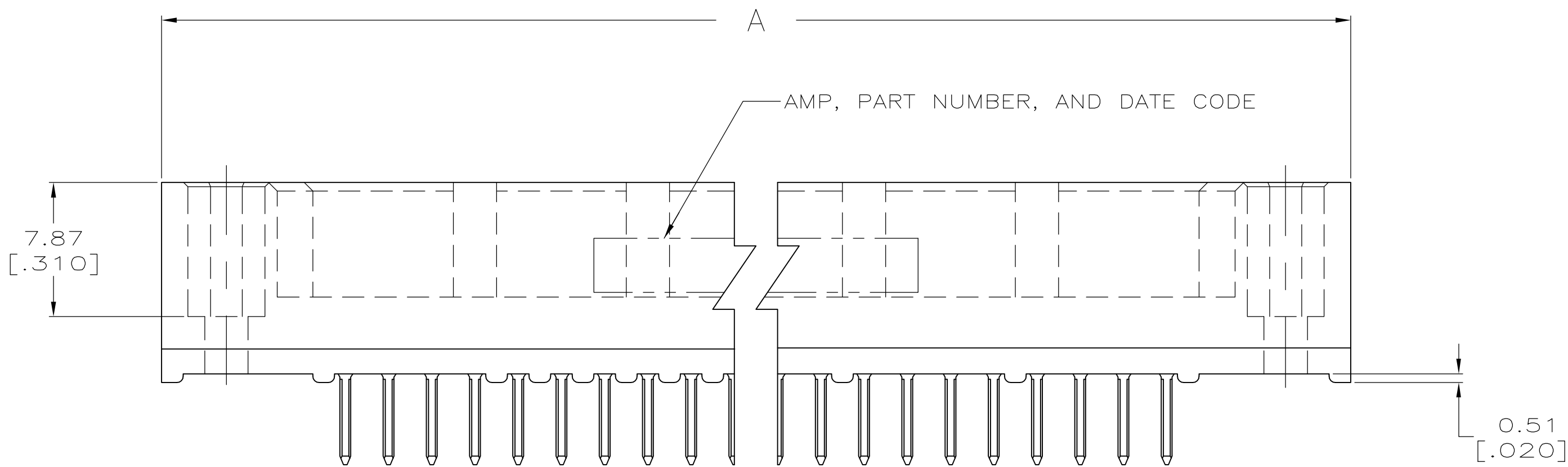
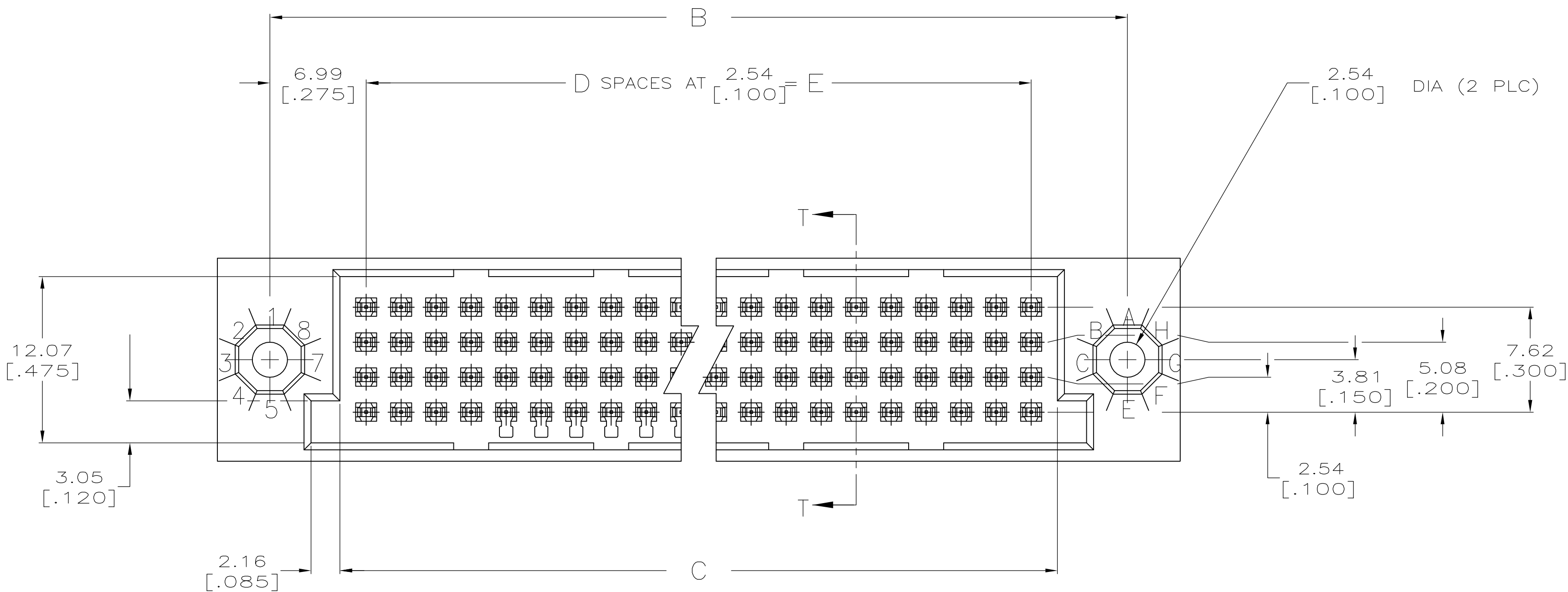


LOC		DIST		REVISIONS				
P	T	LTR	DESCRIPTION	DATE	DWN	APVD		
		R	REV PER EC 0512-0287-05	10DEC05	JDP	AMS		



RECOMMENDED P.C. BOARD LAYOUT

THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN R. K. SEIFRIED 8-18-92	04/09/90	tyco Electronics		Tyco Electronics Corporation Harriaburg, PA 17105-3608	
DIMENSIONS: mm [INCHES]		CHK F. KIBLER	8-18-92	NAME		HDI, 4 ROW, PIN ASSEMBLY, 4.83 [.190] LONG SOLDER PINS, .00076 [.000030] GOLD PLATE	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		PRODUCT SPEC		APPLICATION SPEC		RESTRICTED TO	
0 PLC ± -		SIZE		CAGE CODE		DRAWING NO	
1 PLC ± -		WEIGHT		A1		00779	
2 PLC ± -		SCALE		4:1		SHEET	
3 PLC ± 0.13 [.005]		CUSTOMER DRAWING		1		2	
4 PLC ± -		REV		R			
ANGLES ± -							
FINISH							

