

AK8788A

Shipped in packet-tape reel(5000pcs/Reel)

AK8788A is ultra-small Hall effect IC of a single silicon chip composed of Hall element and a signal processing IC.

Omnipolar Hall Effect Switch

Supply Voltage
1.6~5.5VHall Element
Pulse ExcitationHigh Sensitivity
Bop:3mTOutput
CMOS

SON

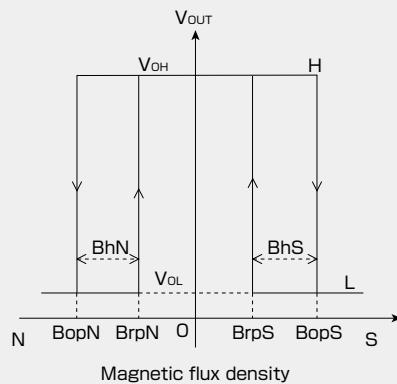
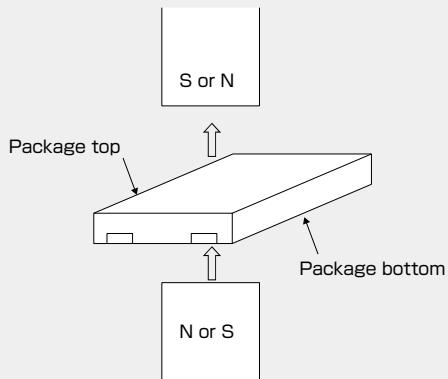
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●Features

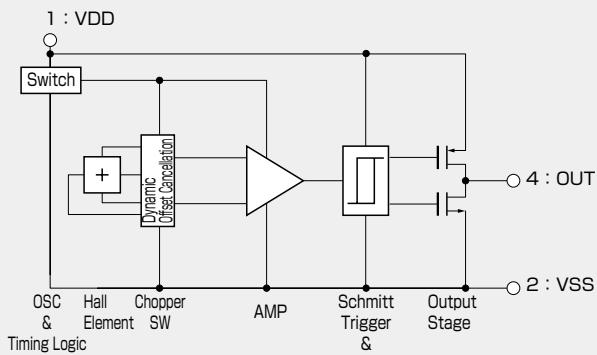
- High sensitive omnipolar operation
- Micropower operation
Typ.4.5 μ A (average : $V_{DD}=1.85V$)
- Ultra small SON package : $1.1 \times 1.4 \times 0.37\text{mm}$
Halogen free



●Operational Characteristics



●Functional Block Diagram



Item	Function
OSC	Generates operating clock
Timing Logic	Generates timing signal required for Chopper SW, AMP and other circuits
Hall Element	Hall element fabricated by CMOS process
Chopper SW	Performs chopping in order to cancel the offset voltage of Hall sensor
AMP	Reduces offset voltage and amplifies Hall output voltage
Schmitt Trigger	Hysteresis comparator
Latch Logic Output Stage	CMOS output. During the power down mode, output is latched in its previous state

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●Absolute Maximum Ratings

Item	symbol	Min.	Max.	Unit
Power supply voltage	V_{DD}	-0.3	+6.5	V
Output current	I_{OUT}	-0.5	+0.5	mA
Storage temperature	T_{STG}	-55	+125	°C

Note) Stresses beyond these listed values may cause permanent damage to the device.

●Recommended Operating Conditions

Item	symbol	Min.	Typ.	Max.	Unit
Power supply voltage	V_{DD}	1.6	1.85	5.5	V
Operating temperature	T_a	-40		+85	°C

●Electrical Characteristics① (Ta=25°C V_{DD} =1.85V Unless otherwise noted)

Item	symbol	Min.	Typ.	Max.	Unit	Note
Current consumption	I_{DD}		4.5	9	μA	Average
	I_{DD2}		7.5	12	μA	Average, V_{DD} =5.5V
High level output voltage	V_{OH}	$V_{DD} - 0.4$			V	$I_{OUT} = -0.5mA$
Low level output voltage	V_{OL}			0.4	V	$I_{OUT} = +0.5mA$
Pulse drive period	T_{PD1}	25	50	100	ms	
Pulse drive time	T_{PD2}	43	85.4	170	μs	

●Electrical Characteristics② (Ta=-40~+85°C V_{DD} =1.6~5.5V)

Item	symbol	Min.	Typ.	Max.	Unit	Note
Current consumption	I_{DD}		4.5	15	μA	Average
High level output voltage	V_{OH}	$V_{DD} - 0.4$			V	$I_{OUT} = -0.5mA$
Low level output voltage	V_{OL}			0.4	V	$I_{OUT} = +0.5mA$
Pulse drive period	T_{PD1}	25	50	100	ms	
Pulse drive time	T_{PD2}	43	85.4	170	μs	

Note) The specifications in Electrical Characteristics ② are design targets.

●Magnetic Characteristics① (Ta=25°C V_{DD} =1.85V)

Item	symbol	Min.	Typ.	Max.	Unit
Operating points	BopS	*1.9	3.0	3.7	mT
	BopN	-3.7	-3.0	*-1.9	mT
Releasing points	BrpS	1.6	2.4	*3.4	mT
	BrpN	*-3.4	-2.4	-1.6	mT
Hysteresis	BhS,BhN	*0.3	0.6	*1.5	mT

Note) The characteristics with * mark are design targets.

●Magnetic Characteristics② (Ta=-40~+85°C V_{DD} =1.6~5.5V)

Item	symbol	Min.	Typ.	Max.	Unit
Operating points	BopS	1.7	3.0	4.1	mT
	BopN	-4.1	-3.0	-1.7	mT
Releasing points	BrpS	1.4	2.4	3.8	mT
	BrpN	-3.8	-2.4	-1.4	mT
Hysteresis	BhS,BhN	0.1	0.6	1.7	mT

Note) The specifications in Magnetic Characteristics ② are design targets.

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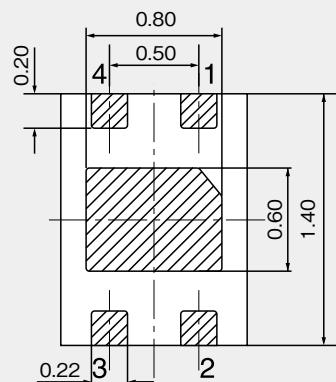
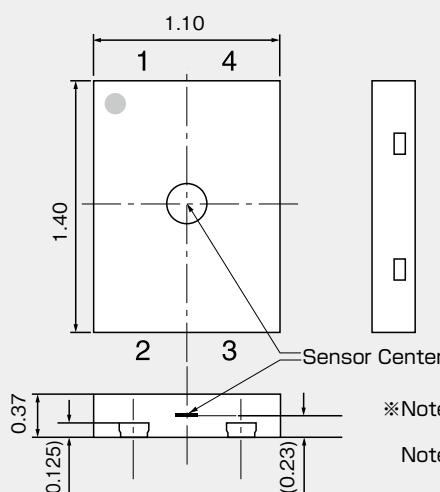
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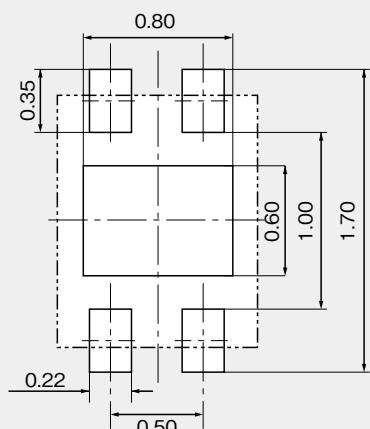
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● Package (Unit:mm)

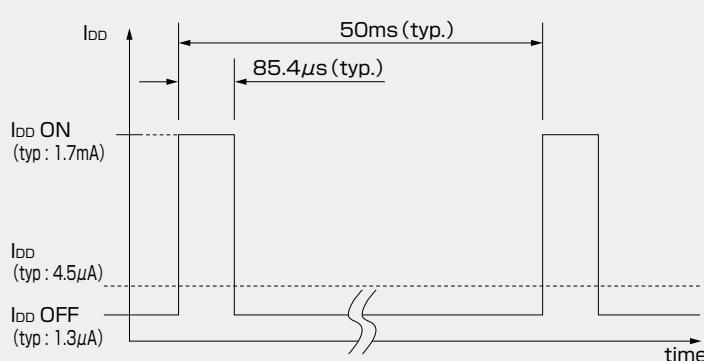


● Footprint (for reference)

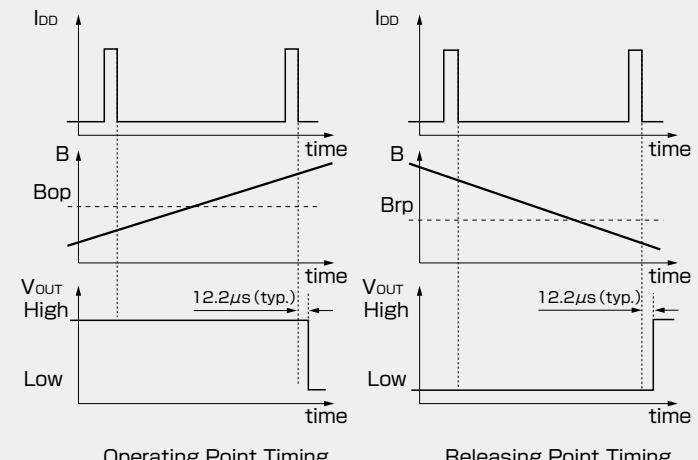


※Note 1) Sensitive area position referenced to the center of package within $\phi 0.3$ mm circle.
 Note 2) Tolerances of dimension otherwise noted is ± 0.05 mm.
 Note 3) Hatched area is plated.
 Note 4) Center pad area (TAB) should be tied to the VSS or floating

Pin No.	Pin name	Function	Note
1	VDD	Power supply	
2	VSS	Ground	
3	N.C. (No internal connection)	Connect to VSS externally	
4	OUT	Output	CMOS output

● I_{DD} Timing Chart

● Functional Timing Chart

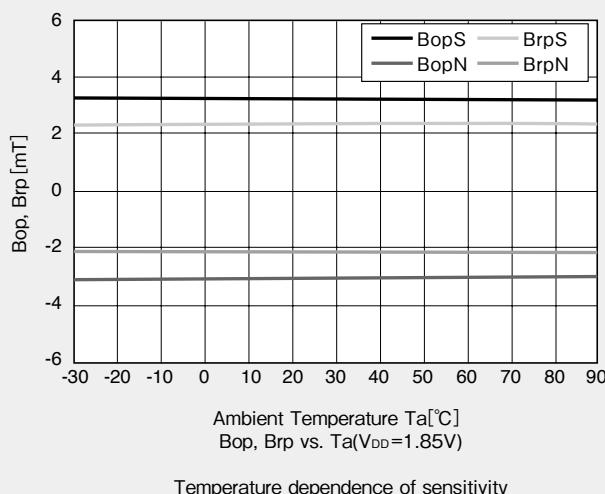


Operating Point Timing

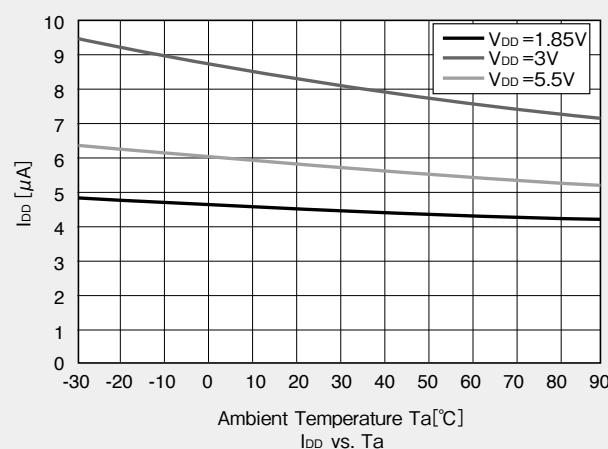
Releasing Point Timing

Note) Hall effect IC's output is held as internal data just before the internal circuit turns off. And after 12.2 μ s (typ.) the output changes.

● Typical Characteristics Data (for reference)

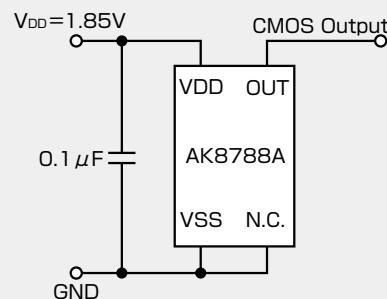


Temperature dependence of sensitivity



Temperature dependence of current consumption (Average)

● Application Circuit



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