

Features

- +3.5 V Operation
- Output Power up to 35 dBm
- Efficiency Typically 55%
- Outstanding Efficiency vs. Supply Voltage
- High Power SSOP-16 Package with Slug
- Wide Power Control Range (50 dB)

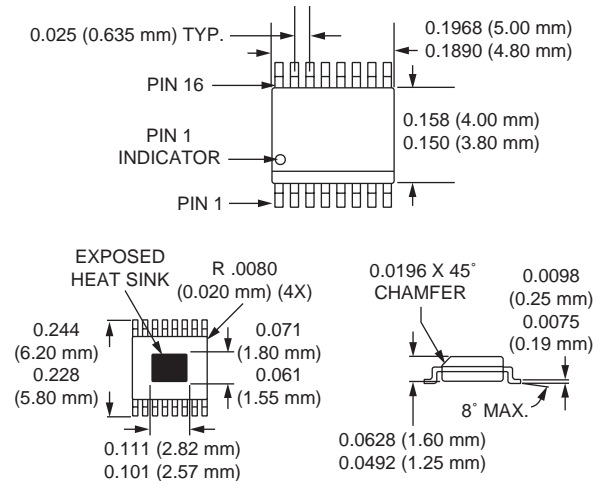
Description

The AP112-79 is a low cost IC power amplifier designed for the 880–915 MHz frequency band. It features 3 cell battery operation and high efficiency. The AP112-79 has external matching for improved performance and flexibility.

Output Matching Circuit

The output match for the AP112-79 is provided externally in order to improve performance, reduce cost, and add flexibility. By making use of ceramic surface mount components with better Qs than GaAs matching elements, a lower loss matching network can be made. This lower loss results in higher power and efficiency for the amplifier. Also, by keeping these elements external the GaAs die size is reduced and the overall cost is less. This approach also permits the flexibility to tweak the amplifier for optimum performance at different powers, and/or frequencies.

SSOP-16 Slug



Absolute Maximum Ratings

Quantity	Value
Amplifier Supply Voltage (V_{DS})	10 V
Input RF Power (P_{IN})	15 dBm
Duty Cycle	50%
Operating Temperature (T_{OP})	-40 to +85°C
Storage Temperature (T_{ST})	-65 to +150°C

Electrical Specifications at 25°C

Quantity	Symbol	Condition	Min.	Typ.	Max.	Unit
Output Power	P_{OUT}	$T_{OP} = +25^{\circ}\text{C}$	34.5	35		dBm
		$V_{DS} = 2.8\text{ V}$, $T_{OP} = (-40\text{ to }+85^{\circ}\text{C})$	32.5	33		
Power Added Efficiency	η_{PAE}		50	55		%
Control Voltage Range	V_{GG}		-3		-1	V
2nd Harmonic	H_2			-43	-40	dBc
3rd Harmonic	H_3			-48	-45	dBc
Input VSWR	$VSWR_{IN}$	$P_{OUT} (5-35\text{ dBm})$, Controlled by V_{GG}	3:1	2:1		
Forward Isolation	$P_{OUT, STANDBY}$	$P_{IN} = 12\text{ dBm}$, $V_{GG} = -3.0\text{ V}$		-39	-30	dBm
Switching Time	t_R, t_F	Time from $P_{OUT} = -10\text{ dBm}$ to $P_{OUT} = 34.5\text{ dBm}$		1	2	μS
Burn Out	BO	$V_{DS} = 2.8\text{ V to }6.0\text{ V}$, $P_{IN} = 8\text{ dBm to }14\text{ dBm}$, $Z_S = 50\ \Omega$, Load $VSWR = 10:1$, All Phase Angles	No Module Damage or Permanent Degradation			
Stability	Stab.	All Combinations of the Following Parameters: $I_{DS} = 0\text{ A to }x\text{ A}$, $x = \text{Current at } P_{OUT} = 34.5\text{ dBm in } 50\ \Omega$ $P_{IN} = 10\text{ dBm to }14\text{ dBm}$, $V_{DS} = 2.5\text{ V to }4.5\text{ V}$, $T_{OP} = -40\text{ to }+85^{\circ}\text{C}$, Load $VSWR = 10:1$, All Phase Angles	No Parasitic Oscillations Above -36 dBm			
Slope	P_{OUT}/V_{GG}	$P_{OUT} = -15\text{ dBm to }35\text{ dBm}$	10	100	150	dB/V
Noise Power		100 KHz BW 925–960 MHz Band		-90	-85	dBm
Phase Change	AM-PM	The Change in Phase When P_{OUT} Changes from 33 dBm to 34 dBm		5	10	Deg.

Characteristic Values:

$P_{IN} = 12\text{ dBm}$

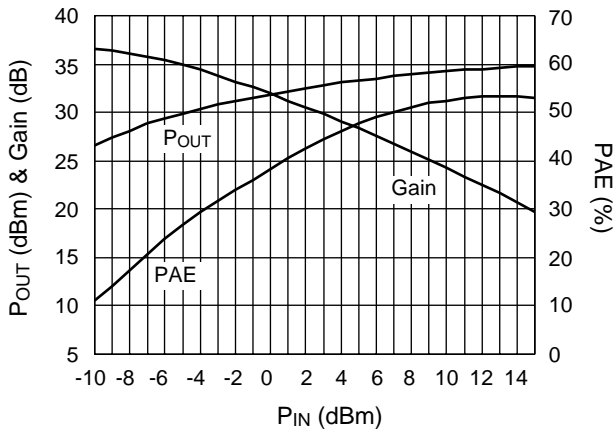
$f_c = 880\text{--}915\text{ MHz}$

$V_{DS} = 3.5\text{ V}$

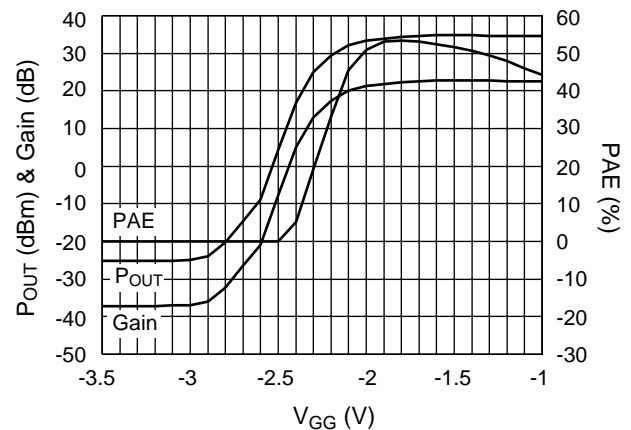
$T_{OP} = +25^{\circ}\text{C}$

$V_{GG} = \text{Switched at } 217\text{ Hz with Duty Cycle of } 12.5\%$

Typical Performance Data

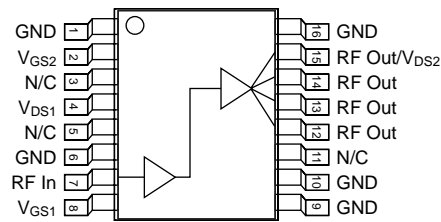


GSM 3.5 V Power Performance
 $V_G = -1.7\text{ V}$, $V_{DD} = 3.5\text{ V}$,
 Frequency = 902 MHz



GSM 3.5 V Gate Sweep
 $P_{IN} = 12\text{ dBm}$, $V_{DD} = 3.5\text{ V}$,
 Frequency = 902 MHz

Pin Out



Pin Configuration

Terminal	Symbol	Function
1	GND	Ground
2	V _{GS2}	Gate Voltage 2
3	N/C	Not Connected
4	V _{DS1}	Drain Voltage 1
5	N/C	Not Connected
6	GND	Ground
7	RF In	RF Input
8	V _{GS1}	Gate Voltage 1
9	GND	Ground
10	GND	Ground
11	N/C	Not Connected
12	RF Out	RF Output
13	RF Out	RF Output
14	RF Out	RF Output
15	RF Out/V _{DS2}	RF Output/Drain Voltage 2
16	GND	Ground