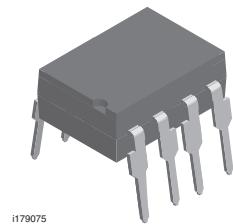
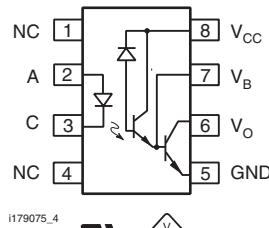


## High Speed Optocoupler, 100 kBd, Low Input Current, High Gain



i179075



i179075\_4



### DESCRIPTION

High common mode transient immunity and very high current ratio together with 5300 V<sub>RMS</sub> insulation are achieved by coupling an LED with an integrated high gain photon detector in an eight pin dual-in-line package. Separate pins for the photo diode and output stage enable TTL compatible saturation voltages with high speed operation.

Photodarlington operation is achieved by tying the V<sub>CC</sub> and V<sub>O</sub> terminals together. Access to the base terminal allows adjustment to the gain bandwidth.

The SFH6138 is ideal for TTL applications since the 300 % minimum current transfer ratio with an LED current of 1.6 mA enables operation with one unit load-in and one unit load-out with a 2.2 kΩ pull-up resistor.

The SFH6139 is best suited for low power logic applications involving CMOS and low power TTL. A 400 % current transfer ratio with only 0.5 mA of LED current is guaranteed from 0 °C to 70 °C.

### FEATURES

- High current transfer ratio, 800 %
- Low input current requirement, 0.5 mA
- High output current, 60 mA
- Isolation test voltage, 5300 V<sub>RMS</sub>
- TTL compatible output, V<sub>OL</sub> = 0.1 V
- High common mode rejection, 500 V/μs
- DC to 0.1 megabit/s operation
- Adjustable bandwidth access to base
- Standard molded DIP plastic package
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC


**RoHS**  
COMPLIANT

### APPLICATIONS

- Logic ground isolation-TTL/TTL, TTL/CMOS, CMOS/CMOS, CMOS/TTL
- EIA RS 232 C line receiver
- Low input current line receiver-long lines, party lines
- Telephone ring detector
- 117 V<sub>AC</sub> line voltage status indication-low input power dissipation
- Low power systems-ground isolation

### AGENCY APPROVALS

- UL1577, file no. E52744 system code H or J, double protection
- DIN EN 60747-5-5 (VDE 0884) available with option 1

### ORDERING INFORMATION

S	F	H	6	1	3	#	-	X	0	#	#	T	DIP-8 7.62 mm Option 7	Option 6 10.16 mm
PART NUMBER							PACKAGE OPTION				TAPE AND REEL			

### AGENCY CERTIFIED/PACKAGE

### CTR (%)

UL	≥ 300	≥ 500
DIP-8	SFH6138	SFH6139
DIP-8, option 6	SFH6138-X006	SFH6139-X006
VDE, UL	≥ 300	≥ 500
SMD-8, option 7	SFH6138-X017T (1)	SFH6139-X017T (1)

### Note

(1) Also available in tubes; do not add T to end

# SFH6138, SFH6139

Vishay Semiconductors High Speed Optocoupler, 100 kBd,  
Low Input Current, High Gain



## ABSOLUTE MAXIMUM RATINGS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT
<b>INPUT</b>					
Reverse voltage				5	V
Average current			I <sub>F(AVG)</sub>	20	mA
Peak current	50 % duty cycle - 1 ms pulse width			40	mA
Peak transient current	t ≤ 1 μs, 300 pps			1	A
<b>OUTPUT</b>					
Supply and output voltage	V <sub>CC</sub> (pin 8-5), V <sub>O</sub> (pin 6-5)	SFH6138	V <sub>S</sub> , V <sub>O</sub>	- 0.5 to 7	V
		SFH6139	V <sub>S</sub> , V <sub>O</sub>	- 0.5 to 18	V
Emitter base reverse voltage (pin 5 to 7)			V <sub>EBO</sub>	0.5	V
Output current I <sub>O</sub> (pin 6)			I <sub>O</sub>	60	mA
Derate linearly above 25 °C, free air temperature				0.7	mA/°C
Input power dissipation			P <sub>diss IN</sub>	35	mW
Derate linearly above 50 °C, free air temperature				0.7	mW/°C
Output power dissipation			P <sub>diss OUT</sub>	100	mW
Derate linearly above 25 °C, free air temperature				0.2	mA/°C
<b>COUPLER</b>					
Isolation test voltage	t = 1 s		V <sub>ISO</sub>	5300	V <sub>RMS</sub>
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C		R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C		R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Storage temperature range			T <sub>stg</sub>	- 55 to + 125	°C
Ambient temperature range			T <sub>amb</sub>	- 55 to + 100	°C
Lead soldering temperature <sup>(1)</sup>	t = 10 s		T <sub>sld</sub>	260	°C

### Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to wave profile for soldering conditions for through hole devices.

## ELECTRICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>INPUT</b>							
Forward voltage	I <sub>F</sub> = 16 mA		V <sub>F</sub>		1.4	1.7	V
Reverse breakdown voltage	I <sub>R</sub> = 10 μA		BV <sub>R</sub>	5			V
Temperature coefficient of forward voltage	I <sub>F</sub> = 1.6 mA				- 1.8		mV/°C
<b>OUTPUT</b>							
Logic low output voltage <sup>(1)</sup>	I <sub>F</sub> = 16 mA, I <sub>O</sub> = 4.8 mA, V <sub>CC</sub> = 4.5 V	SFH6138	V <sub>OL</sub>		0.1	0.4	V
	I <sub>F</sub> = 16 mA, I <sub>O</sub> = 8 mA, V <sub>CC</sub> = 4.5 V	SFH6139	V <sub>OL</sub>		0.1	0.4	V
Logic low output voltage	I <sub>F</sub> = 5 mA, I <sub>O</sub> = 15 mA, V <sub>CC</sub> = 4.5 V	SFH6139	V <sub>OL</sub>		0.15	0.4	V
	I <sub>F</sub> = 12 mA, I <sub>O</sub> = 24 mA, V <sub>CC</sub> = 4.5 V	SFH6139	V <sub>OL</sub>		0.25	0.4	V
Logic high output current <sup>(1)</sup>	I <sub>F</sub> = 0 mA, V <sub>O</sub> = V <sub>CC</sub> = 7 V	SFH6138	I <sub>OH</sub>		0.1	250	μA
	I <sub>F</sub> = 0 mA, V <sub>O</sub> = V <sub>CC</sub> = 18 V	SFH6139	I <sub>OH</sub>		0.05	100	μA
Logic low supply current <sup>(1)</sup>	I <sub>F</sub> = 1.6 mA, V <sub>O</sub> = open, V <sub>CC</sub> = 18 V		I <sub>CCL</sub>		0.2	1.5	mA
Logic high supply current	I <sub>F</sub> = 0 mA, V <sub>O</sub> = open, V <sub>CC</sub> = 18 V		I <sub>CCH</sub>		0.001	10	μA

**ELECTRICAL CHARACTERISTICS** ( $T_{amb} = 25^{\circ}\text{C}$ , unless otherwise specified)

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
<b>COUPLER</b>							
Input capacitance	$f = 1 \text{ MHz}, V_F = 0$		$C_{IN}$		25		pF
Capacitance (input to output) <sup>(2)</sup>	$f = 1 \text{ MHz}$		$C_O$		0.6		pF

**Notes**

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

<sup>(1)</sup> Pin 7 open.

<sup>(2)</sup> Device considered a two-terminal device: pins 1, 2, 3 and 4 shorted together and pins 5, 6, 7, and 8 shorted together.

**CURRENT TRANSFER RATIO**

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio <sup>(1)</sup>	$I_F = 1.6 \text{ mA}, V_O = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	SFH6138	CTR	300	1600		%
		SFH6139	CTR	400	1600		%
Current transfer ratio	$I_F = 1.6 \text{ mA}, V_O = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}$	SFH6139	CTR	500	2000		%

**Note**

<sup>(1)</sup> DC current transfer ratio is defined as the ratio of output collector current,  $I_O$ , to the forward LED input current,  $I_F$  times 100 %. Pin 7 open.

**SWITCHING CHARACTERISTICS**

PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Propagation delay time to logic low at output	$I_F = 1.6 \text{ mA}, R_L = 2.2 \text{ k}\Omega$	SFH6138	$t_{PHL}$		2	10	$\mu\text{s}$
	$I_F = 0.5 \text{ mA}, R_L = 4.7 \text{ k}\Omega$	SFH6139	$t_{PHL}$		6	25	$\mu\text{s}$
	$I_F = 12 \text{ mA}, R_L = 270 \Omega$	SFH6139	$t_{PHL}$		0.6	1	$\mu\text{s}$
Propagation delay time to logic high at output <sup>(1)</sup>	$I_F = 1.6 \text{ mA}, R_L = 2.2 \text{ k}\Omega$	SFH6138	$t_{PLH}$		4	35	$\mu\text{s}$
	$I_F = 0.5 \text{ mA}, R_L = 4.7 \text{ k}\Omega$	SFH6139	$t_{PLH}$		5	60	$\mu\text{s}$
	$I_F = 12 \text{ mA}, R_L = 270 \Omega$	SFH6139	$t_{PLH}$		1	7	$\mu\text{s}$

**Note**

<sup>(1)</sup> Pin 7 open. Using a resistor between pin 5 and 7 will decrease gain and delay time.

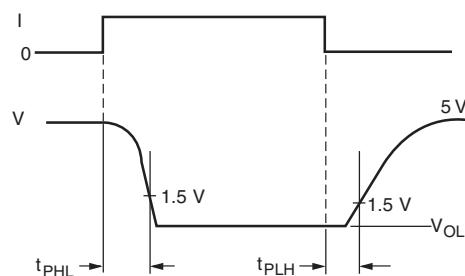
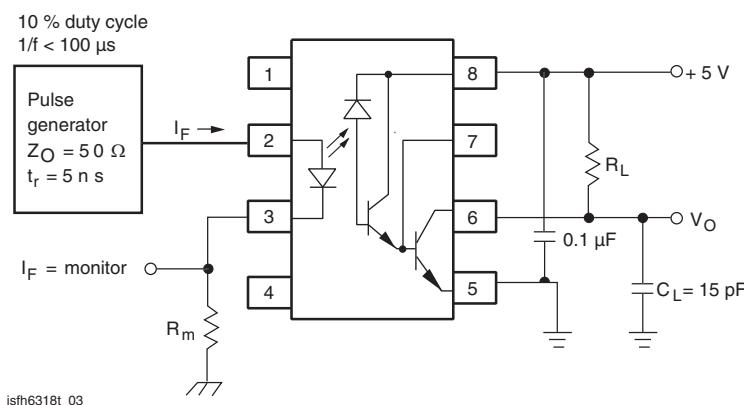


Fig. 1 - Switching Test Circuit

**COMMON MODE TRANSIENT IMMUNITY**

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Common mode transient immunity at logic high level output <sup>(1)</sup>	$I_F = 0 \text{ mA}$ , $R_L = 2.2 \text{ k}\Omega$ , $R_{CC} = 0$ , $V_{CM} = 10 \text{ V}_{P-P}$	$CM_H$		500		$\text{V}/\mu\text{s}$
Common mode transient immunity at logic low level output <sup>(1)</sup>	$I_F = 16 \text{ mA}$ , $R_L = 2.2 \text{ k}\Omega$ , $R_{CC} = 0$ , $V_{CM} = 10 \text{ V}_{P-P}$	$CM_H$		- 500		$\text{V}/\mu\text{s}$

**Note**

<sup>(1)</sup> Common mode transient immunity in logic high level is the maximum tolerable (positive)  $dV_{cm}/dt$  on the leading edge of the common mode pulse,  $V_{CM}$ , to assure that the output will remain in a logic high state (i.e.  $V_O > 2 \text{ V}$ ) common mode transient immunity in logic low level is the maximum tolerable (negative)  $dV_{cm}/dt$  on the trailing edge of the common mode pulse signal,  $V_{CM}$ , to assure that the output will remain in a logic low state (i.e.  $V_O < 0.8 \text{ V}$ ).

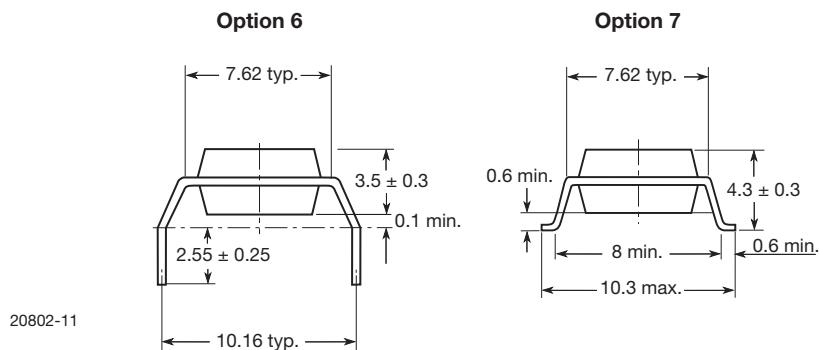
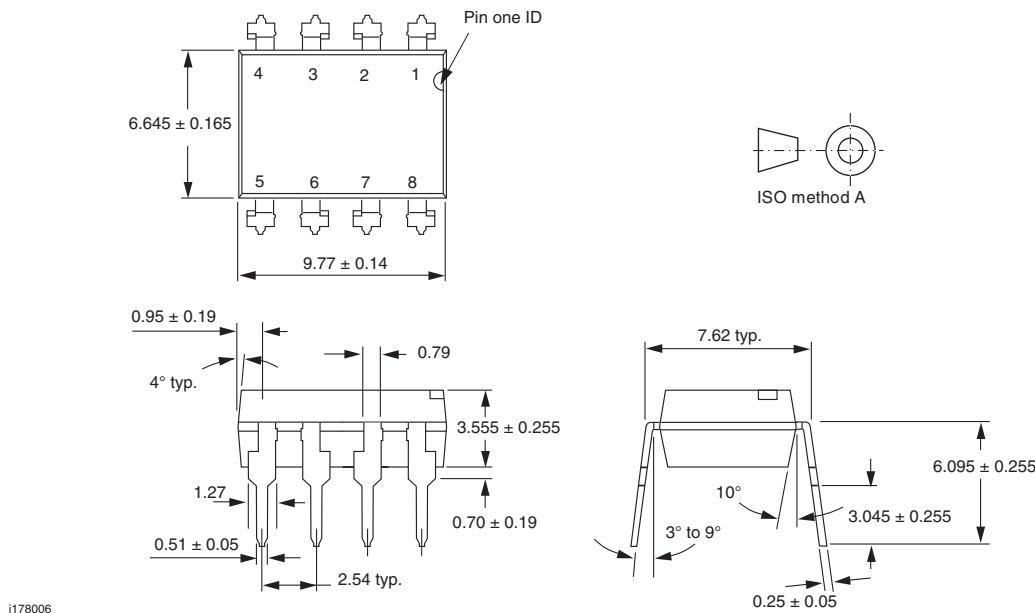
In applications where  $dv/dt$  may exceed 50 000  $\text{V}/\mu\text{s}$  (such as state discharge a series resistor,  $R_{CC}$  should be included to protect  $I_C$  from destructively high surge currents. The recommended value is  $R_{CC} \equiv [IV/0.15 I_F(\text{mA})] \text{ k}\Omega$

**SAFETY AND INSULATION RATINGS**

PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification (according to IEC 68 part 1)				55/100/21		
Comparative tracking index		CTI	175		399	
$V_{IOTM}$			8000			V
$V_{IORM}$			890			V
$P_{SO}$					500	mW
$I_{SI}$					300	mA
$T_{SI}$					175	°C
Creepage distance	Standard DIP-8		7			mm
Clearance distance	Standard DIP-8		7			mm
Creepage distance	400 mil DIP-8		8			mm
Clearance distance	400 mil DIP-8		8			mm

**Note**

- As per IEC 60747-5-5, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

**PACKAGE DIMENSIONS** in millimeters

**PACKAGE MARKING**


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