

TinyLogic ULP-A Dual Inverter with Schmitt-Trigger Input

NC7WP14

The NC7WP14 is a dual inverter with Schmitt-trigger input in tiny footprint packages. The device is designed to operate for $V_{CC} = 0.9\text{ V}$ to 3.6 V .

Features

- Designed for 0.9 V to 3.6 V V_{CC} Operation
- 3.0 ns t_{PD} at 3.3 V (Typ)
- Inputs/Outputs Over-Voltage Tolerant up to 3.6 V
- I_{OFF} Supports Partial Power Down Protection
- Source/Sink 2.6 mA at 3.3 V
- Available in SC-88 and MicroPak™ Packages
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

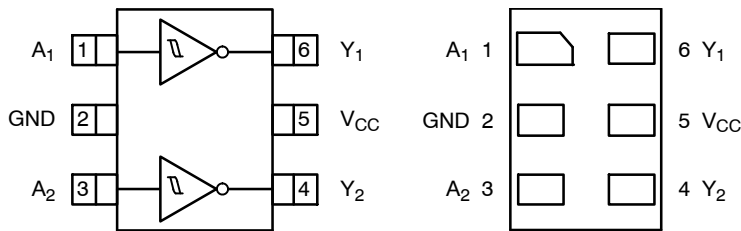


Figure 1. SC-88
(Top View)

Figure 2. MicroPak
(Top Through View)



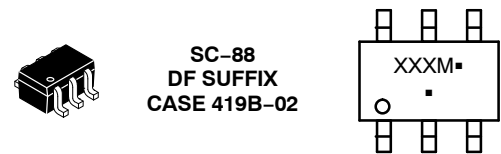
Figure 3. Logic Symbol

PIN ASSIGNMENT

Pin	Function
1	A1
2	GND
3	A2
4	Y2
5	V _{CC}
6	Y1



CC = Specific Device Code
 KK = 2-Digit Lot Run Traceability Code
 XY = 2-Digit Date Code
 Z = Assembly Plant Code



XXX = Specific Device Code
 M = Date Code
 ■ = Pb-Free Package

ORDERING INFORMATION

See detailed ordering, marking and shipping information on page 6 of this data sheet.

FUNCTION TABLE ($Y = \bar{A}$)

Input	Output
A	Y
L	H
H	L

H = HIGH Logic Level
 L = LOW Logic Level

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MAXIMUM RATINGS

Symbol	Characteristics	Value	Unit
V_{CC}	DC Supply Voltage	-0.5 to +4.3	V
V_{IN}	DC Input Voltage	-0.5 to +4.3	V
V_{OUT}	DC Output Voltage Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode ($V_{CC} = 0$ V)	-0.5 to $V_{CC} + 0.5$ -0.5 to +4.3 -0.5 to +4.3	V
I_{IK}	DC Input Diode Current $V_{IN} < GND$	-50	mA
I_{OK}	DC Output Diode Current $V_{OUT} < GND$	-50	mA
I_{OUT}	DC Output Source/Sink Current	± 50	mA
I_{CC} or I_{GND}	DC Supply Current per Supply Pin or Ground Pin	± 50	mA
T_{STG}	Storage Temperature Range	-65 to +150	°C
T_L	Lead Temperature, 1 mm from Case for 10 Seconds	260	°C
T_J	Junction Temperature Under Bias	+150	°C
θ_{JA}	Thermal Resistance (Note 2) SC-88 MicroPak	377 154	°C/W
P_D	Power Dissipation in Still Air SC-88 MicroPak	332 812	mW
MSL	Moisture Sensitivity	Level 1	-
F_R	Flammability Rating Oxygen Index: 28 to 34	UL 94 V-0 @ 0.125 in	-
V_{ESD}	ESD Withstand Voltage (Note 3) Human Body Model Charged Device Model	2000 1000	V
$I_{Latchup}$	Latchup Performance (Note 4)	± 100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Applicable to devices with outputs that may be tri-stated.
2. Measured with minimum pad spacing on an FR4 board, using 10 mm-by-1 inch, 2 ounce copper trace no air flow per JESD51-7.
3. HBM tested to EIA / JESD22-A114-A. CDM tested to JESD22-C101-A. JEDEC recommends that ESD qualification to EIA/JESD22-A115A (Machine Model) be discontinued.
4. Tested to EIA/JESD78 Class II.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{CC}	Positive DC Supply Voltage	0.9	3.6	V
V_{IN}	DC Input Voltage	0	3.6	V
V_{OUT}	DC Output Voltage Active-Mode (High or Low State) Tri-State Mode (Note 1) Power-Down Mode ($V_{CC} = 0$ V)	0 0 0	V_{CC} 3.6 3.6	V
T_A	Operating Temperature Range	-40	+85	°C
t_r, t_f	Input Transition Rise and Fall Time	0	No Limit	ns/V

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied. Extended exposure to stresses beyond the Recommended Operating Ranges limits may affect device reliability.

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DC ELECTRICAL CHARACTERISTICS

Symbol	Parameter	Condition	V _{CC} (V)	T _A = 25°C			T _A = -40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
V _P	Positive Threshold Voltage		0.9	–	0.62	–	–	–	V
			1.1	–	–	1.0	–	1.0	
			1.4	–	–	1.2	–	1.2	
			1.65	–	–	1.5	–	1.5	
			2.3	–	–	1.9	–	1.9	
			3.0	–	–	2.6	–	2.6	
V _N	Negative Threshold Voltage		0.9	–	0.34	–	–	–	V
			1.1	0.15	–	–	0.15	–	
			1.4	0.2	–	–	0.2	–	
			1.65	0.25	–	–	0.25	–	
			2.3	0.4	–	–	0.4	–	
			3.0	0.6	–	–	0.6	–	
V _H	Hysteresis Voltage		0.9	–	0.29	–	–	–	V
			1.1	0.08	–	0.6	0.08	0.6	
			1.4	0.09	–	0.8	0.09	0.8	
			1.65	0.1	–	1.0	0.1	1.0	
			2.3	0.25	–	1.1	0.25	1.1	
			3.0	0.6	–	1.8	0.6	1.8	
V _{OH}	High-Level Output Voltage	V _{IN} = V _{IH} or V _{IL}							V
		I _{OH} = -20 µA	0.9	–	V _{CC} - 0.1	–	–	–	
			1.1 to 1.3	V _{CC} - 0.1	–	–	V _{CC} - 0.1	–	
			1.4 to 1.6	V _{CC} - 0.1	–	–	V _{CC} - 0.1	–	
			1.65 to 1.95	V _{CC} - 0.1	–	–	V _{CC} - 0.1	–	
			2.3 to 2.7	V _{CC} - 0.1	–	–	V _{CC} - 0.1	–	
			3.0 to 3.6	V _{CC} - 0.1	–	–	V _{CC} - 0.1	–	
		I _{OH} = -0.5 mA	1.1 to 1.3	0.75 x V _{CC}	–	–	0.70 x V _{CC}	–	
		I _{OH} = -1 mA	1.4 to 1.6	1.07	–	–	0.99	–	
		I _{OH} = -1.5 mA	1.65 to 1.95	1.24	–	–	1.22	–	
		I _{OH} = -2.1 mA	2.3 to 2.7	1.95	–	–	1.87	–	
		I _{OH} = -2.6 mA	3.0 to 3.6	2.61	–	–	2.55	–	
V _{OL}	Low-Level Output Voltage	V _{IN} = V _{IH} or V _{IL}							V
		I _{OL} = 20 µA	0.9	–	0.1	–	–	–	
			1.1 to 1.3	–	–	0.1	–	0.1	
			1.4 to 1.6	–	–	0.1	–	0.1	
			1.65 to 1.95	–	–	0.1	–	0.1	
			2.3 to 2.7	–	–	0.1	–	0.1	
			3.0 to 3.6	–	–	0.1	–	0.1	
		I _{OL} = 0.5 mA	1.1 to 1.3	–	–	0.3 x V _{CC}	–	0.3 x V _{CC}	
		I _{OL} = 1 mA	1.4 to 1.6	–	–	0.31	–	0.37	
		I _{OL} = 1.5 mA	1.65 to 1.95	–	–	0.31	–	0.35	
		I _{OL} = 2.1 mA	2.3 to 2.7	–	–	0.31	–	0.33	
		I _{OL} = 2.6 mA	3.0 to 3.6	–	–	0.31	–	0.33	

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DC ELECTRICAL CHARACTERISTICS (continued)

Symbol	Parameter	Condition	V _{CC} (V)	T _A = 25°C			T _A = -40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
I _{IN}	Input Leakage Current	V _{IN} = 0 V to 3.6 V	0.9 to 3.6	–	–	±0.1	–	±0.5	μA
I _{OFF}	Power Off Leakage Current	V _{IN} = 0 V to 3.6 V or V _{OUT} = 0 V to 3.6 V	0	–	–	0.5	–	0.5	μA
I _{CC}	Quiescent Supply Current	V _{IN} = V _{CC} or GND	0.9 to 3.6	–	–	0.9	–	0.9	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

AC ELECTRICAL CHARACTERISTICS

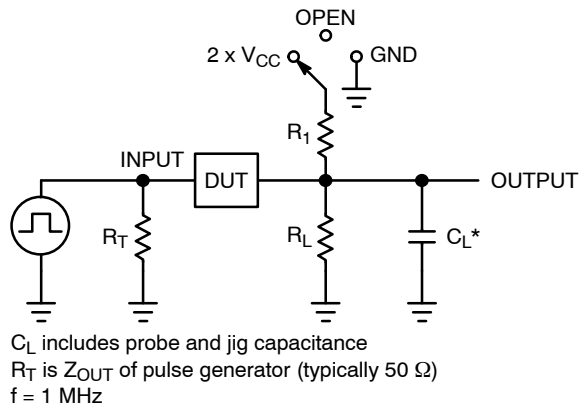
Symbol	Parameter	Condition	V _{CC} (V)	T _A = 25°C			T _A = -40°C to +85°C		Unit
				Min	Typ	Max	Min	Max	
t _{PLH} , t _{PHL}	Propagation Delay, A to Y (Figures 4 and 5)	R _L = 1 MΩ, C _L = 10 pF	0.9	–	51.2	–	–	–	ns
			1.10 to 1.30	–	14.0	28.6	–	37.3	
			1.40 to 1.60	–	7.5	15.8	–	16.0	
			1.65 to 1.95	–	5.4	12.0	–	12.2	
			2.3 to 2.7	–	3.7	9.4	–	9.9	
			3.0 to 3.6	–	3.0	8.3	–	9.0	
t _{PLH} , t _{PHL}	Propagation Delay, A to Y (Figures 4 and 5)	R _L = 1 MΩ, C _L = 15 pF	0.9	–	52.7	–	–	–	ns
			1.10 to 1.30	–	14.6	30.0	–	39.3	
			1.40 to 1.60	–	7.9	16.5	–	17.5	
			1.65 to 1.95	–	5.8	12.6	–	13.6	
			2.3 to 2.7	–	4.0	9.9	–	10.8	
			3.0 to 3.6	–	3.2	8.7	–	9.5	
t _{PLH} , t _{PHL}	Propagation Delay, A to Y (Figures 4 and 5)	R _L = 1 MΩ, C _L = 30 pF	0.9	–	56.9	–	–	–	ns
			1.10 to 1.30	–	16.2	34.0	–	48.3	
			1.40 to 1.60	–	9.3	18.8	–	19.2	
			1.65 to 1.95	–	6.9	14.4	–	15.9	
			2.3 to 2.7	–	4.7	11.3	–	12.8	
			3.0 to 3.6	–	3.8	9.2	–	10.7	

CAPACITIVE CHARACTERISTICS

Symbol	Parameter	Test Condition	Typical (T _A = 25°C)	Unit
C _{IN}	Input Capacitance	V _{CC} = 0 V	2.0	pF
C _{OUT}	Output Capacitance	V _{CC} = 0 V	4.0	pF
C _{PD}	Power Dissipation Capacitance (Note 5)	f = 10 MHz, V _{CC} = 0.9 to 3.6 V, V _{IN} = 0 V or V _{CC}	8.0	pF

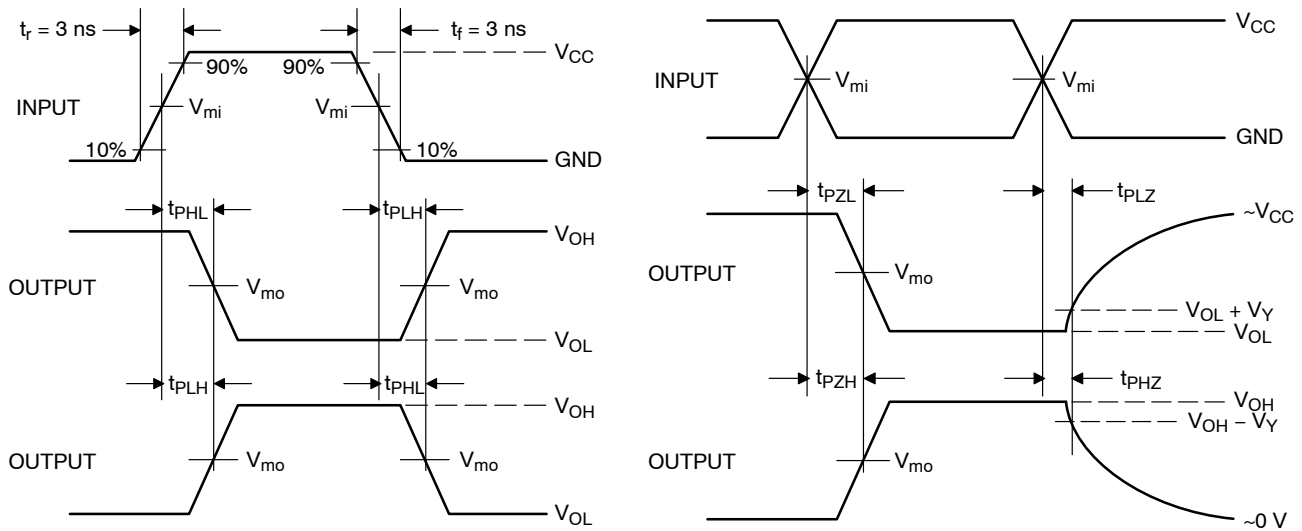
5. C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation I_{CC(OPR)} = C_{PD} • V_{CC} • f_{in} + I_{CC}. C_{PD} is used to determine the no-load dynamic power consumption: P_D = C_{PD} • V_{CC}² • f_{in} + I_{CC} • V_{CC}.

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Test	Switch Position
t_{PLH} / t_{PHL}	Open
t_{PLZ} / t_{PZL}	$2 \times V_{CC}$
t_{PHZ} / t_{PZH}	GND

Figure 4. Test Circuit



V_{CC}, V	V_{mi}, V	V_{mo}, V	V_Y, V
0.9	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.1 to 1.3	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.4 to 1.6	$V_{CC} / 2$	$V_{CC} / 2$	0.1
1.65 to 1.95	$V_{CC} / 2$	$V_{CC} / 2$	0.15
2.3 to 2.7	$V_{CC} / 2$	$V_{CC} / 2$	0.15
3.0 to 3.6	1.5	1.5	0.3

Figure 5. Switching Waveforms

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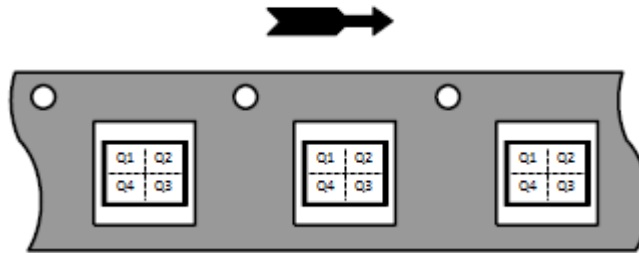
ORDERING INFORMATION

Device	Package	Marking	Pin 1 Orientation (See below)	Shipping [†]
NC7WP14P6X	SC-88	P14	Q4	3000 / Tape & Reel
NC7WP14L6X	MicroPak	AZ	Q4	5000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Pin 1 Orientation in Tape and Reel

Direction of Feed



MicroPak is a trademark of Semiconductor Components Industries, LLC dba "onsemi" or its affiliates and/or subsidiaries in the United States and/or other countries.

PACKAGE DIMENSIONS

2X

0.05 C

1.45

B

2X

0.05 C

1.00

(0.254)

PIN 1 IDENTIFIER

5

TOP VIEW

0.50±0.05

0.05

0.00

C

0.30

6X

RECOMMENDED LAND PATTERN

0.35±0.05

0.40±0.05

0.075 X 45° CHAMFER

DETAIL A

1.00±0.05

1.45±0.05

0.20±0.05 6X

1.0

0.10(M) C B A

0.05(M) C

0.30±0.05 5X

0.35±0.05 5X

(0.050) 6X

0.5

(0.125) 4X

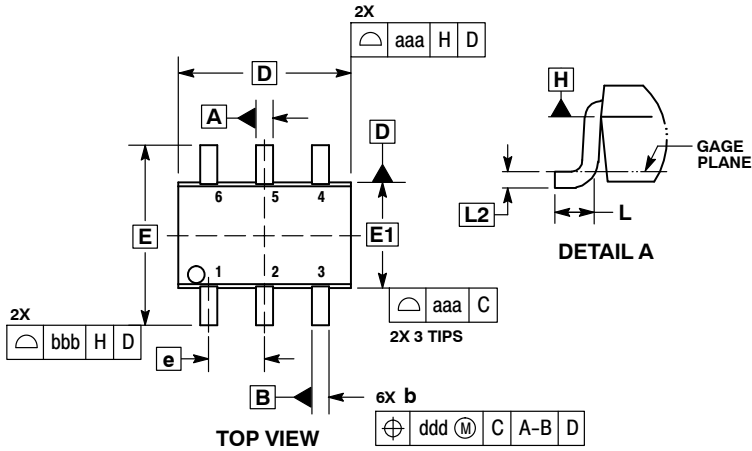
BOTTOM VIEW

1. CONFORMS TO JEDEC STANDARD MO-252 VARIATION UAAD
2. DIMENSIONS ARE IN MILLIMETERS
3. DRAWING CONFORMS TO ASME Y14.5M-2009
4. PIN ONE IDENTIFIER IS 2X LENGTH OF ANY OTHER LINE IN THE MARK CODE LAYOUT.

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PACKAGE DIMENSIONS

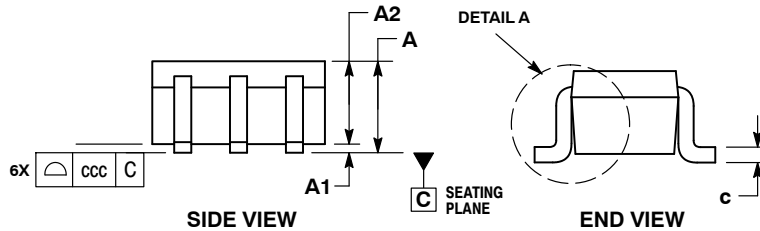
SC-88/SC70-6/SOT-363
CASE 419B-02
ISSUE Y



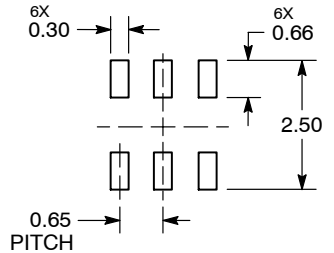
NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSIONS D AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS. MOLD FLASH, PROTRUSIONS, OR GATE BURRS SHALL NOT EXCEED 0.20 PER END.
4. DIMENSIONS D AND E1 AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY AND DATUM H.
5. DATUMS A AND B ARE DETERMINED AT DATUM H.
6. DIMENSIONS b AND c APPLY TO THE FLAT SECTION OF THE LEAD BETWEEN 0.08 AND 0.15 FROM THE TIP.
7. DIMENSION b DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.08 TOTAL IN EXCESS OF DIMENSION b AT MAXIMUM MATERIAL CONDITION. THE DAMBAR CANNOT BE LOCATED ON THE LOWER RADIUS OF THE FOOT.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	---	---	1.10	---	---	0.043
A1	0.00	---	0.10	0.000	---	0.004
A2	0.70	0.90	1.00	0.027	0.035	0.039
b	0.15	0.20	0.25	0.006	0.008	0.010
C	0.08	0.15	0.22	0.003	0.006	0.009
D	1.80	2.00	2.20	0.070	0.078	0.086
E	2.00	2.10	2.20	0.078	0.082	0.086
E1	1.15	1.25	1.35	0.045	0.049	0.053
e	0.65 BSC			0.026 BSC		
L	0.26	0.36	0.46	0.010	0.014	0.018
L2	0.15 BSC			0.006 BSC		
aaa	0.15			0.006		
bbb	0.30			0.012		
ccc	0.10			0.004		
ddd	0.10			0.004		



RECOMMENDED SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the **onsemi** Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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