

Rev. V2

#### **Features**

- 21 dB Adjustable Gain
- 2.25 dB Noise Figure
- +5 V, 95 mA Adjustable Bias
- Low Distortion
- Wide Bandwidth for DOCSIS 3.1
- Lead-Free MSOP8-EP Package
- RoHS\* Compliant and 260°C Reflow Compatible

### **Description**

The MAAM-011184 is a 75  $\Omega$  single ended GaAs MMIC amplifier assembled in a lead-free MSOP8-EP package. This device provides high gain, low noise, and excellent linearity from 5 - 300 MHz.

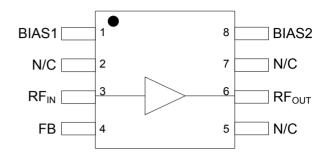
This amplifier is ideally suited for use in CATV return path applications, including DOCSIS 3.1 systems: it typically provides 2.25 dB noise figure, 64 dBm OIP2 and 43 dBm OIP3 while drawing 95 mA DC current at 5 V bias.

## Ordering Information<sup>1</sup>

Part Number	Package
MAAM-011184-TR1000	1000 piece reel
MAAM-011184-TR3000	3000 piece reel
MAAM-011184-001SMB	Sample Board

<sup>1.</sup> All sample boards include 5 loose parts.

#### **Functional Schematic**



## Pin Configuration<sup>2</sup>

Pin No.	Pin Name	Description	
1	BIAS1	V <sub>CC</sub> Bias	
2	N/C	No Connection	
3	RF <sub>IN</sub>	RF Input	
4	FB	Feedback	
5	N/C	No Connection	
6	RF <sub>OUT</sub>	RF Output (DC Bias)	
7	N/C	No Connection	
8	BIAS2	Active Bias	
9	Pad <sup>3</sup>	RF and DC Ground	

- 2. All pins listed as 'No Connection' should be grounded.
- The exposed pad centered on the package bottom must be connected to RF and DC ground.

<sup>\*</sup> Restrictions on Hazardous Substances, European Union Directive 2011/65/EU.



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## Electrical Specifications<sup>4</sup>: $T_A = 25$ °C, $V_{CC} = 5$ V, $Z_0 = 75$ $\Omega$

Parameter	Test Conditions	Units	Min.	Тур.	Max.
Gain	P <sub>IN</sub> = -21 dBm, 5 - 300 MHz P <sub>IN</sub> = -21 dBm, 205 MHz	dB	_ 20	21 21	
Input Return Loss	P <sub>IN</sub> = -21 dBm <sub>,</sub> 5 - 300 MHz	dB	_	26	_
Output Return Loss	P <sub>IN</sub> = -21 dBm <sub>,</sub> 5 - 300 MHz	dB	_	23	-
Reverse Isolation	P <sub>IN</sub> = -21 dBm, 5 - 300 MHz	dB	_	23	_
Noise Figure	5 - 205 MHz 205 - 300 MHz	dB	_	2.25 2.5	
P1dB	5 - 300 MHz	dBm	_	21.7	_
OIP3⁵	$P_{\text{IN}}$ = -21 dBm per tone, 3 MHz spacing, $f_1$ = 5 - 205 MHz $P_{\text{IN}}$ = -21 dBm per tone, 3 MHz spacing, $f_1$ = 205 MHz	dBm	— 38	43 41	_
OIP2 <sup>5</sup>	$P_{IN}$ = -21 dBm per tone, 3 MHz spacing, $f_1$ = 5 - 205 MHz	dBm	_	64	_
Output Power at 30 dB MER <sup>6</sup>	16 Channels, 5 - 205 MHz	dBmV/Channel	_	51	
Icc <sup>7</sup>	V <sub>CC</sub> = 5 V	mA	_	95	115

<sup>4.</sup> Data corresponds to the typical application circuit shown on page 3 of this datasheet. See pages 4 and 5 for typical performance using this application circuit.

## Absolute Maximum Ratings<sup>8,9</sup>

Parameter	Absolute Maximum
Input Power	11 dBm
V <sub>CC</sub>	6 V
Junction Temperature 10,11	+150°C
Operating Temperature	-40°C to +85°C
Storage Temperature	-65°C to +125°C

<sup>8.</sup> Exceeding any one or a combination of these limits may cause permanent damage to this device.

a) For  $T_C = +25^{\circ}C$ ,

 $T_J = 46^{\circ}C @ 5 V, 95 mA$ 

b) For  $T_C = +85^{\circ}C$ ,

T<sub>J</sub> = 106°C @ 5 V, 95 mA

### **Handling Procedures**

Please observe the following precautions to avoid damage:

#### Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

<sup>5.</sup>  $f_1$  is the frequency of the lower of the two input tones. Higher tone  $f_2 = f_1 + 3$  MHz. OIP2 is measured at intermodulation frequency  $f_1 + f_2$ .

<sup>6.</sup> Modulation Error Ratio, 64 QAM 5.12 MS/s.

<sup>7.</sup> I<sub>CC</sub> is the total DC current draw from the V<sub>CC</sub> supply. As shown on page 3 of this datasheet, it is distributed to device pins 1, 6, and 8.

MACOM does not recommend sustained operation near these survivability limits.

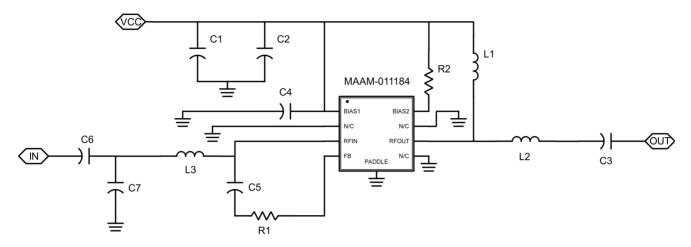
<sup>10.</sup> Operating at nominal conditions with  $T_J \le 150^{\circ}C$  will ensure MTTF > 1 x  $10^6$  hours.

<sup>11.</sup> Junction Temperature ( $T_J$ ) =  $T_C$  +  $\Theta$ jc \* (V \* I) Typical thermal resistance ( $\Theta$ jc) = 44° C/W.



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### **Typical Application Circuit: Schematic**

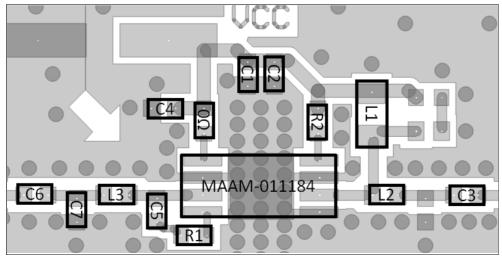


### **Typical Application Circuit: Component Values**

Component	Value	
C1 - C6	100 nF	
C7	0.5 pF	
R1 <sup>12</sup>	330 Ω	
R2 <sup>12</sup>	SHORT - 0 Ω	
L1 <sup>13</sup>	22 μH	
L2	27 nH	
L3	10 nH	

<sup>12.</sup> Designers may decrease resistor R1 to reduce the gain of the amplifier by approximately 1 dB per 164 Ohms. Below 19.8 dB gain, typical input and output return losses fall below 20 dB. Resistor R2 may be increased in order to reduce bias current I<sub>CC</sub> (at the cost of large-signal performance) by approximately 1 mA per 42 Ohms.

### **Typical Application Circuit: Sample Board Layout**



<sup>13.</sup> Low-ESR inductor LQH2MCN220K02 from Murata.

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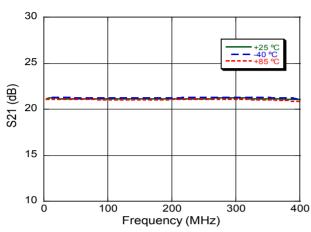


# CATV Return Path Amplifier 5 - 300 MHz

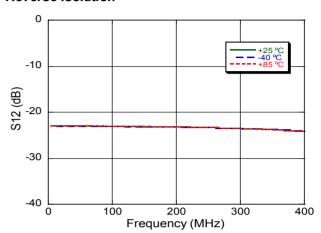
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### **Typical Performance Curves: Small-Signal**

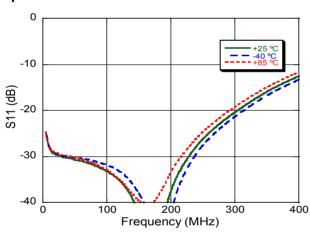




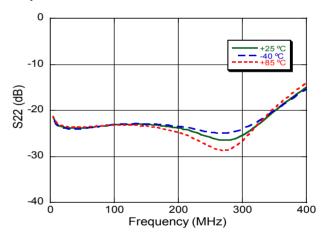
#### Reverse Isolation



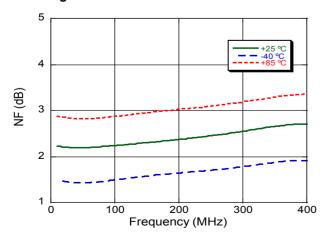
#### Input Return Loss



#### **Output Return Loss**



#### Noise Figure

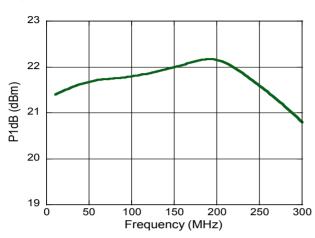




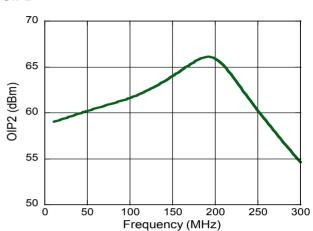
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### **Typical Performance Curves: Large-Signal**

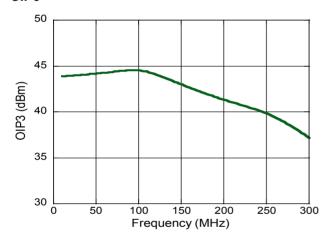




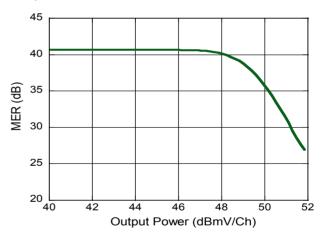
#### OIP2



#### OIP3



#### MER, 16 Channels 64-QAM



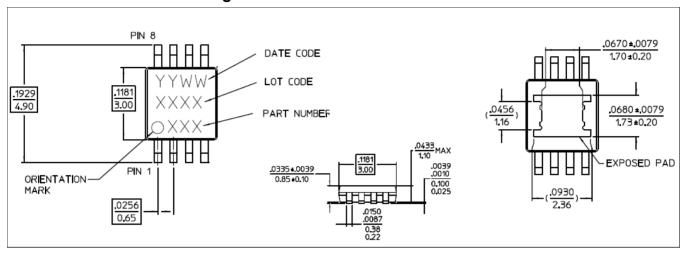
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# CATV Return Path Amplifier 5 - 300 MHz

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## Lead-Free MSOP8-EP Package<sup>†</sup>



<sup>&</sup>lt;sup>†</sup> Dimensions shown as inches over millimeters [in/mm]. Meets JEDEC moisture sensitivity level 1 requirements. Plating is 100% matte tin over copper.

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CATV Return Path Amplifier 5 - 300 MHz

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