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ADC1443D160W1 ADC1443D160W1 Demo board With ARRIAII GX FPGA

IDT'S ADC demoboard is suitable for dynamic performance evaluations from low to high IF configuration. The FPGA eases the evaluation and analysis of the ADC dynamic and enables use of the full JESD204B feature set.

Status: **Active**

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Features

- SMA connector for clock and analog input signals
- On board Data clock generator for ADC and FPGA
- On board ARRIAII GX FPGA for JESD204B data acquisition
- 2 dual ADC on same PCB to easily evaluate the Multi ADC synchronization
- Single power supply (on-board regulators)

Product Options

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Documents

	Title	Type ▲	Format	File Size	Date
	Demo Board Software ADC4	Manual - Software	ZIP	4.12 MB	07/18/2012

Related Products

Product ID	Title
ADC1443D160HD	Dual channel 14-bit ADC; 160 Msps; JESD204B serial outputs