

DUAL N-CHANNEL ENHANCEMENT MODE MOSFET
Product Summary

$V_{(BR)DSS}$	$R_{DS(ON)}$ max	I_D max $T_A = +25^\circ\text{C}$
30V	30mΩ @ $V_{GS} = 10\text{V}$	6.2A
	42mΩ @ $V_{GS} = 4.5\text{V}$	5.2A

Description and Applications

This MOSFET is designed to minimize the on-state resistance ($R_{DS(ON)}$) and yet maintain superior switching performance, making it ideal for high-efficiency power management applications.

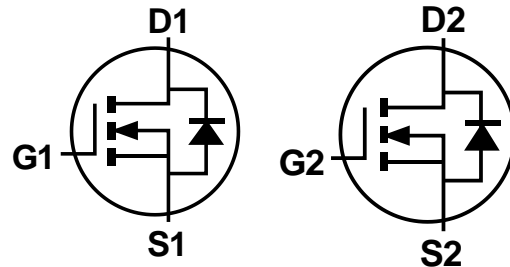
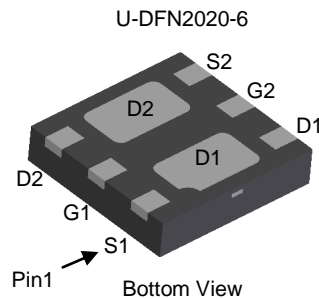
- Body Control Electronics
- Power Management Functions
- DC-DC Converters

Features and Benefits

- Low On-Resistance
- Low Input Capacitance
- Fast Switching Speed
- Low Input/Output Leakage
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**
- **Qualified to AEC-Q101 Standards for High Reliability**

Mechanical Data

- Case: U-DFN2020-6
- Case Material: Molded Plastic, "Green" Molding Compound.
UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish NiPdAu Annealed over Copper Leadframe.
Solderable per MIL-STD-202, Method 208 (e4)
- Terminals Connections: See Diagram Below
- Weight: 0.0065 grams (Approximate)

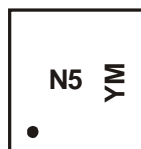

Ordering Information (Note 4)

Part Number	Case	Packaging
DMN3032LFDB-7	U-DFN2020-6	3,000/Tape & Reel
DMN3032LFDB-13	U-DFN2020-6	10,000/Tape & Reel

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
 2. See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <http://www.diodes.com/products/packages.html>.

Marking Information

U-DFN2020-6



N5 = Product Type Marking Code
 YM = Date Code Marking
 Y = Year (ex: C = 2015)
 M = Month (ex: 9 = September)

Date Code Key

Year	2015	2016	2017	2018	2019	2020	2021
Code	C	D	E	F	G	H	I

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Drain-Source Voltage	V _{DSS}	30	V
Gate-Source Voltage	V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	I _D	6.2 5.0	A
Maximum Continuous Body Diode Forward Current (Note 6)	I _S	2	A
Pulsed Drain Current (10μs Pulse, Duty Cycle = 1%)	I _{DM}	25	A
Avalanche Current (Note 7) L = 0.1mH	I _{AS}	12	A
Avalanche Energy (Note 7) L = 0.1mH	E _{AS}	10	mJ

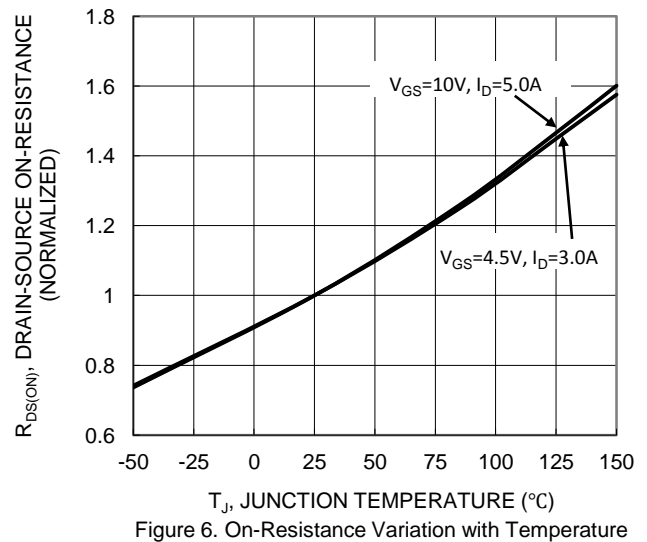
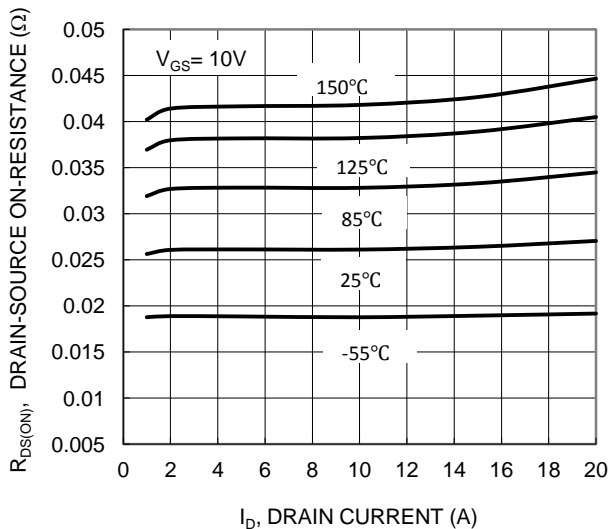
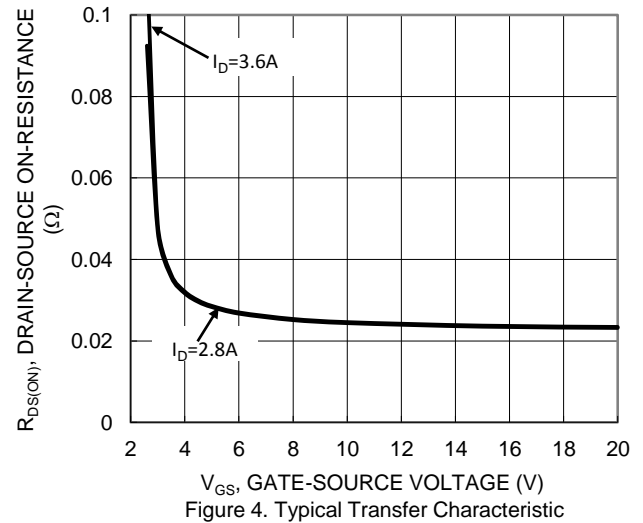
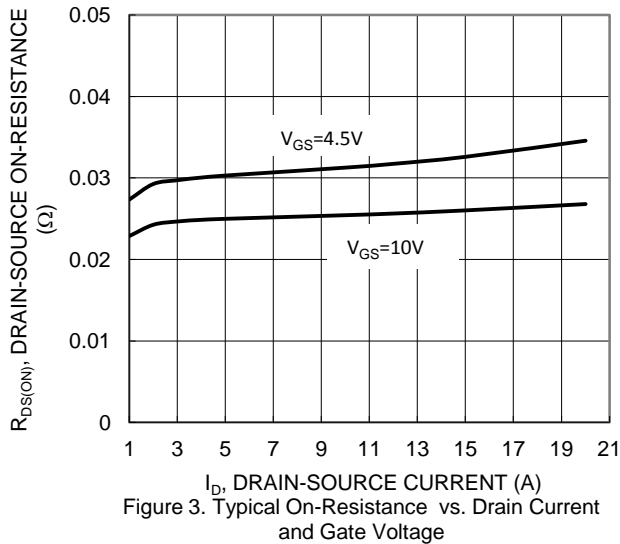
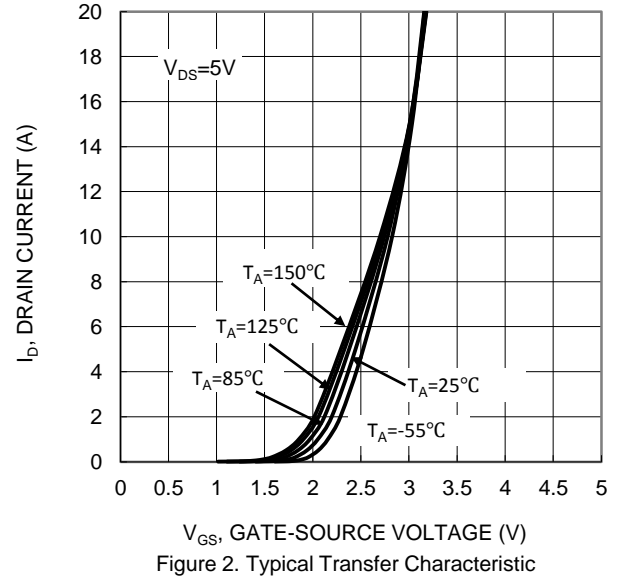
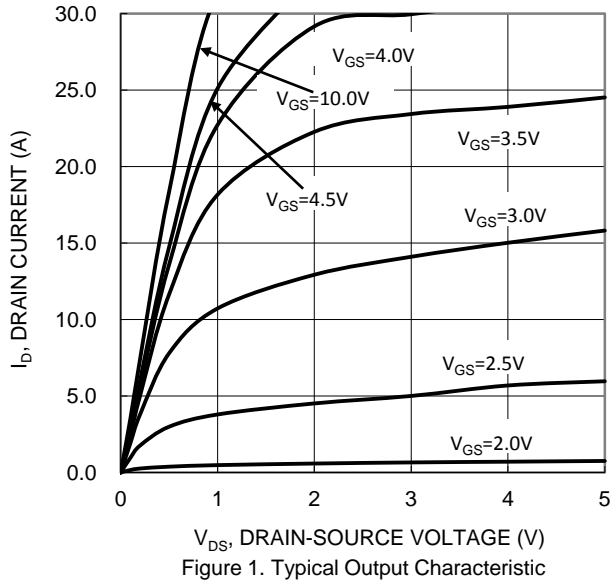
Thermal Characteristics

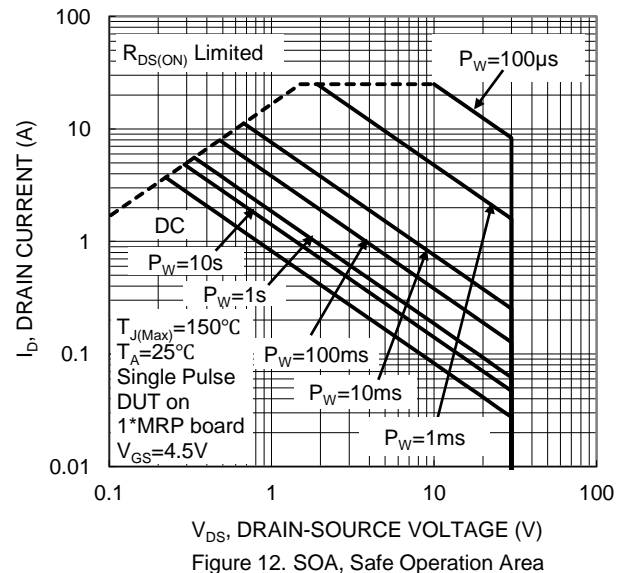
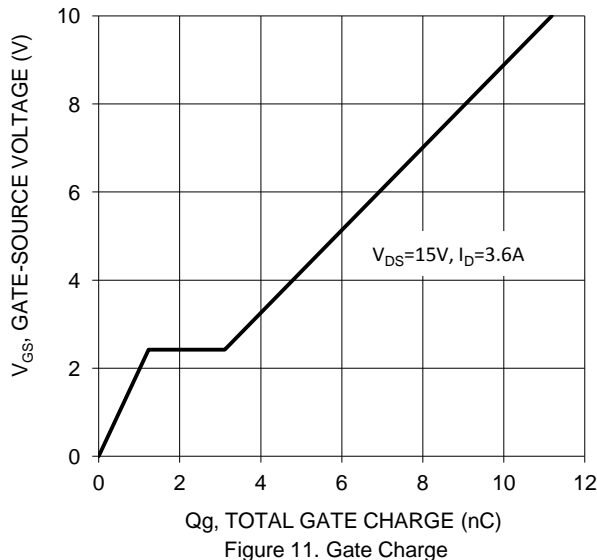
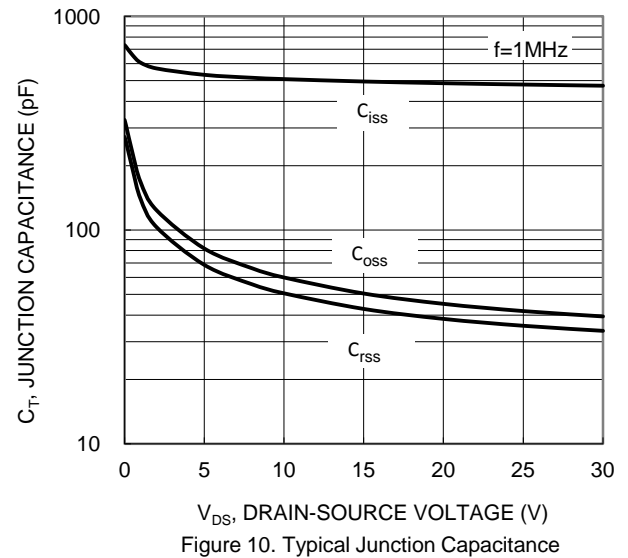
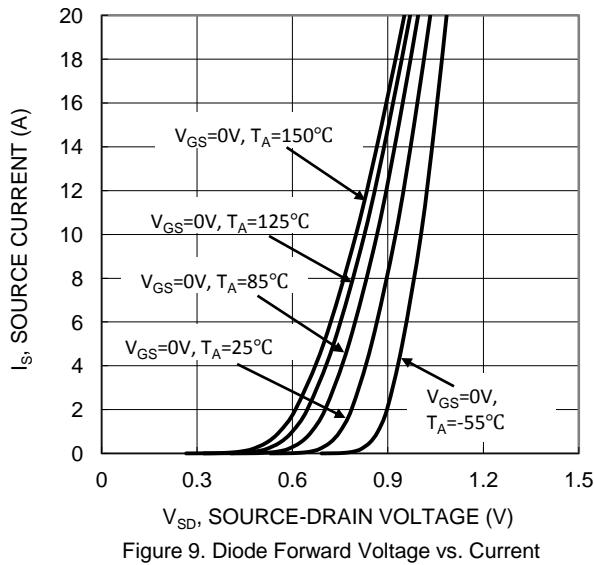
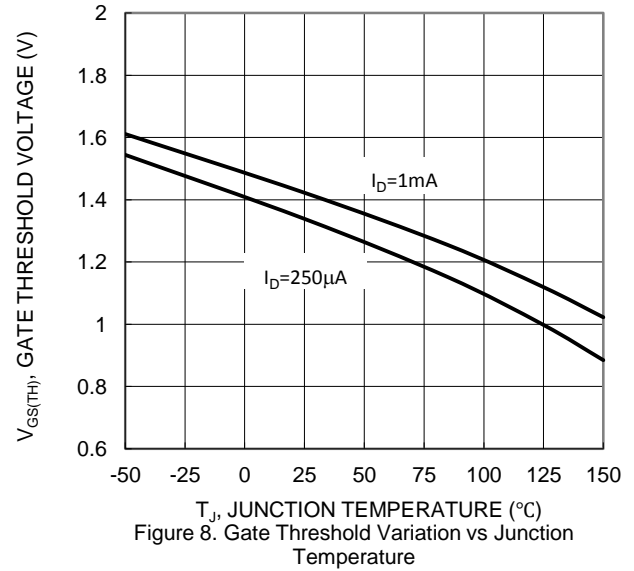
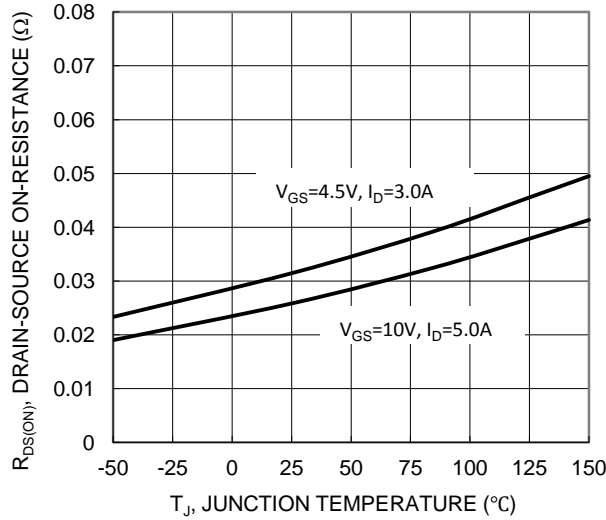
Characteristic	Symbol	Value	Unit
Total Power Dissipation (Note 5)	P _D	1.0	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	127 75	°C/W
Total Power Dissipation (Note 6)	P _D	1.7	W
Thermal Resistance, Junction to Ambient (Note 6)	R _{θJA}	72 43	°C/W
Thermal Resistance, Junction to Case (Note 6)	R _{θJC}	9	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

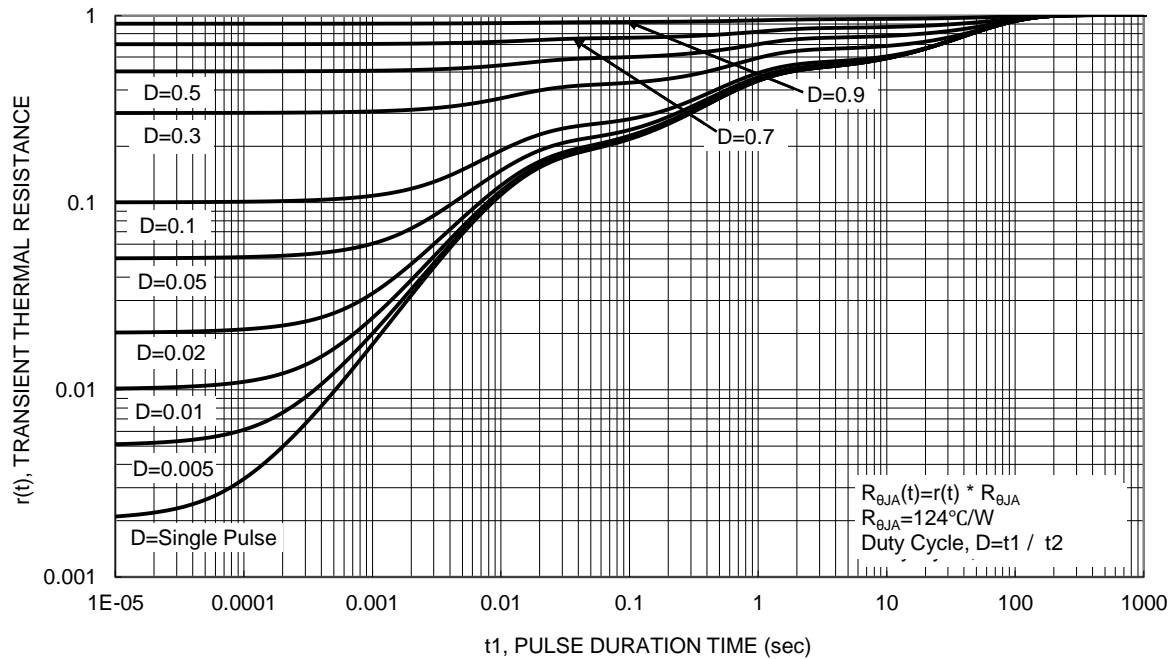
Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 8)						
Drain-Source Breakdown Voltage	BV _{DSS}	30	-	-	V	V _{GS} = 0V, I _D = 250μA
Zero Gate Voltage Drain Current T _J = +25°C	I _{DSS}	-	-	1.0	μA	V _{DS} = 30V, V _{GS} = 0V
Zero Gate Voltage Drain Current T _J = +150°C (Note 9)	I _{DSS}	-	-	100	μA	V _{DS} = 30V, V _{GS} = 0V
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	V _{GS} = ±20V, V _{DS} = 0V
ON CHARACTERISTICS (Note 8)						
Gate Threshold Voltage	V _{GS(TH)}	1.0	1.5	2.0	V	V _{DS} = V _{GS} , I _D = 250μA
Static Drain-Source On-Resistance	R _{DS(ON)}	-	25 30	30 42	mΩ	V _{GS} = 10V, I _D = 5.8A V _{GS} = 4.5V, I _D = 4.8A
Diode Forward Voltage	V _{SD}	-	0.75	1.2	V	V _{GS} = 0V, I _S = 1A
DYNAMIC CHARACTERISTICS (Note 9)						
Input Capacitance	C _{iss}	-	500	-	pF	V _{DS} = 15V, V _{GS} = 0V, f = 1.0MHz
Output Capacitance	C _{oss}	-	52	-	pF	
Reverse Transfer Capacitance	C _{rss}	-	44	-	pF	
Gate Resistance	R _g	-	2.3	-	Ω	V _{DS} = 0V, V _{GS} = 0V, f = 1MHz
Total Gate Charge (V _{GS} = 4.5V)	Q _g	-	5.0	-	nC	V _{DS} = 15V, I _D = 5.8A
Total Gate Charge (V _{GS} = 10V)	Q _g	-	10.6	-	nC	
Gate-Source Charge	Q _{gs}	-	1.3	-	nC	
Gate-Drain Charge	Q _{gd}	-	1.8	-	nC	
Turn-On Delay Time	t _{D(ON)}	-	2.2	-	ns	V _{DD} = 15V, V _{GS} = 10V, R _L = 2.6Ω, R _G = 3Ω
Turn-On Rise Time	t _R	-	2.6	-	ns	
Turn-Off Delay Time	t _{D(OFF)}	-	9.7	-	ns	
Turn-Off Fall Time	t _F	-	2.0	-	ns	

- Notes:
- Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 - Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 - I_{AS} and E_{AS} ratings are based on low frequency and duty cycles to keep T_J = +25°C.
 - Short duration pulse test used to minimize self-heating effect.
 - Guaranteed by design. Not subject to product testing.

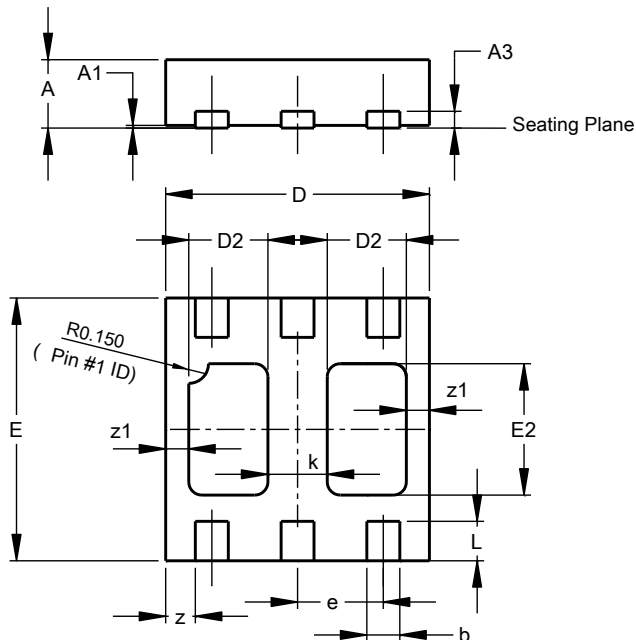






Package Outline Dimensions

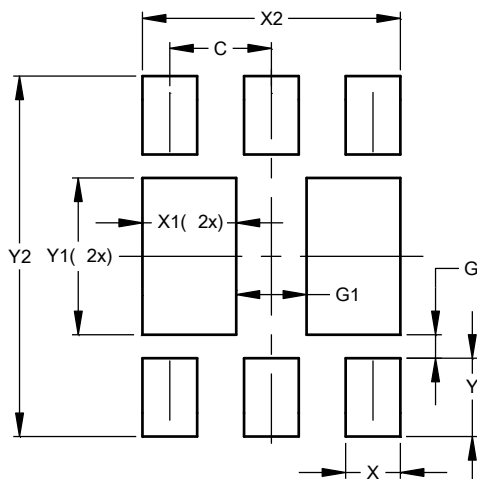
Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.



U-DFN2020-6 Type B			
Dim	Min	Max	Typ
A	0.545	0.605	0.575
A1	0.00	0.05	0.02
A3	-	-	0.13
b	0.20	0.30	0.25
D	1.95	2.075	2.00
D2	0.50	0.70	0.60
e	-	-	0.65
E	1.95	2.075	2.00
E2	0.90	1.10	1.00
k	-	-	0.45
L	0.25	0.35	0.30
z	-	-	0.225
z1	-	-	0.15
All Dimensions in mm			

Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.



Dimensions	Value (in mm)
C	0.650
G	0.150
G1	0.450
X	0.350
X1	0.600
X2	1.650
Y	0.500
Y1	1.000
Y2	2.300

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