

Products > ADC1413D080W0 demoboard; compliant with Lattice, Altera, Xilinx FPGA boards through specific connectors

ADC1413D080W0 Obsolete

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Our ADC demonstration board is suitable for dynamic performances evaluation from low to high IF configuration. A FPGA mother board (Xilinx, Altera, and Lattice) could be connected to ease the evaluation and analysis of the ADC dynamic and enable usage of the JESD204A full features sets

Features

- Single POWER supply (on board regulators)
- On board demodulator + LO synthesizer
- SMA connector for clock and analog inputs signal
- On board Data clock generator

Product Options

Orderable Part ID	Part Status	Pkg. Code	Pkg. Type	Lead Count (#)	Temp. Grade	Pb (Lead) Free	Buy Sample
ADC1413D080W0-DB	Obsolete	BD0	BOARD	0	I	No	Availability

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ADC1413D080HN	Dual 14 bits ADC; 80 Msp; serial JESD204A

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ADC1413D080W0 - Evaluation Board

