

STR-V653

General Descriptions

The STR-V653 is a power IC for switching power supplies, incorporating a power MOSFET and a current mode PWM controller IC in one package.

The SIP8L full mold package features low height and creeping distance of 4mm or longer between high and low voltage pin bases.

To achieve low power consumption, the product includes a startup circuit and a standby function in the controller.

The switching modes are automatically changed according to load conditions so that the PWM mode is in normal operation and the burst mode is in light load condition.

The rich set of protection features helps to realize low component counts, and high performance-to-cost power supply.

Package

SIP8L



Features

- SIP8L package (2.54 pitch, straight lead): Creeping distance of 4mm or longer between high voltage and low voltage pin bases. Low height of less than 12 mm from PCB (Printed Circuit Board)
- Current mode PWM control
- Auto Standby function: improves efficiency by burst mode operation in light load
 - Normal operation: PWM mode
 - Light load operation: Burst mode
- No load power consumption < 25 mW
- Brown-In and Brown-Out function: auto-restart, prevents excess input current and heat rise at low input voltage
- Random Switching function: reduces EMI noise, and simplifies EMI filters
- Slope Compensation function: avoids subharmonic oscillation
- Leading Edge Blanking function
- High Speed Latch Release function

Protection features

- Overcurrent Protection function (OCP): pulse-by-pulse, with input compensation function
- Overvoltage Protection function (OVP): latched shutdown
- Overload Protection function (OLP): auto-restart, with timer
- Thermal Shutdown function (TSD): latched shutdown

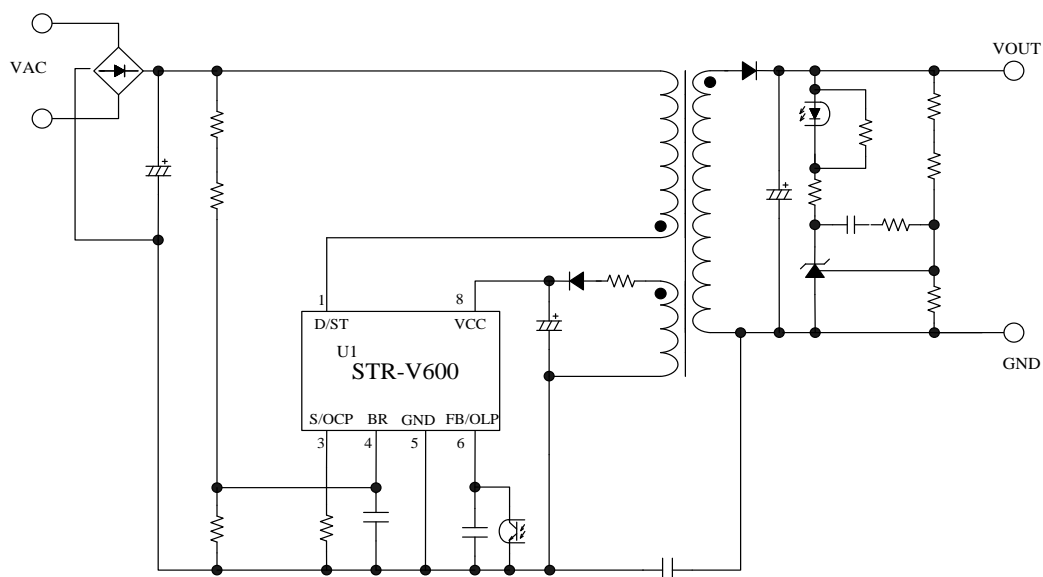
Electrical Characteristics

- Operation Frequency, $f_{OSC(AVG)}$ (typ) = 67 kHz
- Power MOSFET
 - V_{DSS} (min) = 650 V
 - $R_{DS(ON)}$ (max) = 1.9 Ω

Applications

- Standby power supply
- Home appliances
- Digital appliances
- Office automation (OA) equipment
- Industrial apparatus
- Communication facilities

Typical Application circuit



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Absolute Maximum Ratings

- The polarity value for current specifies a sink as "+," and a source as "–," referencing the IC.
- Unless otherwise specified, Ta=25°C

Characteristic	Pins	Symbol	Rating	Unit	Notes
Drain Peak Current ⁽¹⁾	1 – 3	I _{DPEAK}	6.7	A	Single pulse
Avalanche Energy ⁽²⁾	1 – 3	E _{AS}	99	mJ	Single pulse V _{DD} =99V, L=20mH
		I _{LPEAK}	2.9	A	
S/OCP Pin Voltage	3 – 5	V _{OCP}	–2 to 6	V	
Control Part Input Voltage	8 – 5	V _{CC}	32	V	
FB/OLP Pin Voltage	6 – 5	V _{FB}	–0.3 to 14	V	
FB/OLP Pin Sink Current	6 – 5	I _{FB}	1.0	mA	
BR Pin Voltage	4 – 5	V _{BR}	–0.3 to 7	V	
BR Pin Sink Current	4 – 5	I _{BR}	1.0	mA	
Power Dissipation of MOSFET ⁽³⁾	1 – 3	P _{D1}	10.8	W	With infinite heat sink
			1.6	W	Without heat sink
Power Dissipation of Control Part	8 – 5	P _{D2}	1.2	W	
Operating Ambient Temperature	–	T _{op}	–30 to +125	°C	
Storage Temperature	–	T _{stg}	–40 to +125	°C	
Channel Temperature	–	T _{ch}	+150	°C	

⁽¹⁾ Refer to MOSFET Safe Operating Area Curve

⁽²⁾ Refer to MOSFET Avalanche Energy Derating Coefficient Curve

⁽³⁾ Refer to MOSFET Temperature versus Power Dissipation Curve

Electrical characteristics of Control Part

- The polarity value for current specifies a sink as "+," and a source as "–," referencing the IC.
- Unless otherwise specified, Ta is 25 °C, V_{CC} is 18 V

Characteristic	Pins	Symbol	Min.	Typ.	Max.	Unit	Notes
Operation Start Voltage	8 – 5	V _{CC(ON)}	13.8	15.3	16.8	V	
Operation Stop Voltage ⁽¹⁾	8 – 5	V _{CC(OFF)}	7.3	8.1	8.9	V	
Circuit Current in Operation	8 – 5	I _{CC(ON)}	–	–	4	mA	V _{CC} =12V
Minimum Startup Voltage	8 – 5	V _{ST(ON)}	–	38	–	V	
Startup Current	8 – 5	I _{STARTUP}	–3.7	–2.5	–1.5	mA	
Startup Current Supply Threshold Biasing Voltage ⁽¹⁾	8 – 5	V _{CC(BIAS)}	8.5	9.5	10.5	V	
Frequency Modulation Deviation	1 – 5	f _{OSC(AVE)}	60	67	74	kHz	
Oscillation Frequency Fluctuation Range	1 – 5	Δf	–	5	–	kHz	
Maximum Duty Cycle	1 – 5	D _{MAX}	77	83	89	%	
Minimum On-time	–	t _{ON(MIN)}	–	550	–	ns	
Leading Edge Blanking Time	–	t _{BW}	–	330	–	ns	
OCP Compensation Coefficient	–	D _{PC}	–	20	–	mV/ μ s	
OCP Compensation Duty Cycle Limit	–	D _{DPC}	–	36	–	%	

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Characteristic	Pins	Symbol	Min.	Typ.	Max.	Unit	Notes
OCP Threshold Voltage at Zero Duty Cycle	3 – 5	$V_{\text{OCP(L)}}$	0.70	0.78	0.86	V	
OCP Threshold Voltage at 36% Duty Cycle	3 – 5	$V_{\text{OCP(H)}}$	0.81	0.90	0.99	V	
Maximum Feedback Current	6 – 5	$I_{\text{FB(MAX)}}$	–340	–230	–150	μA	
Minimum Feedback Current	6 – 5	$I_{\text{FB(MIN)}}$	–30	–15	–7	μA	
FB/OLP Pin Oscillation Stop Threshold Voltage	6 – 5	$V_{\text{FB(OFF)}}$	0.85	0.95	1.05	V	
OLP Threshold Voltage	6 – 5	$V_{\text{FB(OLP)}}$	7.3	8.1	8.9	V	
OLP Delay Time	6 – 5	t_{OLP}	54	68	82	ms	
OLP Operation Current	8 – 5	$I_{\text{CC(OLP)}}$	–	300	600	μA	
FB/OLP Pin Clamp Voltage	6 – 5	$V_{\text{FB(CLAMP)}}$	11	12.8	14	V	
Brown-In Threshold Voltage	4 – 5	$V_{\text{BR(IN)}}$	5.2	5.6	6	V	
Brown-Out Threshold Voltage	4 – 5	$V_{\text{BR(OUT)}}$	4.45	4.8	5.15	V	
BR Pin Clamp Voltage	4 – 5	$V_{\text{BR(CLAMP)}}$	6	6.4	7	V	
BR Function Disable Threshold Voltage	4 – 5	$V_{\text{BR(DIS)}}$	0.3	0.48	0.7	V	
V_{CC} Pin OVP Threshold Voltage	8 – 5	$V_{\text{CC(OVP)}}$	26	29	32	V	
Latch Circuit Holding Current ⁽²⁾	8 – 5	$I_{\text{CC(LATCH)}}$	–	700	–	μA	
Thermal Shutdown Temperature	–	$T_{\text{J(TSD)}}$	135	–	–	$^{\circ}\text{C}$	

⁽¹⁾ $V_{\text{CC(BIAS)}} > V_{\text{CC(OFF)}}$ always.

⁽²⁾ A latch circuit is a circuit operated with Overvoltage Protection (OVP) and/or Thermal Shutdown Protection (TSD) in operation.

Electrical Characteristics of MOSFET

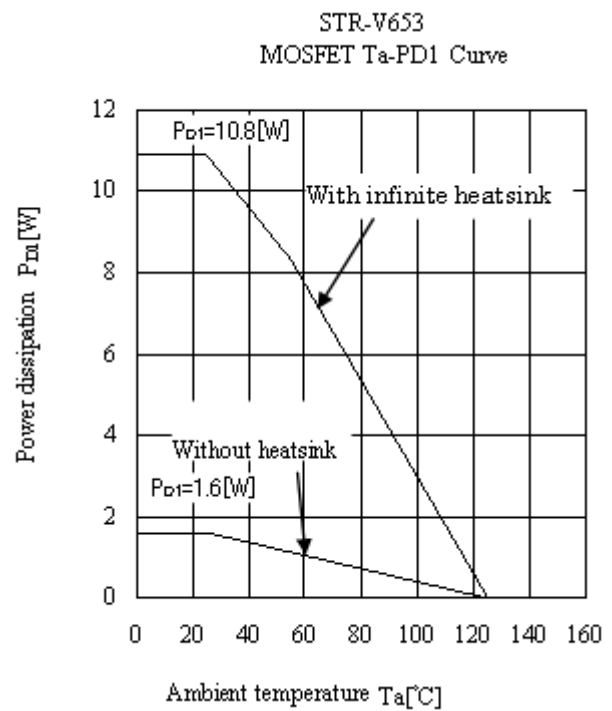
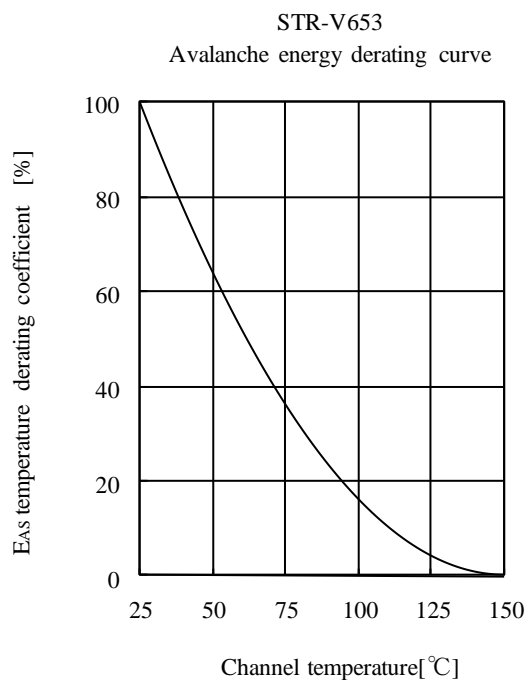
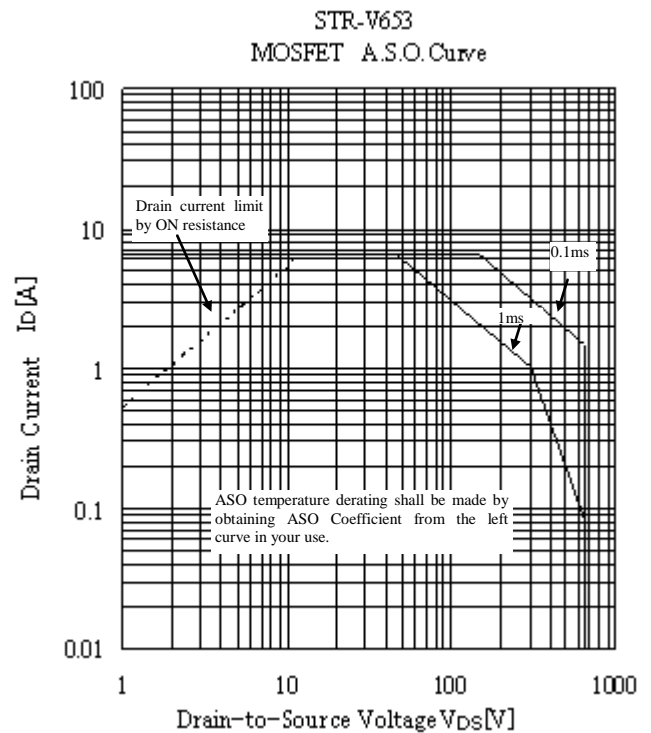
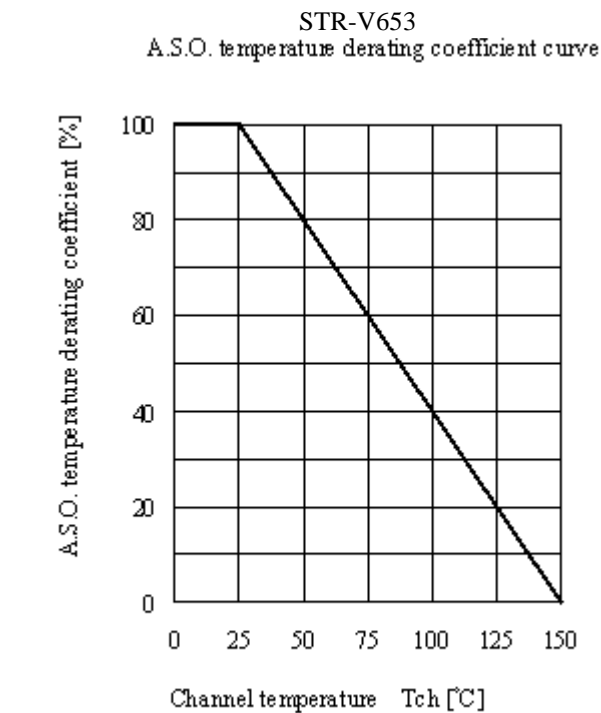
Unless otherwise specified, T_a is 25 $^{\circ}\text{C}$

Characteristic	Pins	Symbol	Min.	Typ.	Max.	Unit	Notes
Drain-to-Source Breakdown Voltage	1 – 3	V_{DSS}	650	–	–	V	
Drain Leakage Current	1 – 3	I_{DSS}	–	–	300	μA	
On-Resistance	1 – 3	$R_{\text{DS(ON)}}$	–	–	1.9	Ω	
Switching Time	1 – 3	t_f	–	–	250	ns	
Thermal Resistance *	–	$\theta_{\text{ch-F}}$	–	–	3.0	$^{\circ}\text{C/W}$	

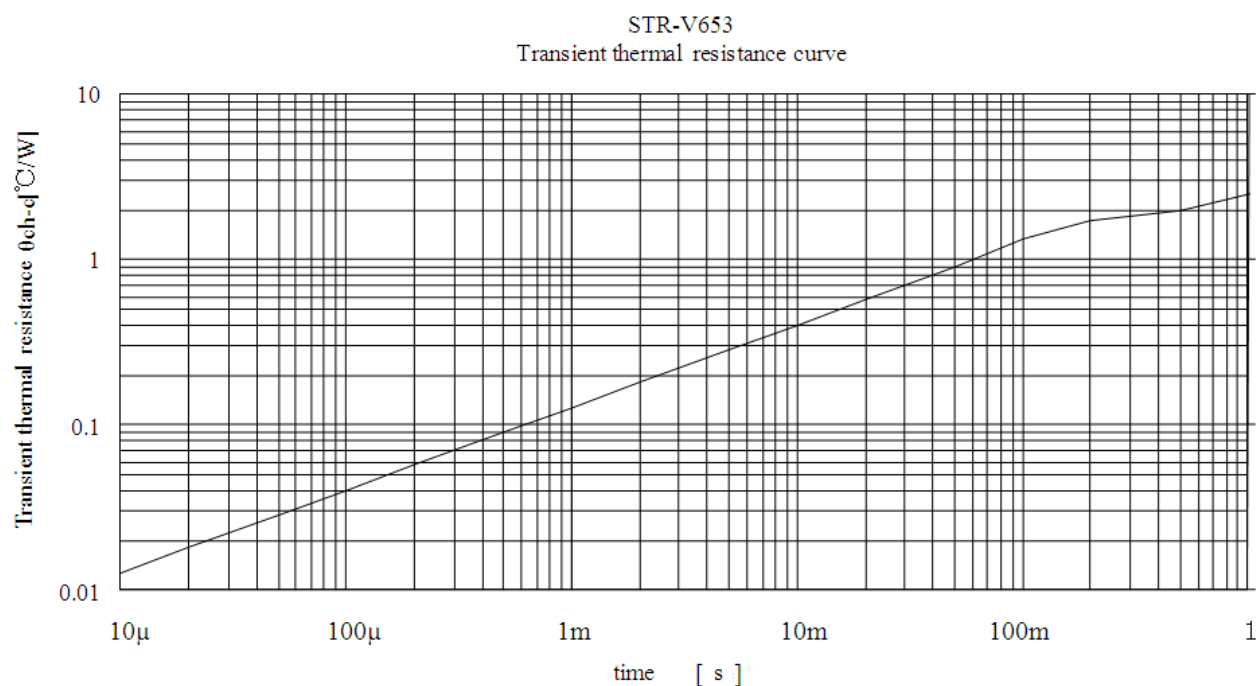
* The thermal resistance between the channels of the MOSFET and the internal frame

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STR-V653 Characteristic Performance

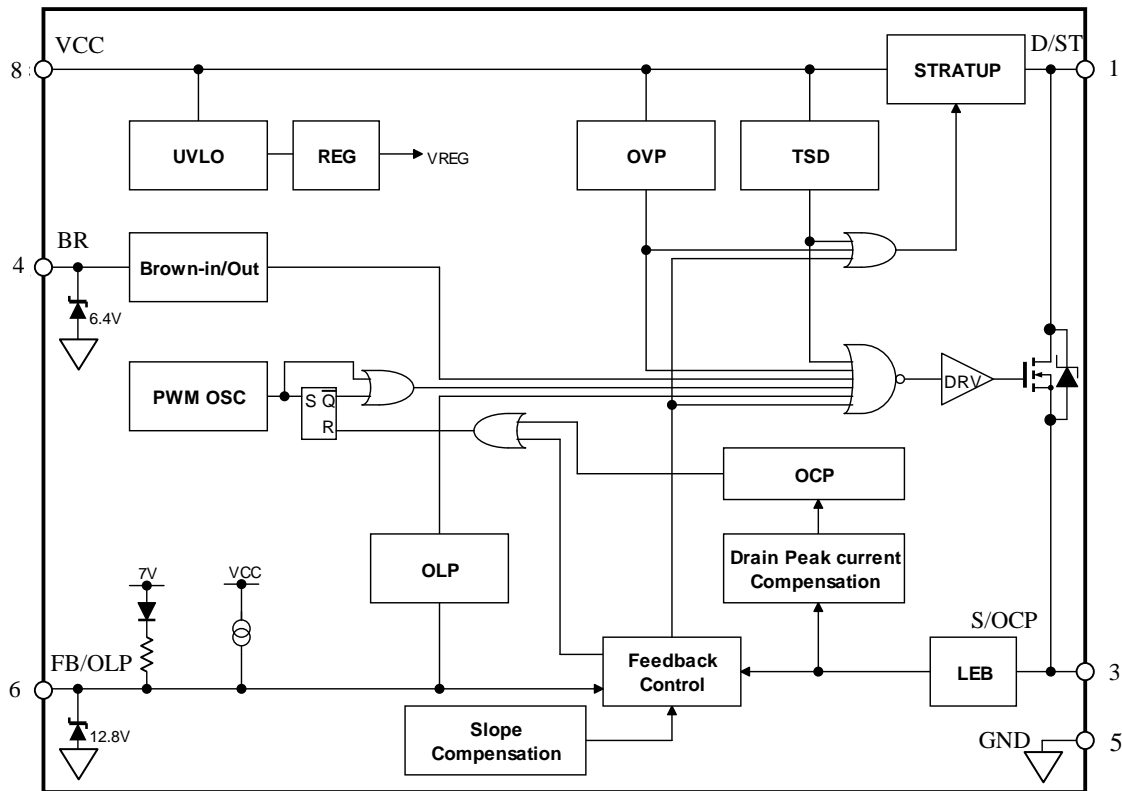


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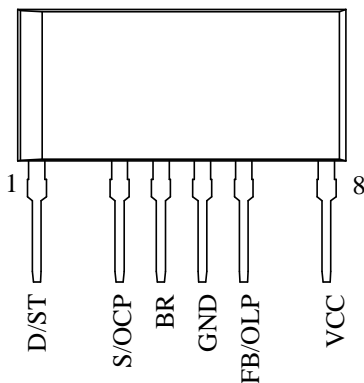


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Functional Block Diagram



Pin List Table



Number	Name	Function
1	D/ST	MOSFET drain pin and input of the startup current
2	—	(Pin removed)
3	S/OCP	MOSFET source and input of Overcurrent Protection (OCP) signal
4	BR	Input of Brown-In and Brown-Out detection voltage
5	GND	Ground
6	FB/OLP	Feedback signal input for constant voltage control signal and input of Overload Protection (OLP) signal
7	—	(Pin removed)
8	VCC	Power supply voltage input for Control Part and input of Overvoltage Protection (OVP) signal

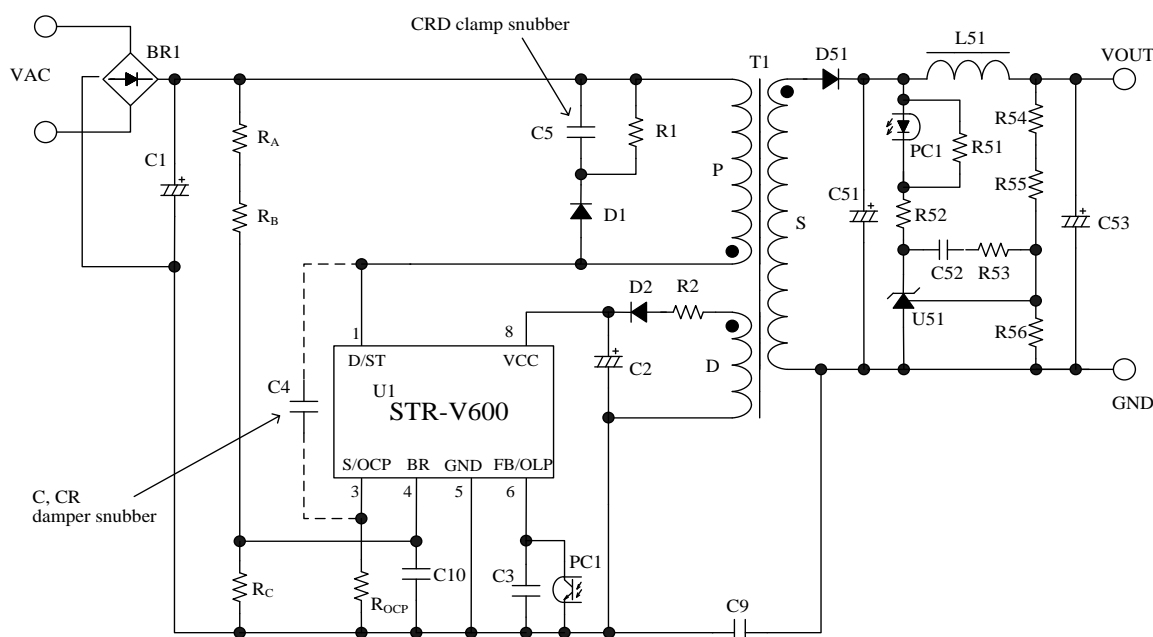
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Typical Application Circuit

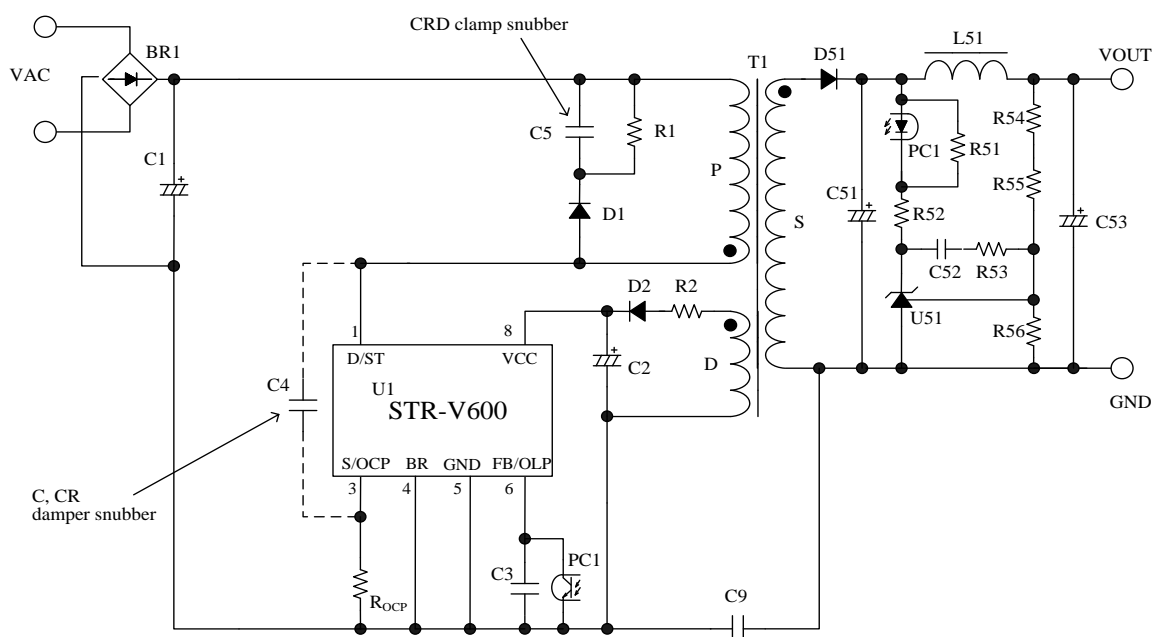
The following drawings show circuits enabled and disabled the Brown-In/Brown-Out function.

The following design features should be observed:

- The PCB traces from the D/ST pin (pin 1) should be as wide as possible, in order to enhance thermal dissipation.
- In applications having a power supply specified such that V_{DS} has large transient surge voltages, a clamp snubber circuit of a capacitor-resistor-diode (CRD) combination should be added on the primary-side winding P, or a damper snubber circuit of a capacitor (C) or a resistor-capacitor (RC) combination should be added between the D/ST pin and the S/OCP pin.



Typical application circuit example, enabled Brown-In/Brown-Out function (DC line detection)

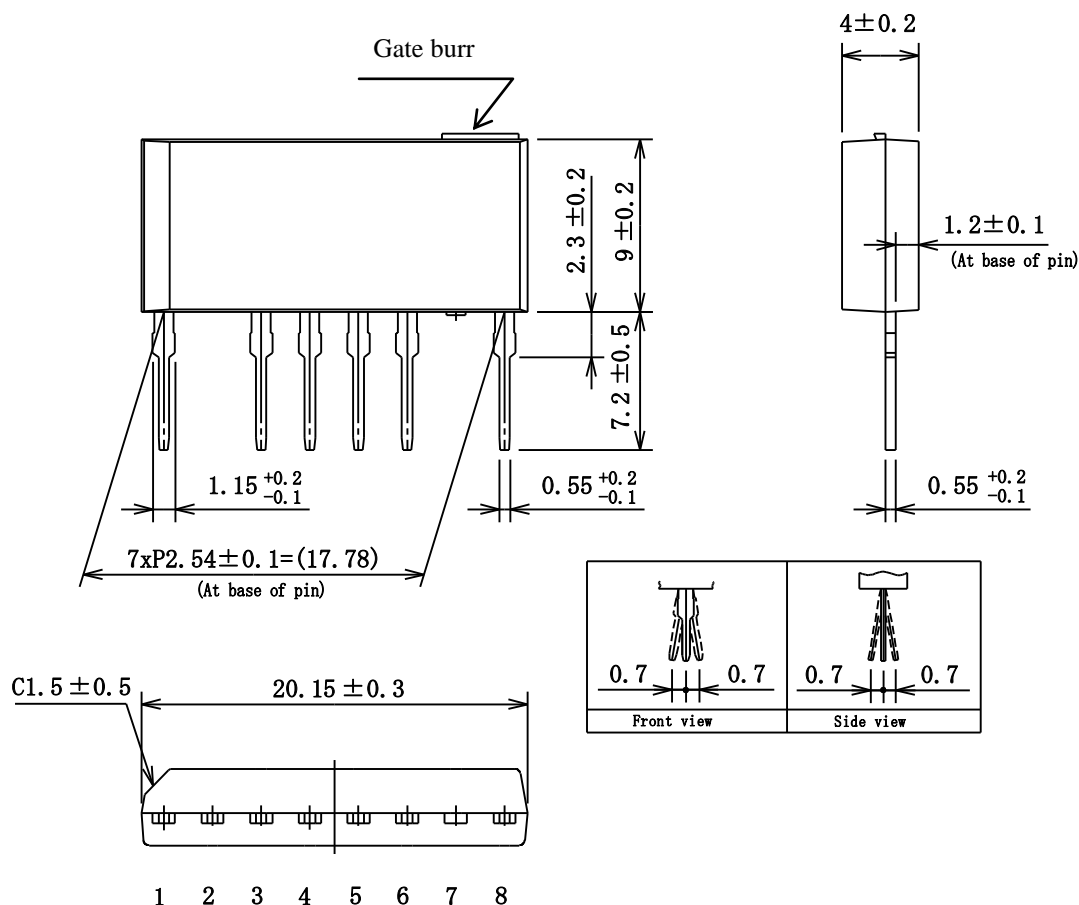


Typical application circuit example, disabled Brown-In/Brown-Out function

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Package Diagram

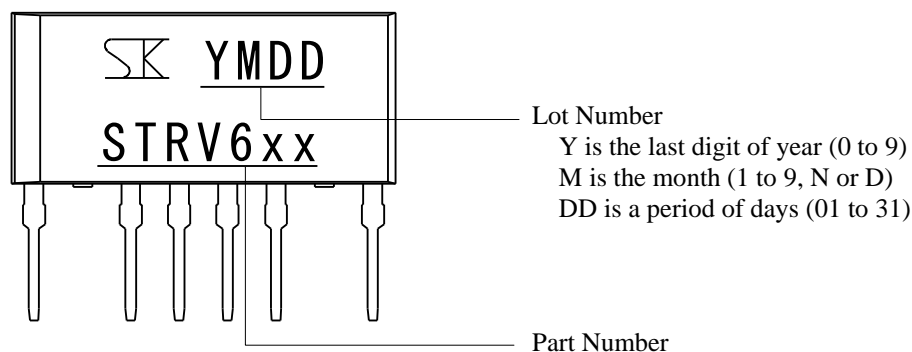
- SIP8L package (2.54 pitch, straight lead)
- The pin 2 removed to provide greater creepage and clearance isolation between the high voltage pins (pins 1: D/ST) and the low voltage pin (pin 3: S/OCP).
- Creeping distance of 4mm or longer between high voltage and low voltage pin bases.
- Low height of less than 12 mm from PCB (Printed Circuit Board)



NOTES:

- Unit: mm
- Gate burr indicates protrusion of 0.3 mm (max).
- Pin treatment Pb-free. Device composition compliant with the RoHS directive.

Marking Diagram



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OPERATING PRECAUTIONS

In the case that you use Sanken products or design your products by using Sanken products, the reliability largely depends on the degree of derating to be made to the rated values. Derating may be interpreted as a case that an operation range is set by derating the load from each rated value or surge voltage or noise is considered for derating in order to assure or improve the reliability. In general, derating factors include electric stresses such as electric voltage, electric current, electric power etc., environmental stresses such as ambient temperature, humidity etc. and thermal stress caused due to self-heating of semiconductor products. For these stresses, instantaneous values, maximum values and minimum values must be taken into consideration. In addition, it should be noted that since power devices or IC's including power devices have large self-heating value, the degree of derating of junction temperature affects the reliability significantly.

Because reliability can be affected adversely by improper storage environments and handling methods, please observe the following cautions.

Cautions for Storage

- Ensure that storage conditions comply with the standard temperature (5 to 35°C) and the standard relative humidity (around 40 to 75%); avoid storage locations that experience extreme changes in temperature or humidity.
- Avoid locations where dust or harmful gases are present and avoid direct sunlight.
- Reinspect for rust on leads and solderability of the products that have been stored for a long time.

Cautions for Testing and Handling

When tests are carried out during inspection testing and other standard test periods, protect the products from power surges from the testing device, shorts between the product pins, and wrong connections. Ensure all test parameters are within the ratings specified by Sanken for the products.

Remarks About Using Silicone Grease with a Heatsink

- When silicone grease is used in mounting the products on a heatsink, it shall be applied evenly and thinly. If more silicone grease than required is applied, it may produce excess stress.
- Volatile-type silicone greases may crack after long periods of time, resulting in reduced heat radiation effect. Silicone greases with low consistency (hard grease) may cause cracks in the mold resin when screwing the products to a heatsink. Our recommended silicone greases for heat radiation purposes, which will not cause any adverse effect on the product life, are indicated below:

Type	Suppliers
G746	Shin-Etsu Chemical Co., Ltd.
YG6260	Momentive Performance Materials Inc.
SC102	Dow Corning Toray Co., Ltd.

Soldering

- When soldering the products, please be sure to minimize the working time, within the following limits:
 - 260 \pm 5 °C 10 \pm 1 s (Flow, 2 times)
 - 380 \pm 10 °C 3.5 \pm 0.5 s (Soldering iron, 1 time)
- Soldering should be at a distance of at least 1.5 mm from the body of the products.

Electrostatic Discharge

- When handling the products, the operator must be grounded. Grounded wrist straps worn should have at least 1M Ω of resistance from the operator to ground to prevent shock hazard, and it should be placed near the operator.
- Workbenches where the products are handled should be grounded and be provided with conductive table and floor mats.
- When using measuring equipment such as a curve tracer, the equipment should be grounded.
- When soldering the products, the head of soldering irons or the solder bath must be grounded in order to prevent leak voltages generated by them from being applied to the products.
- The products should always be stored and transported in Sanken shipping containers or conductive containers, or be wrapped in aluminum foil.

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