

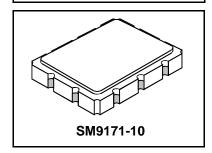
- Designed for Interactive Video Applications
- Wide Bandwidth and Excellent GD Variation
- 9.1 x 7.1 mm Surface-mount Case
- Single Ended Input and Output

Absolute Maximum Ratings

Rating	Value	Units
Maximum Incident Power in Passband	+10	dBm
Max. DC voltage between any 2 terminals	30	VDC
Storage Temperature Range	-40 to +85	°C
Max. Soldering Profile	265°C for 10 s	

SF1126A

127 MHz SAW Filter



Electrical Characteristics

Characteristic			Notes	Min	Тур	Max	Units
Nominal Center Frequency		f _C	1		127.000		MHz
Passband	Insertion Loss at fc	IL			14	15.0	dB
	1.3 db Passband	BW _{1.3}		±15.0			MHz
Group Delay Variation over fc ±fc12.0 MHz		GDV	1, 2		11	30	ns _{P-P}
Phase Linearity over fc±12.0 MHz			İ			10	°P-P
Rejection	< 107.0 MHz			40			
	> 147.25 MHz		1, 2, 3	40			dB
	Ultimate		<u> </u>		40		
Operating Temperature Range		T _A	1	+25		+30	°C
Frequency Temperature Coefficient		FTC	'		-94		ppm/°C

Impedance Matching to 50Ω Unbalanced	External L-C
Case Style	SM9171-10 9.1 x 7.1 mm Nominal Footprint
Lid Symbolization (YY = year, WW = week)	RFM SF1126A YYWW

Notes:

- 1. Unless noted otherwise, all specifications apply over the operating temperature range with filter soldered to the specified demonstration board with impedance matching to $50~\Omega$ and measured with $50~\Omega$ network analyzer.
- Unless noted otherwise, all frequency specifications are referenced to the nominal center frequency, fc.
- Rejection is measured as attenuation below the minimum IL point in the passband.
 Rejection in final user application is dependent on PCB layout and external impedance matching design. See Application Note No. 42 for details.
- Part to part absolute delay measurement records the absolute delay mean across 1 dB passband.
- "LRIP" or "L" after the part number indicates "low rate initial production" and "ENG" or "E" indicates "engineering prototypes."
- 6. The design, manufacturing process, and specifications of this filter are subject to change.
- 7. Either Port 1 or Port 2 may be used for either input or output in the design. However, impedances and impedance matching may vary between Port 1 and Port 2, so that the filter must always be installed in one direction per the circuit design.
- 8. US and international patents may apply.
- 9. Electrostatic Sensitive Device. Observe precautions for handling.



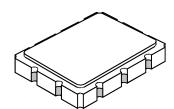
Electrical Connections

Connection	Terminals
Port 1 Hot (Input)	1
Port 1 Gnd Return	10
Port 2 Hot (Output)	6
Port 2 Gnd Return	5
Case Ground	All others

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SM9171-10 Case

10-Terminal Ceramic Surface-Mount Case 9.1 x 7.1 mm Nominal Footprint

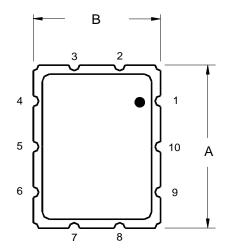


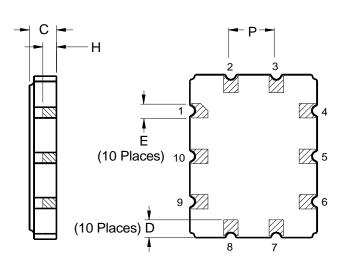
Case Dimensions

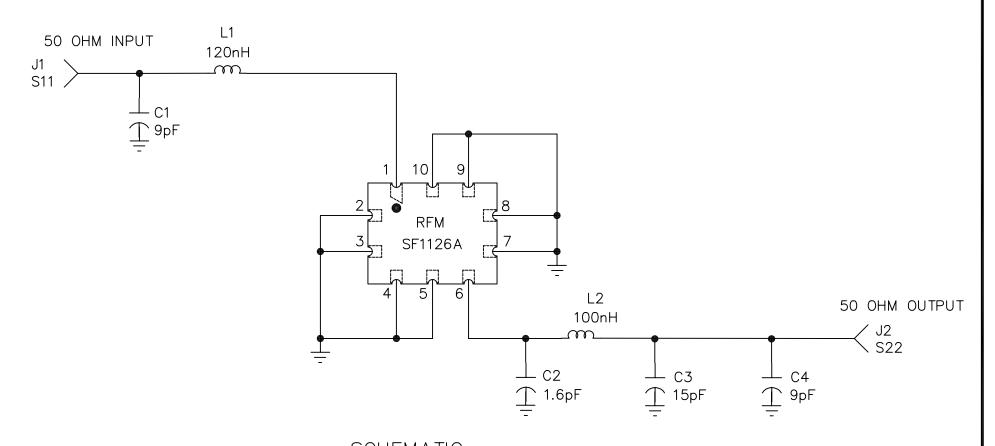
Dimension		mm			Inches	
Dilliension	Min	Nom	Max	Min	Nom	Max
Α	8.86	9.09	9.40	0.349	0.358	0.370
В	6.88	7.11	7.40	0.271	0.280	0.291
С		1.91	2.00		0.075	0.079
D		0.99			0.039	
E		0.79			0.031	
Н		1.0			0.039	
Р		2.54			0.100	

Electrical Connections

Connection		Terminals
Port 1	Output or Return	1
	Return or Output	10
Port 2	Input or Return	6
	Return or Input	5
	Ground	All others
Single Ended Operation		Return is ground
Differential Operation		Return is hot







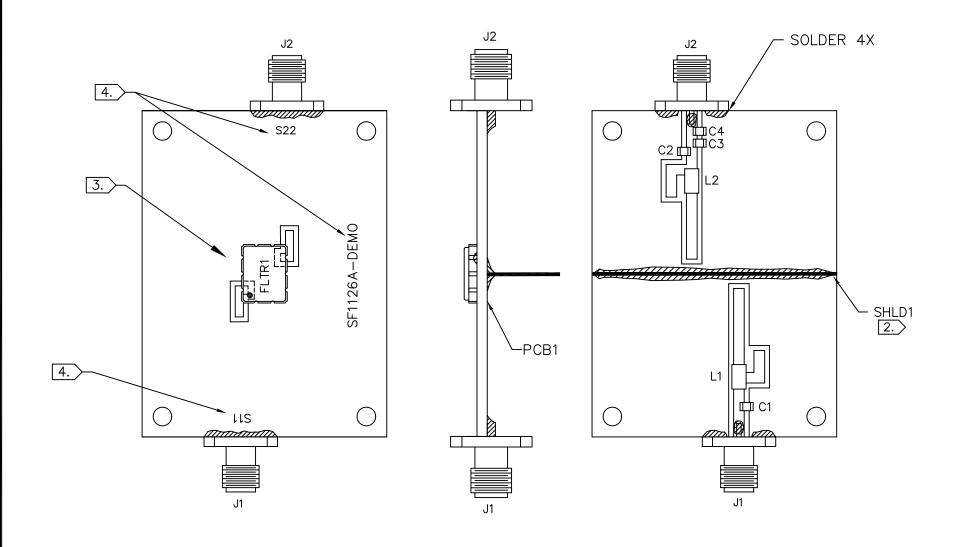
SCHEMATIC

D.U.T. VIEWED FROM TOP

DRAWN BY/DATE:	J.F.Christop	herson	08oct99	TITLE:	ASSEME	BLY [DIAGRAM, SF1126A	·–DE	MO
RF Monolit dallas, tex	, , , , , , , , , , , , , , , , , , ,			size A	code ident 2U874	DWG. NO.	SF1126A-000	rev A	SHEET 1/3

NOTES:

- 1. SOLDER MOUNT COMPONENTS AND CONNECTORS TO PCB1
- 2. SOLDER SHLD1 AS SHOWN AND TRIM TAB FROM SHIELD SO THAT IT IS FLUSH WITH PCB.
- 3. ORIENT THE FLTR1 AND SOLDER IT DOWN TO THE BOARD AS SHOWN
- 4. LABEL AS SHOWN.



RF	Monolithics, Inc.	
	DALLAS, TEXAS 75244	

SIZE	CODE IDENT
А	2U874

DWG.	CE1126A 000
NO.	SF1126A-000

