

Vishay Siliconix

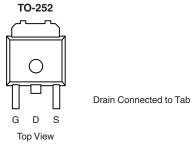
N-Channel 60-V (D-S), 175 °C MOSFET

PRODUCT SUMMARY				
V _{(BR)DSS} (V)	(V) $r_{DS(on)}(\Omega)$ $I_D(A)$			
60	0.0074 at V _{GS} = 10 V	96		
	$0.0088 \text{ at V}_{GS} = 4.5 \text{ V}$	88		

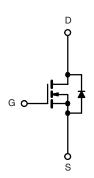
FEATURES

- TrenchFET® Power MOSFETS
- 175 °C Junction Temperature









N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS TA	= 25 °C, unless othe	rwise noted			
Parameter	Symbol	Limit	Unit		
Drain-Source Voltage		V _{DS}	60	V	
Gate-Source Voltage		V _{GS}	± 20		
Continuous Drain Current (T _{.I} = 175 °C)	T _C = 25 °C	I-	96 ^c	A	
Continuous Diam Current (1) = 173 C)	T _C = 100 °C	- ' _D -	67 ^c		
Pulsed Drain Current		I _{DM}	100	A	
Single Pulse Avalanche Current		I _{AS}	45		
Single Pulse Repetitive Avalanche Energy ^a	L = 0.1 mH	E _{AS}	101	mJ	
Power Dissipation $T_C = 25 ^{\circ}C$		P _D	136	W	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 175	°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Location to Ambienth	t ≤ 10 sec	- R _{thJA}	15	18	°C/W
Junction-to-Ambient ^b	Steady State		40	50	
Junction-to-Case		R_{thJC}	0.85	1.1	

Notes:

- a. Duty cycle \leq 1 %.
- b. Surface Mounted on 1" FR4 board.
- c. Based on maximum allowable Junction Temperature. Package limitation current is 50 A.

SUD50N06-07L

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Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static	1 -7						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	60			.,	
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$	1		3	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 60 V, V _{GS} = 0 V			1		
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 125 °C			50	μΑ	
		V _{DS} = 60 V, V _{GS} = 0 V, T _J = 175 °C			150	-	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = 5 \text{ V}, V_{GS} = 10 \text{ V}$	50			Α	
Drain-Source On-State Resistance ^a		V _{GS} = 10 V, I _D = 20 A		0.0061	0.0074	Ω	
		V _{GS} = 10 V, I _D = 20 A, T _J = 125 °C			0.0122		
	r _{DS(on)}	V _{GS} = 10 V, I _D = 20 A, T _J = 175 °C			0.0148		
		$V_{GS} = 4.5 \text{ V}, I_D = 20 \text{ A}$		0.0071	0.0088		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 15 A	20	80		S	
Dynamic ^b	•			•			
Input Capacitance	C _{iss}			5800		pF	
Output Capacitance	C _{oss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		450			
Reversen Transfer Capacitance	C _{rss}			300			
Total Gate Charge ^c	Qg			96	144	nC	
Gate-Source Charge ^c	Q_{gs}	$V_{DS} = 30 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 50 \text{ A}$		19			
Gate-Drain Charge ^c	Q_{gd}			20			
Gate Resistance	R _g			1.5		Ω	
Turn-On Delay Time ^c	t _{d(on)}			15	25	ns	
Rise Time ^c	t _r	$V_{DD} = 30 \text{ V, R}_{L} = 0.6 \Omega$ $I_{D} \cong 50 \text{ A, V}_{GEN} = 10 \text{ V, R}_{g} = 2.5 \Omega$		13	20		
Turn-Off Delay Time ^c	t _{d(off)}			62	95		
Fall Time ^c	t _f			14	25		
Source-Drain Diode Ratings and Cha	aracteristics	(T _C = 25 °C) ^b					
Continuous Current	I _S				50	Α	
Pulsed Current	I _{SM}				100	Α	
Forward Voltage ^a	V_{SD}	$I_F = 30 \text{ A}, V_{GS} = 0 \text{ V}$		0.90	1.50	V	
Reverse Recovery Time	t _{rr}	I _F = 30 A, di/dt = 100 A/μs		37	55	ns	

Notes:

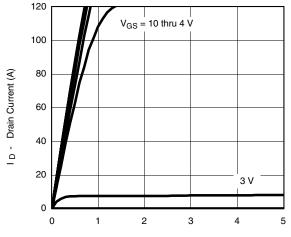
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

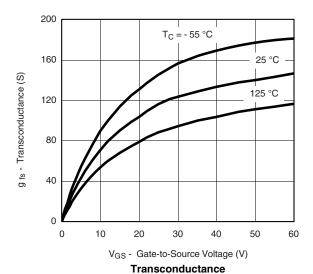


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TYPICAL CHARACTERISTICS 25 °C unless noted



V_{DS} - Drain-to-Source Voltage (V) **Output Characteristics**



Ciss 6000 C - Capacitance (pF) 5000 4000 3000 2000 1000

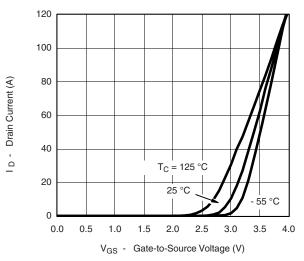
20

30 V_{DS} - Drain-to-Source Voltage (V) Capacitance

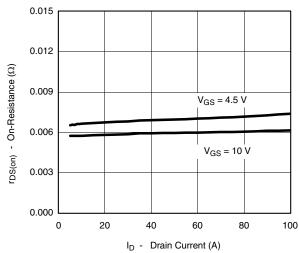
40

50

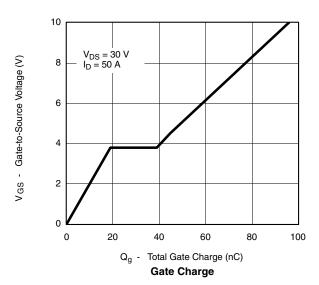
60



Transfer Characteristics



On-Resistance vs. Drain Current



0

0

10

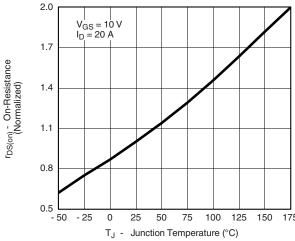
8000 7000

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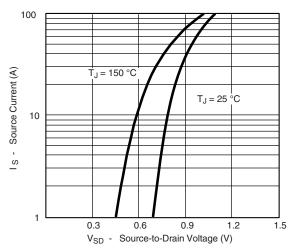
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TYPICAL CHARACTERISTICS 25 °C unless noted

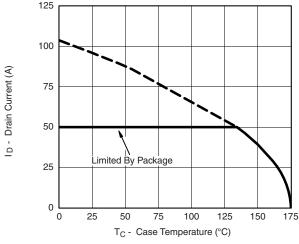


On-Resistance vs. Junction Temperature

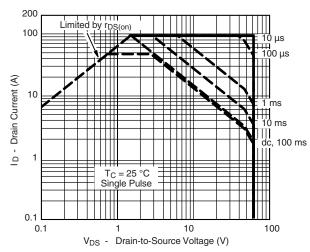


Source-Drain Diode Forward Voltage

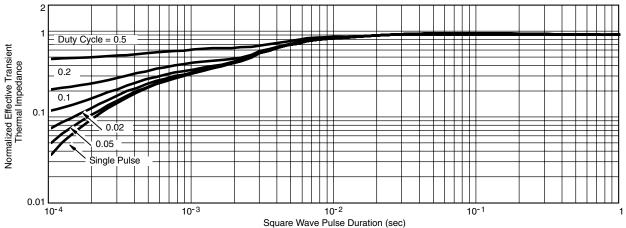
THERMAL RATINGS



Maximum Avalanche and Drain Current vs. Case Temperature



Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see http://www.vishay.com/ppg?72953.



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