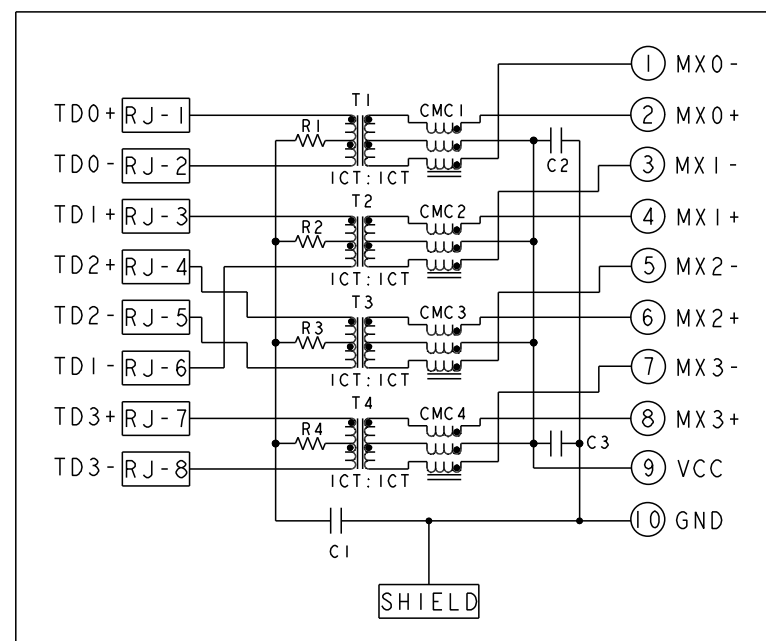


TOP AND BOTTOM PORTS



PIN DESIGNATIONS
(REPEAT FOR EACH VERTICAL PAIR OF PORTS.)

PCB PINS
(TOP VIEW,
COMP. SIDE)

The diagram shows a 100-pin connector with two sections. The top section, labeled 'TOP PORTS', contains pins 1 through 50. The bottom section, labeled 'BOTTOM PORTS', contains pins 51 through 90. The pins are arranged in a grid-like pattern, with pin numbers increasing from left to right and top to bottom within each section.

TOP PORTS

P_{1-8}

 P_{1-1}



RJ CABLE
CONTACTS

21-1  R1-8

BOTTOM PORTS

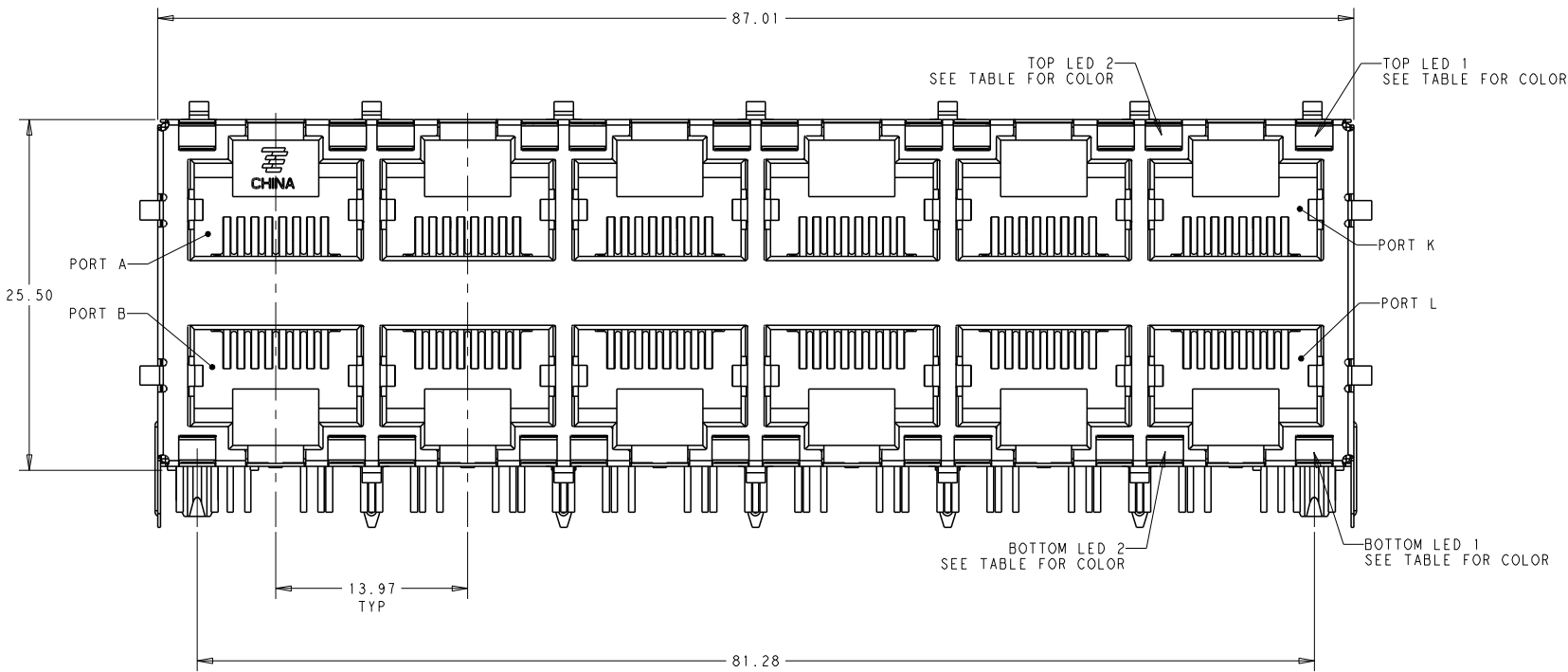
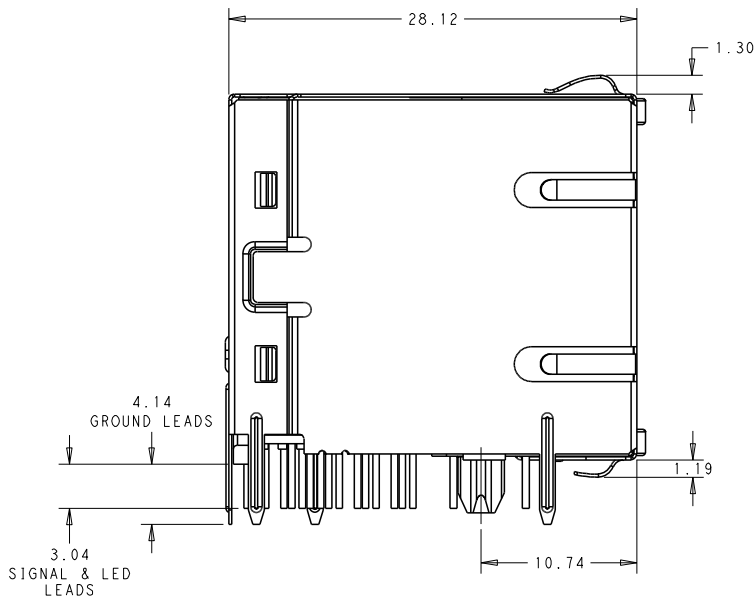
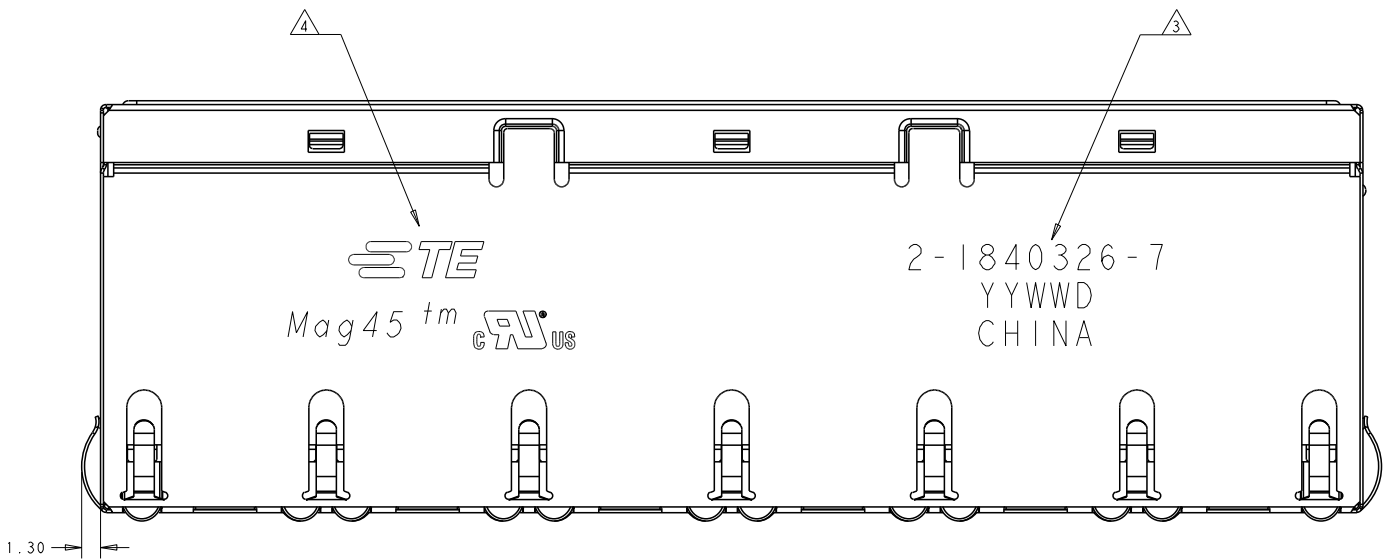
- I. MATERIALS: BLACK, THERMOPLASTIC FLAMMABILITY RATING UL 94V-0
SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,
POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS,
CONTACTS: PHOSPHOR BRONZE, 1.27um MIN OVERALL NICKEL UNDERPLATE,
WITH SELECT 0.05um MIN GOLD OVER 0.76um MIN PALLADIUM-NICKEL AT
MATING INTERFACE AND 2.54um MIN MATTE TIN ON SOLDER TAILS.
LED: DIFFUSED EPOXY LENS, CARBON STEEL LEAD FRAME TAILS OF LED
ARE PREPLATED WITH 2.03um MIN SILVER OVER 1.02um MIN NICKEL
UNDERPLATE OVER 1.02um MIN COPPER UNDERPLATE. POST-PLATED WITH
2.54um MIN MATTE TIN AND/OR SAC SOLDER DIP OR PURE TIN SOLDER DIP
ALL PC BOARDS: HIGH TEMPERATURE PCB,TG>170°C
2. MAGNETICS
APPLICATION: 10/100/1000 BASE-T
IMPEDANCE: 100 OHMS
TURNS RATIO (CHIP: CABLE): 1:1 ALL FOUR PAIRS
OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS,
8mA DC BIAS FROM 0°C TO 70°C, ALL FOUR PAIRS
ALL FOUR PAIRS BI-DIRECTIONAL
PERFORMANCE @ 25°C:
INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz
RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz
12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz
CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz
33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz
COMMON MODE REJECTION RATIO (CMRR): 30dB MIN FROM 0.5MHz TO 100MHz
ISOLATION VOLTAGE: 2250VDC(MAX) FOR 60 SECONDS WITH A RISE TIME OF
500V/SEC AND WITH ALL PORTS CONNECTED.
3. PART NUMBER, DATE CODE AND COUNTRY OF ORIGIN ARE
LOCATED IN APPROXIMATE AREA SHOWN. DATE CODE: YYWWDD WHERE "YY" IS YEAR,
"WW" IS WEEK, "D" IS DAY OF WEEK, WITH SUNDAY=1.
4. TE CONNECTIVITY LOGO AND AGENCY APPROVAL LOGO ARE
LOCATED IN APPROXIMATE AREA SHOWN.
5. OPERATING TEMP: FROM 0°C TO +70°C.
6. RJ45 CAVITY CONFORMS TO FCC RULES AND REGULATION PART 68 SUBPART F.
7. INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND
SUPPORT AUTO-MDI/MDIX.
8. DATUM AND BASIC DIMENSION ESTABLISHED BY CUSTOMER.
9. BASIC DIMENSION ESTABLISHED BY CUSTOMER, BUT MAY NOT BE
GREATER THAN 5.08mm.
10. LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX 20mA
LED COLOR: DOMINANT WAVELENGTH (λ D): GREEN 568 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): GREEN 2.2V TYP. @ IF=20mA
DOMINANT WAVELENGTH (λ D): ORANGE 605 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): ORANGE 2.1V TYP. @ IF=20mA
DOMINANT WAVELENGTH (λ D): YELLOW 588 nm TYP. @ IF=20mA
FORWARD VOLTAGE (VF): YELLOW 2.1V TYP. @ IF=20mA
- II. THE PARTS ARE RECOMMENDED FOR WAVE SOLDERING PROCESS,
PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

SEE SHEET 3	3.04	GRN/YEL	GRN/YEL	GRN/YEL	GRN/YEL	1840326-4
SEE SHEET 2	3.04	GRN/ORA	GRN/ORA	GRN/ORA	GRN/ORA	2-1840326-7
SEE SHEET 3	3.56	GRN/ORA	GRN/ORA	GRN/ORA	GRN/ORA	1-1840326-7
SEE SHEET 3	3.56	GRN/YEL	GRN/YEL	GRN/YEL	GRN/YEL	1-1840326-4
SEE SHEET 3	3.04	GREEN	GREEN	GREEN	GREEN	1840326-1
OUTLINE DIMENSION	DIM A	BOTTOM LED 2	BOTTOM LED 1	TOP LED 2	TOP LED 1	PART NUMBER

DRAWING IS A CONTROLLED DOCUMENT.		DWN TOMMY REN 30 JAN 2010		NUMBER	
DIMENSIONS:		CHK TONY YUAN 30 JAN 2010		 TE Connectivity	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD KEITH ZHU 30 JAN 2010		NAME	
(mm)  0 PLC ± 1 PLC ± 2 PLC ±0.25 3 PLC ± 4 PLC ± ANGLES ±		PRODUCT SPEC		2X6 MAG45(TM), GIGABIT S8G56 CIRCUIT WAVE PANEL, GROUND SHIELD, W/ LEDS	
MATERIAL		FINISH		SIZE CASE CODE DRAWING NO A1 00779 C=1840326	
WEIGHT		CUSTOMER DRAWING		SCALE 1:1 SHEET 1 OF 1 REV 1	

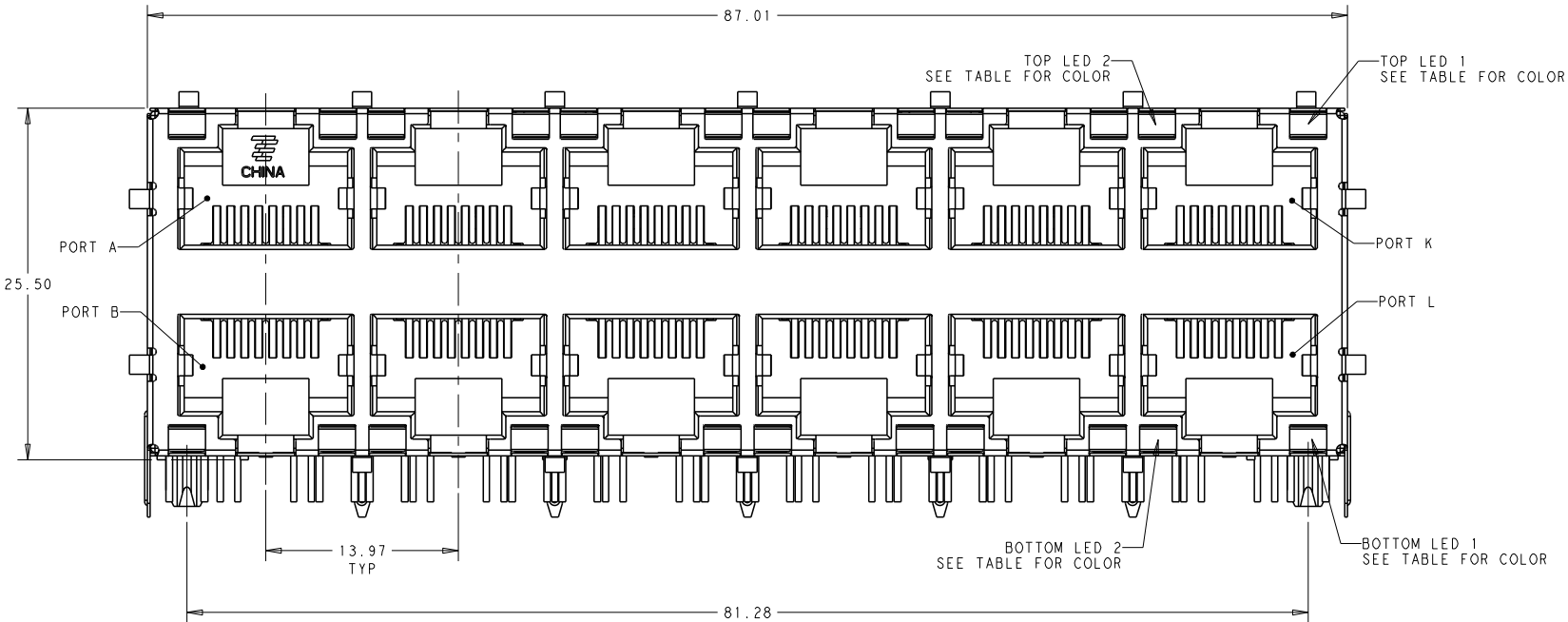
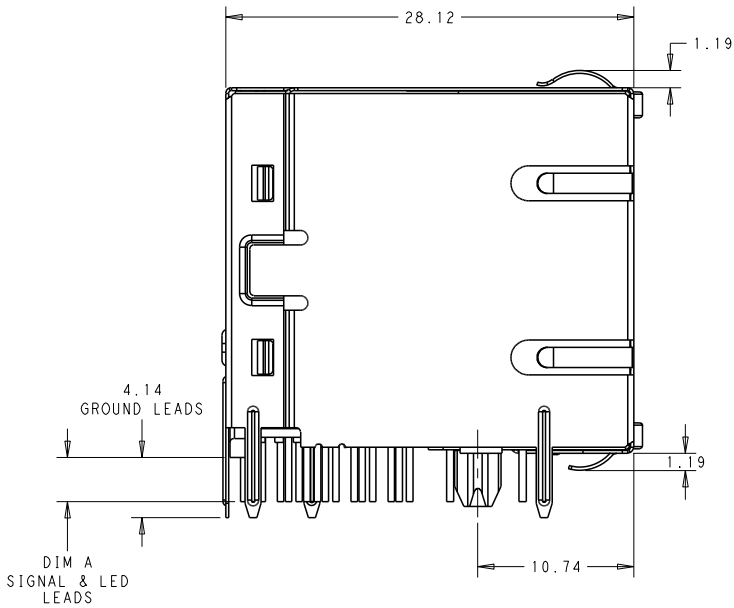
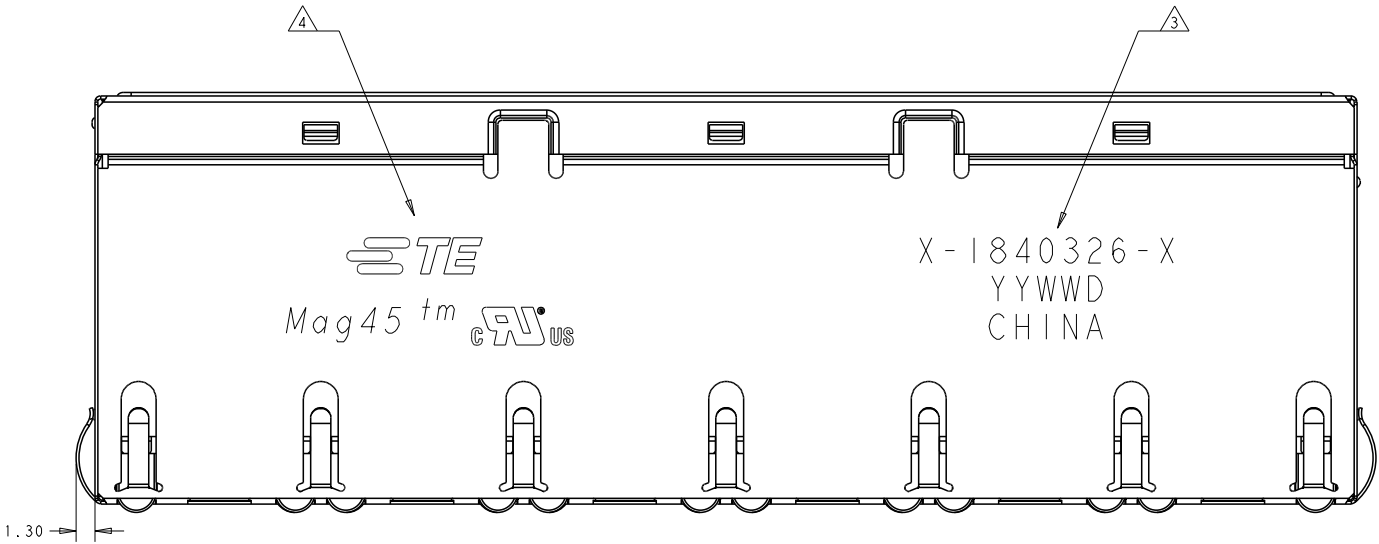
LOC		DIST		REVISONS				
P	LTR	DESCRIPTION			DATE	DWN	APVD	
-	-	SEE SHEET 1			-	-	-	

THIS SHEET FOR 2-1840326-7 ONLY



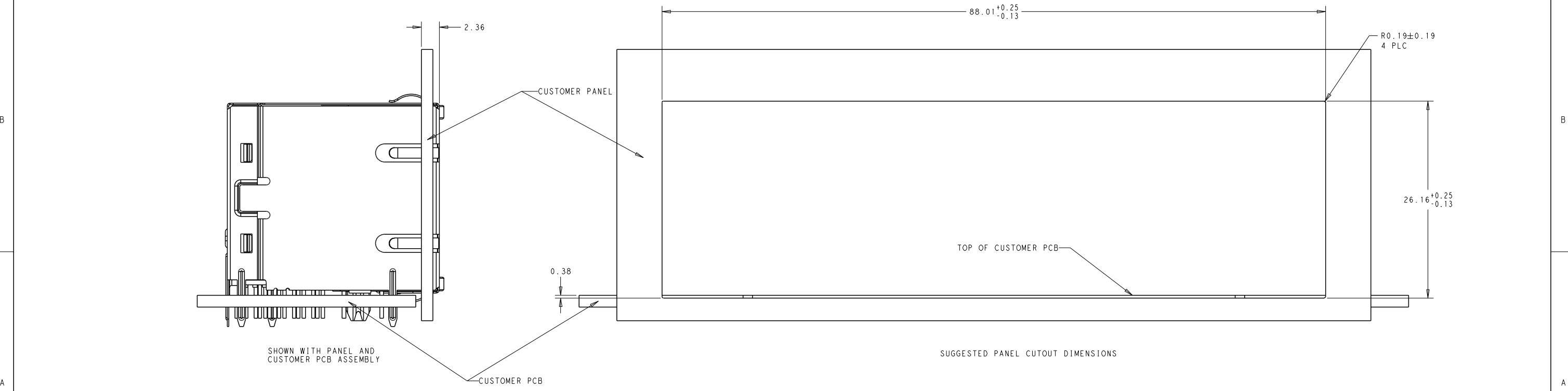
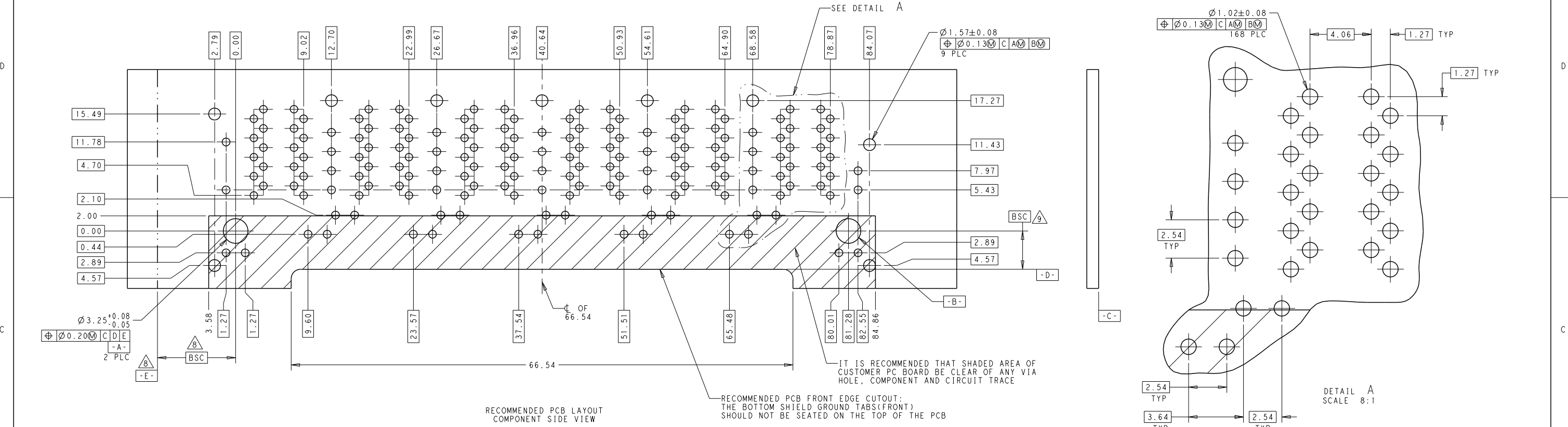
THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN	30JAN2010	TE Connectivity	
		CHK	30JAN2010		
DIMENSIONS:		APVD	30JAN2010	NAME	
mm				2X6 MAG45(TM), GIGABIT S8G56 CIRCUIT	
				WAVE PANEL, GROUND SHIELD, W/ LEDS	
				SIZE	
				A1	
				CAGE CODE	
				00779	
				DRAWING NO	
				1840326	
				RESTRICTED TO	
				-	
MATERIAL		FINISH		SCALE	
-		-		4:1	
				SHEET	
				2	
				OF	
				5	
				REV	
				H	

LOC		DIST		REVISONS				
P	LTR	DESCRIPTION			DATE	DWN	APVD	
-	-	SEE SHEET 1			-	-	-	



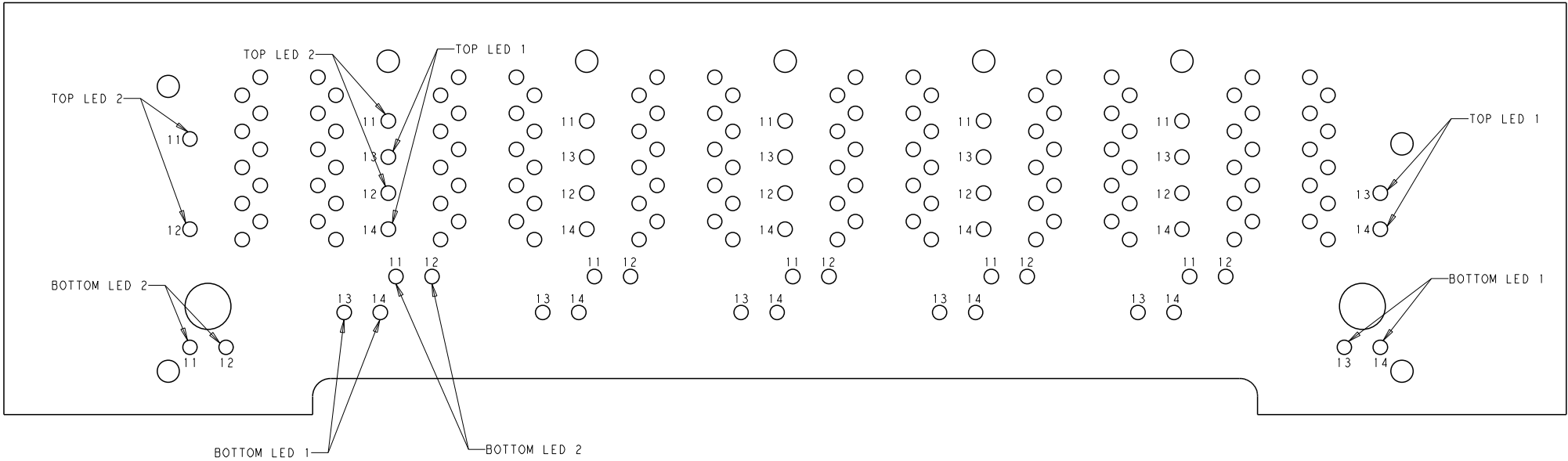
THIS DRAWING IS A CONTROLLED DOCUMENT.				TE Connectivity			
DWN: TOMMY REN		30JAN2010		CHK: TONY YUAN		30JAN2010	
APVD: KEITH ZHU		30JAN2010		NAME:		2X6 MAG45(TM), GIGABIT S8G56 CIRCUIT	
PRODUCT SPEC		APPLICATION SPEC		SIZE		CAGE CODE	
WEIGHT		CUSTOMER DRAWING		DRAWING NO		RESTRICTED TO	
SCALE		1:1		SHEET		OF	
3		5		REV		H	

LOC		DIST		REVISONS				
P	LTR	DESCRIPTION			DATE	DWN	APVD	
-	-	SEE SHEET 1			-	-	-	-

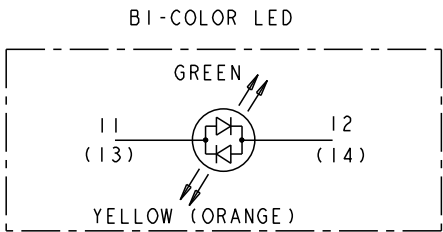
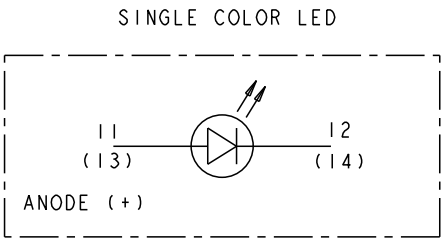


THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN	30JAN2010	TE Connectivity	
		CHE	30JAN2010		
		APVD	30JAN2010		
		NAME	30JAN2010		
		PRODUCT SPEC	30JAN2010		
		APPLICATION SPEC	30JAN2010		
		WEIGHT	30JAN2010		
		CUSTOMER DRAWING	30JAN2010		
		SCALE	1:1		
		SHEET	4		
		OF	5		
		REV	H		

LOC	DIST	REVISONS				
P	LTR	DESCRIPTION	DATE	DWN	APVD	
-	-	SEE SHEET 1	-	-	-	



LED HOLE DESIGNATIONS
VIEWED FROM COMPONENT SIDE



THIS DRAWING IS A CONTROLLED DOCUMENT.		DWN TOMMY REN 30JAN2010	TE Connectivity	
DIMENSIONS: mm		CHE TONY YUAN 30JAN2010	NAME	
TOLERANCES UNLESS OTHERWISE SPECIFIED:		APVD KEITH ZHU 30JAN2010	PRODUCT SPEC	
0 PLC ±			2X6 MAG45(TM), GIGABIT S8G56 CIRCUIT	
1 PLC ±			WAVE PANEL, GROUND SHIELD, W/ LEDS	
2 PLC ±0.25			APPLICATION SPEC	
3 PLC ±			SIZE	
4 PLC ±			CAGE CODE	
ANGLES ±			DRAWING NO	
FINISH			RESTRICTED TO	
MATERIAL			A100779C=1840326	
CUSTOMER DRAWING			SCALE 1:1	
			SHEET 5 OF 5	
			REV H	