

[Products](#) > [Clocks & Timing](#) > [Clock Distribution](#) > [Zero Delay Buffers \(PLL\)](#) > [Dual Channel DDRII Zero Delay Buffer](#)

Status: **Active**

Product Quick Links

Specifications

Product Options

Documentation & Tools

Technical Support

Contact Sales

9P946

Dual Channel DDRII Zero Delay Buffer

Dual DDR zero delay buffer

Features

- High performance, low jitter zero delay buffer
- I2C for functional and output control
- Dual bank 1-6 differential clock distribution
- 2 separate feedback in & out for input to output
- synchronization for each bank Supports up to 4 DDR DIMMs
- Supports up to 533MHz (DDR II 1066)

Product Specification

OUTPUTS (#)	OUTPUT TYPE	OUTPUT FREQ RANGE (MHZ)	INPUTS (#)	INPUT SIGNAL	OUTPUT VOLTAGE (V)	OUTPUT SKEW (PS)
12	SSTL-2	125.000000 - 533.000000			1.8	40

Product Options

ORDERABLE PART ID	PART STATUS	PKG. CODE	PKG. TYPE	LEAD COUNT (#)	TEMP. GRADE	PB (LEAD) FREE	ORDER
9P946AFLF	Active	PVG48	SSOP	48	C	Y	Check Inventory
9P946AFLFT	Active	PVG48	SSOP	48	C	Y	Check Inventory

Documents

	TITLE	TYPE ▲	FORMAT	FILE SIZE	DATE
	PC Clocks Contact Info	Misc	PDF	62 KB	05/29/2007

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