

89KTPES32NT24AG2 Active

Evaluation Board SDK for the 89H32NT24A

The EB-LOGAN-23 Evaluation Board provides an evaluation platform for both the PES32NT24AG2 and PES32NT24BG2 switches and for several other members of this switch family including PES32NT8AG2, PES32NT8BG2, and PES24NT6AG2.

The evaluation board, along with additional adapters and daughter boards provide by IDT, can be configured to test every possible combination of the number of lanes and ports offered by the switch. Advanced capabilities such as switch partitioning, NTB, DMA, and local port clocking can be evaluated with the evaluation board.

The EB-LOGAN-23 brings out all 32 lanes of the device to 4 Mezzanine connectors located close to the device — one connector per stack of 8 lanes. Various types of daughter cards (provided by IDT) can then be plugged into these connectors to facilitate connectivity to one x8 or two x4 or four x2 or eight x1 link partners. Link partners may be plugged directly into these daughter cards, or they can be connected to these daughter cards via SAS or SATA cables and a different board with PCIe slots known as the 12-PACK board (provided by IDT). Given that the majority of the hosts/servers offer PCIe standard slots, IDT provides the necessary adapter cards that may be plugged into these host/server slots as well as the cables that connect such adapters to the daughter cards, which in turn are plugged into the main evaluation board on which the IDT PCIe switch device is populated.

Features

- **Hardware**
 - 32NT24AG2 PCIe 24-port switch
 - Upstream, Downstream Ports
 - Numerous user selectable configurations set using onboard jumpers and DIP-switches
 - SMBUS Slave Interface (4 pin header)
 - SMBUS Master Interface connected to the Serial EEPROMs and I/O Expanders
 - Push button for Warm Reset
 - Many LEDs to display status, reset, power, hot-plug, etc.
 - JTAG connector to the 32NT24AG2 JTAG pins
- **Software**
 - GUI based application for Windows and Linux
- **Other**
 - SMBUS cable/dongle may be required for certain evaluation exercises.
 - SMA/SATA connectors are provided on the EB-LOGAN-23 board for clock outputs.

Product Options

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Orderable Part ID	Part Status	Buy Sample
89KTPES32NT24AG2	Active	Availability

Technical Documentation

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Title	Other Languages	Type	Format	File Size	Date
User Guides & Manuals					
EB-LOGAN-23 REV-B Evaluation Board User Manual (Fab: 18-691-001)	-	Manual - Eval Board	PDF	6.93 MB	Feb 15, 2011
PCI Express Browser Rev 5.0.2 User Manual	-	Manual	PDF	2.49 MB	Dec 2, 2010
Other					
IDT PCI Express Solutions Overview	日本語, 简体中文	Overview	PDF	768 KB	Apr 19, 2017
89HPES32NT24AG2 Based REV-B Eval Board Schematics (Fab: 18-691-001)	-	Schematic	PDF	6.05 MB	Jun 30, 2010
89HPES32NT24AG2 Based REV-A Eval Board Schematics (Fab: 18-691-000)	-	Schematic	PDF	5.30 MB	Apr 18, 2010

Software & Tools

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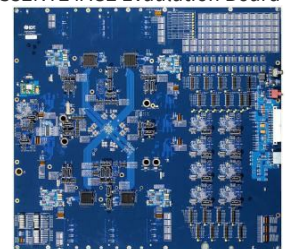
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Title		Other Languages	Type	Format	File Size	Date
PCI Express Browser Rev 5.0.2 Windows Installation - Digitally Signed		-	Software Tool	ZIP	104.57 MB	Jun 16, 2011
PCI Express Browser Rev 5.0.2 Windows Installation		-	Software Tool	ZIP	54.45 MB	Dec 2, 2010
PCI Express Browser Rev 4.9 Linux Installation		-	Software Tool	ZIP	48.85 MB	Jan 18, 2009

Related Products

Product ID	Title ▼
89H32NT24AG2	32-lane, 24-port PCIe Gen2 System Interconnect Switch
	32-lane, 24-port PCIe Gen2 System Interconnect Switch
89H32NT24BG2	System Interconnect Switch