HIGH SPEED LAN MAGNETICS

960027A

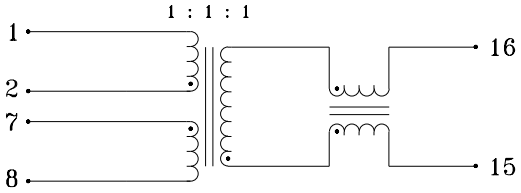
- Designed for use with Microlinear ML6692/6694/6698 10/100 Mbps PHY transceivers
- Low cost, space efficient surface mount packaging
- 350µH OCL (inductance) with 8mA DC bias applied
- 2000 Vrms isolation

ELECTRICALS AT 25ºC

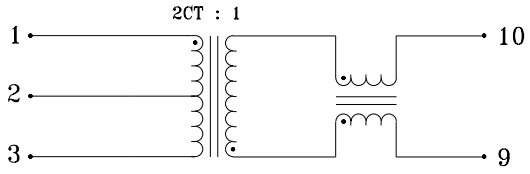
Part No.	Turns Ratio	Insertion Loss (dB) Max 1MHz-100MHz	Return Loss (dB) Min 1MHz-30MHz	Return Loss (dB) Min 30MHz-80MHz	Common to Diff Mode Rej (dB) Min 30MHz	Common to Diff Mode Rej (dB) Min 100MHz	Common to Common Mode Rej (dB) Min 30MHz	Common to Common Mode Rej (dB) Min 100MHz	Schematic
S558-5999-16	1:1:1	-1.0	-18	-12	-50	-40	-40	-30	A
S558-5999-17	2CT:1	-1.0	-18	-12	-50	-40	-40	-30	B

SCHEMATICS

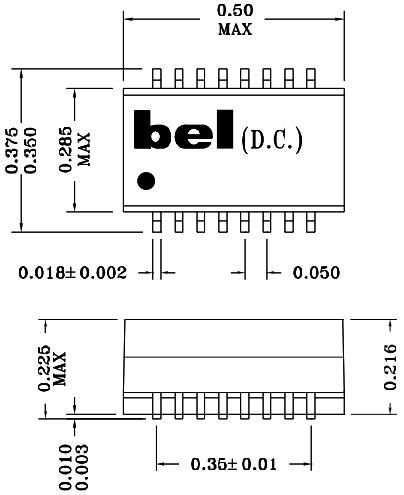
A
Receive

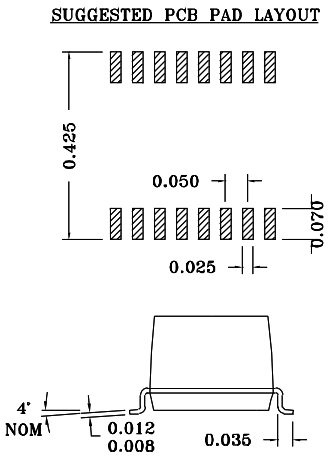


B
Transmit



MECHANICAL





Specifications subject to change without notice.

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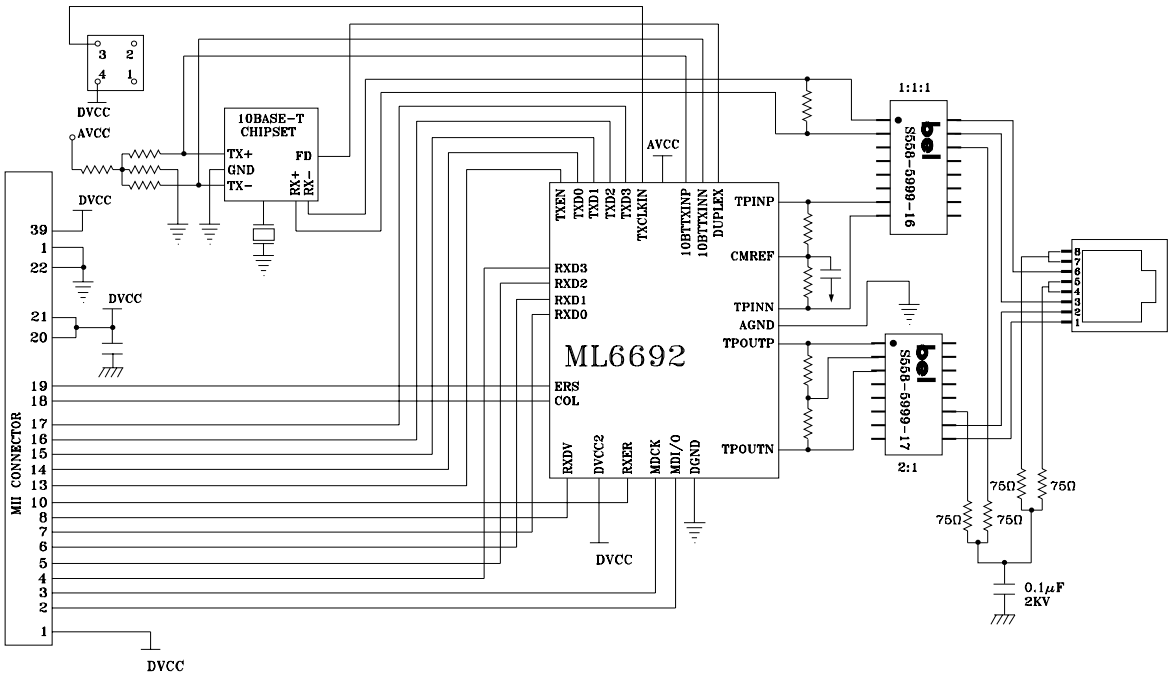
APPLICATION CIRCUIT



APPLICATION NOTES

- These Bel part types have been designed for use in 10 Mbps or 10/100 Mbps data transmission systems over category 5 UTP/STP cable. The combination of these two parts, when used in the recommended application circuit, provides superior EMI noise suppression, high voltage isolation, wave shaping and fast, but controlled rise times. All parts meet IEEE 802.3 standards, which includes 350μH OCL (inductance) when 8mA of DC bias is applied.
- The Bel part types have been tested and qualified for use with Microlinear ML6692/6694/6698 transceivers.
- Bel's low profile, surface mount packaging is ideal for high speed pick and place machinery. Parts can be shipped on tape and reel for high speed placement. Construction processes have been implemented for thermal compatibility with high temperature IR reflow assembly processing. Post dipping of leads assist with PC board solderability. Each part is optically inspected to meet rigid coplanarity requirements.

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