

SN54HC251, SN74HC251 DATA SELECTORS/MUXES WITH 3-STATE OUTPUTS

SCLS132E – DECEMBER 1982 – REVISED SEPTEMBER 2003

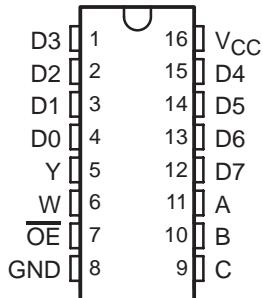
- 3-State Version of 'HC151
- Wide Operating Voltage Range of 2 V to 6 V
- High-Current 3-State Outputs Interface Directly With System Bus or Can Drive Up To 15 LSTTL Loads
- Low Power Consumption, 80- μ A Max I_{CC}
- Typical $t_{PD} = 9$ ns
- ± 6 -mA Output Drive at 5 V
- Low Input Current of 1 μ A Max
- Perform Parallel-to-Serial Conversion
- Complementary Outputs Provide True and Inverted Data

description/ordering information

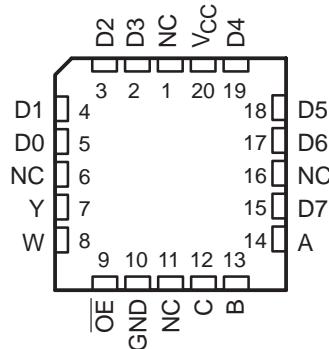
These data selectors/multiplexers contain full binary decoding to select 1-of-8 data sources and feature strobe-controlled complementary 3-state outputs.

The 3-state outputs can interface with and drive data lines of bus-organized systems. With all but one of the common outputs disabled (in the high-impedance state), the low impedance of the single enabled output drives the bus line to a high or low logic level. Both outputs are controlled by the output-enable (OE) input. The outputs are disabled when OE is high.

SN54HC251 . . . J OR W PACKAGE
C251 . . . D, DB, N, NS, OR PW PACKAGE
(TOP VIEW)



**SN54HC251 . . . FK PACKAGE
(TOP VIEW)**



NC – No internal connection

ORDERING INFORMATION

| ORDERING INFORMATION | | | | |
|----------------------|------------|--------------|-----------------------|------------------|
| TA | PACKAGE† | | ORDERABLE PART NUMBER | TOP-SIDE MARKING |
| -40°C to 85°C | PDIP - N | Tube of 25 | SN74HC251N | SN74HC251N |
| | SOIC - D | Tube of 40 | SN74HC251D | HC251 |
| | | Reel of 2500 | SN74HC251DR | |
| | | Reel of 250 | SN74HC251DT | |
| | SOP - NS | Reel of 2000 | SN74HC251NSR | HC251 |
| | SSOP - DB | Reel of 2000 | SN74HC251DBR | HC251 |
| | TSSOP - PW | Tube of 90 | SN74HC251PW | HC251 |
| | | Reel of 2000 | SN74HC251PWR | |
| | | Reel of 250 | SN74HC251PWT | |
| -55°C to 125°C | CDIP - J | Tube of 25 | SNJ54HC251J | SNJ54HC251J |
| | CFP - W | Tube of 150 | SNJ54HC251W | SNJ54HC251W |
| | LCCC - FK | Tube of 55 | SNJ54HC251FK | SNJ54HC251FK |

[†] Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.



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PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



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On products compliant to MIL-PRF-38535, all parameters are tested
unless otherwise noted. On all other products, production
processing does not necessarily include testing of all parameters.

**SN54HC251, SN74HC251
DATA SELECTORS/MULTIPLEXERS
WITH 3-STATE OUTPUTS**

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FUNCTION TABLE

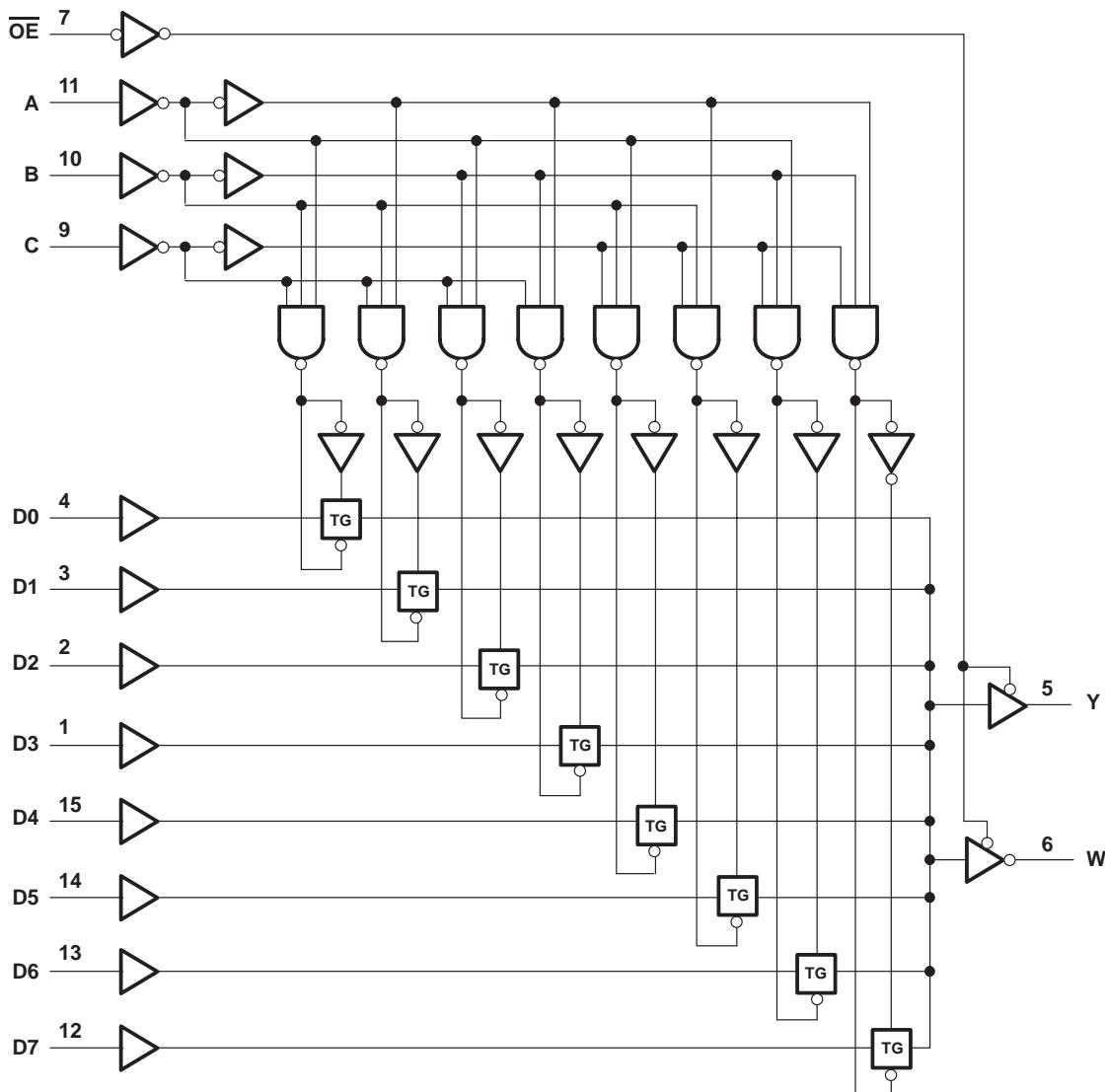
| INPUTS | | | OUTPUTS | | |
|--------|---|---|-----------------|----|-----------------|
| SELECT | | | \overline{OE} | | |
| C | B | A | | Y | W |
| X | X | X | H | Z | Z |
| L | L | L | L | D0 | $\overline{D0}$ |
| L | L | H | L | D1 | $\overline{D1}$ |
| L | H | L | L | D2 | $\overline{D2}$ |
| L | H | H | L | D3 | $\overline{D3}$ |
| H | L | L | L | D4 | $\overline{D4}$ |
| H | L | H | L | D5 | $\overline{D5}$ |
| H | H | L | L | D6 | $\overline{D6}$ |
| H | H | H | L | D7 | $\overline{D7}$ |

D0, D1 . . . D7 = the level of the respective D input



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logic diagram (positive logic)



Pin numbers shown are for the D, DB, J, N, NS, PW, and W packages.

**SN54HC251, SN74HC251
DATA SELECTORS/MUXES
WITH 3-STATE OUTPUTS**

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absolute maximum ratings over operating free-air temperature range (unless otherwise noted)[†]

| | | | |
|---|----------------|--|--|
| Supply voltage range, V_{CC} | –0.5 V to 7 V | | |
| Input clamp current, I_{IK} ($V_I < 0$ or $V_I > V_{CC}$) (see Note 1) | ± 20 mA | | |
| Output clamp current, I_{OK} ($V_O < 0$ or $V_O > V_{CC}$) (see Note 1) | ± 20 mA | | |
| Continuous output current, I_O ($V_O = 0$ to V_{CC}) | ± 35 mA | | |
| Continuous current through V_{CC} or GND | ± 70 mA | | |
| Package thermal impedance, θ_{JA} (see Note 2): D package | 73°C/W | | |
| DB package | 82°C/W | | |
| N package | 67°C/W | | |
| NS package | 64°C/W | | |
| PW package | 108°C/W | | |
| Storage temperature range, T_{stg} | –65°C to 150°C | | |

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.
2. The package thermal impedance is calculated in accordance with JEDEC 51-7.

recommended operating conditions (see Note 3)

| | | SN54HC251 | | | SN74HC251 | | | UNIT |
|---------------------|---------------------------------|------------------|----------|------|-----------|----------|-----|------|
| | | MIN | NOM | MAX | MIN | NOM | MAX | |
| V_{CC} | Supply voltage | 2 | 5 | 6 | 2 | 5 | 6 | V |
| V_{IH} | High-level input voltage | $V_{CC} = 2$ V | 1.5 | | 1.5 | | | V |
| | | $V_{CC} = 4.5$ V | 3.15 | | 3.15 | | | |
| | | $V_{CC} = 6$ V | 4.2 | | 4.2 | | | |
| V_{IL} | Low-level input voltage | $V_{CC} = 2$ V | | 0.5 | | 0.5 | | V |
| | | $V_{CC} = 4.5$ V | | 1.35 | | 1.35 | | |
| | | $V_{CC} = 6$ V | | 1.8 | | 1.8 | | |
| V_I | Input voltage | 0 | V_{CC} | | 0 | V_{CC} | | V |
| V_O | Output voltage | 0 | V_{CC} | | 0 | V_{CC} | | V |
| $\Delta t/\Delta v$ | Input transition rise/fall time | $V_{CC} = 2$ V | | 1000 | | 1000 | | ns |
| | | $V_{CC} = 4.5$ V | | 500 | | 500 | | |
| | | $V_{CC} = 6$ V | | 400 | | 400 | | |
| T_A | Operating free-air temperature | –55 | 125 | | –40 | 85 | | °C |

NOTE 3: All unused inputs of the device must be held at V_{CC} or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

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electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

| PARAMETER | TEST CONDITIONS | V _{CC} | T _A = 25°C | | | SN54HC251 | | SN74HC251 | | UNIT |
|-----------------|--|---------------------------|-----------------------|-------|-------|-----------|-----|-----------|-----|------|
| | | | MIN | TYP | MAX | MIN | MAX | MIN | MAX | |
| V _{OH} | V _I = V _{IH} or V _{IL} | I _{OH} = -20 µA | 2 V | 1.9 | 1.998 | 1.9 | | 1.9 | | V |
| | | | 4.5 V | 4.4 | 4.499 | 4.4 | | 4.4 | | |
| | | | 6 V | 5.9 | 5.999 | 5.9 | | 5.9 | | |
| | | I _{OH} = -6 mA | 4.5 V | 3.98 | 4.3 | 3.7 | | 3.84 | | |
| | | I _{OH} = -7.8 mA | 6 V | 5.48 | 5.8 | 5.2 | | 5.34 | | |
| V _{OL} | V _I = V _{IH} or V _{IL} | I _{OL} = 20 µA | 2 V | 0.002 | 0.1 | 0.1 | | 0.1 | | V |
| | | | 4.5 V | 0.001 | 0.1 | 0.1 | | 0.1 | | |
| | | | 6 V | 0.001 | 0.1 | 0.1 | | 0.1 | | |
| | | I _{OL} = 6 mA | 4.5 V | 0.17 | 0.26 | 0.4 | | 0.33 | | |
| | | I _{OL} = 7.8 mA | 6 V | 0.15 | 0.26 | 0.4 | | 0.33 | | |
| I _I | V _I = V _{CC} or 0 | | 6 V | ±0.1 | ±100 | ±1000 | | ±1000 | | nA |
| I _{OZ} | V _O = V _{CC} or 0, V _I = V _{IH} or V _{IL} | | 6 V | ±0.01 | ±0.5 | ±10 | | ±5 | | µA |
| I _{CC} | V _I = V _{CC} or 0, I _O = 0 | | 6 V | | 8 | 160 | | 80 | | µA |
| C _i | | | 2 V to 6 V | 3 | 10 | 10 | | 10 | | pF |

switching characteristics over recommended operating free-air temperature range, C_L = 50 pF (unless otherwise noted) (see Figure 1)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | V _{CC} | T _A = 25°C | | | SN54HC251 | | SN74HC251 | | UNIT |
|------------------|--------------|-------------|-----------------|-----------------------|-----|-----|-----------|-----|-----------|-----|------|
| | | | | MIN | TYP | MAX | MIN | MAX | MIN | MAX | |
| t _{pd} | A, B, or C | W or Y | 2 V | 58 | 205 | | 300 | | 256 | | ns |
| | | | 4.5 V | 21 | 41 | | 60 | | 51 | | |
| | | | 6 V | 19 | 35 | | 51 | | 44 | | |
| | Any D | W or Y | 2 V | 44 | 195 | | 283 | | 244 | | |
| | | | 4.5 V | 17 | 39 | | 57 | | 49 | | |
| | | | 6 V | 15 | 33 | | 48 | | 41 | | |
| t _{en} | OE | W or Y | 2 V | 30 | 145 | | 210 | | 181 | | ns |
| | | | 4.5 V | 10 | 29 | | 42 | | 36 | | |
| | | | 6 V | 9 | 25 | | 36 | | 31 | | |
| t _{dis} | OE | W or Y | 2 V | 25 | 195 | | 283 | | 244 | | ns |
| | | | 4.5 V | 15 | 39 | | 57 | | 49 | | |
| | | | 6 V | 14 | 33 | | 48 | | 41 | | |
| t _t | | W or Y | 2 V | 20 | 75 | | 110 | | 95 | | ns |
| | | | 4.5 V | 8 | 15 | | 22 | | 19 | | |
| | | | 6 V | 6 | 13 | | 19 | | 16 | | |

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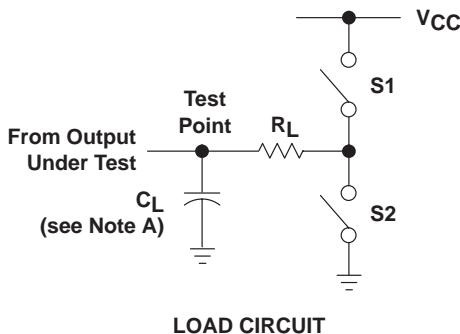
switching characteristics over recommended operating free-air temperature range, $C_L = 150 \text{ pF}$ (unless otherwise noted) (see Figure 1)

| PARAMETER | FROM (INPUT) | TO (OUTPUT) | V _{CC} | T _A = 25°C | | | SN54HC251 | SN74HC251 | UNIT |
|-----------------|-----------------|----------------|-----------------|-----------------------|-----|-----|-----------|-----------|------|
| | | | | MIN | TYP | MAX | MIN | MAX | |
| t _{pd} | A, B, or C | W or Y | 2 V | 72 | 300 | 450 | 375 | 375 | ns |
| | | | 4.5 V | 25 | 60 | 90 | 75 | 75 | |
| | | | 6 V | 22 | 52 | 77 | 65 | 65 | |
| | Any D | W or Y | 2 V | 59 | 300 | 450 | 375 | 375 | |
| | | | 4.5 V | 21 | 60 | 90 | 75 | 75 | |
| | | | 6 V | 18 | 52 | 77 | 65 | 65 | |
| t _{en} | OE | W or Y | 2 V | 50 | 230 | 340 | 285 | 285 | ns |
| | | | 4.5 V | 17 | 46 | 68 | 57 | 57 | |
| | | | 6 V | 15 | 40 | 58 | 50 | 50 | |
| t _t | | W or Y | 2 V | 45 | 210 | 315 | 265 | 265 | ns |
| | | | 4.5 V | 17 | 42 | 63 | 53 | 53 | |
| | | | 6 V | 13 | 36 | 53 | 45 | 45 | |

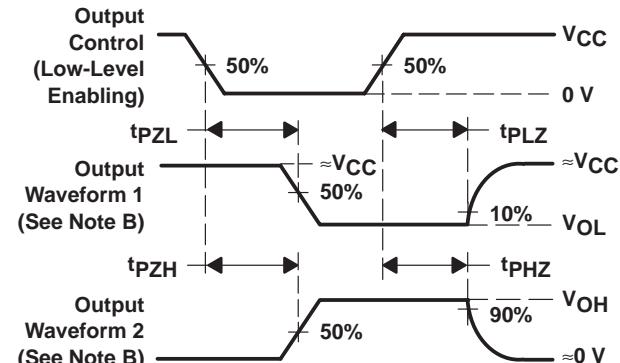
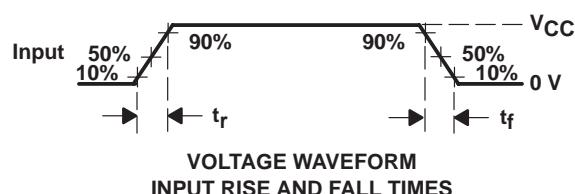
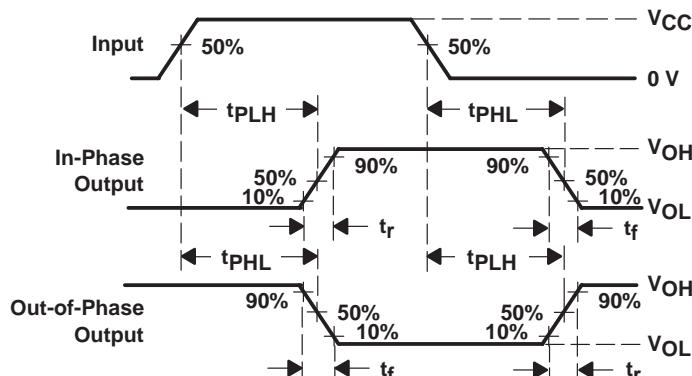
operating characteristics, T_A = 25°C

| PARAMETER | TEST CONDITIONS | TYP | UNIT |
|---|-----------------|-----|------|
| C _{pd} Power dissipation capacitance | No load | 70 | pF |

PARAMETER MEASUREMENT INFORMATION



| PARAMETER | R_L | C_L | S1 | S2 |
|-------------------|--------------|-----------------------|--------|--------|
| t_{en} | 1 k Ω | 50 pF or 150 pF | Open | Closed |
| | | | Closed | Open |
| t_{dis} | 1 k Ω | 50 pF | Open | Closed |
| | | | Closed | Open |
| t_{pd} or t_t | -- | 50 pF or 150 pF | Open | Open |



NOTES: A. C_L includes probe and test-fixture capacitance.
 B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
 C. Phase relationships between waveforms were chosen arbitrarily. All input pulses are supplied by generators having the following characteristics: PRR \leq 1 MHz, $Z_O = 50 \Omega$, $t_r = 6$ ns, $t_f = 6$ ns.
 D. The outputs are measured one at a time with one input transition per measurement.
 E. t_{PLZ} and t_{PHZ} are the same as t_{dis} .
 F. t_{PLZ} and t_{PZH} are the same as t_{en} .
 G. t_{PLH} and t_{PHL} are the same as t_{pd} .

Figure 1. Load Circuit and Voltage Waveforms

PACKAGING INFORMATION

| Orderable Device | Status ⁽¹⁾ | Package Type | Package Drawing | Pins | Package Qty | Eco Plan ⁽²⁾ | Lead/Ball Finish | MSL Peak Temp ⁽³⁾ |
|------------------|-----------------------|--------------|-----------------|------|-------------|-------------------------|------------------|------------------------------|
| 5962-8512501VEA | ACTIVE | CDIP | J | 16 | 1 | TBD | A42 | N / A for Pkg Type |
| 85125012A | ACTIVE | LCCC | FK | 20 | 1 | TBD | POST-PLATE | N / A for Pkg Type |
| 8512501EA | ACTIVE | CDIP | J | 16 | 1 | TBD | A42 | N / A for Pkg Type |
| SN54HC251J | ACTIVE | CDIP | J | 16 | 1 | TBD | A42 | N / A for Pkg Type |
| SN74HC251D | ACTIVE | SOIC | D | 16 | 40 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DBR | ACTIVE | SSOP | DB | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DBRE4 | ACTIVE | SSOP | DB | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DBRG4 | ACTIVE | SSOP | DB | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DE4 | ACTIVE | SOIC | D | 16 | 40 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DG4 | ACTIVE | SOIC | D | 16 | 40 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DR | ACTIVE | SOIC | D | 16 | 2500 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DRE4 | ACTIVE | SOIC | D | 16 | 2500 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DRG4 | ACTIVE | SOIC | D | 16 | 2500 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DT | ACTIVE | SOIC | D | 16 | 250 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DTE4 | ACTIVE | SOIC | D | 16 | 250 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251DTG4 | ACTIVE | SOIC | D | 16 | 250 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251N | ACTIVE | PDIP | N | 16 | 25 | Pb-Free (RoHS) | CU NIPDAU | N / A for Pkg Type |
| SN74HC251NE4 | ACTIVE | PDIP | N | 16 | 25 | Pb-Free (RoHS) | CU NIPDAU | N / A for Pkg Type |
| SN74HC251NSR | ACTIVE | SO | NS | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251NSRE4 | ACTIVE | SO | NS | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251NSRG4 | ACTIVE | SO | NS | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251PW | ACTIVE | TSSOP | PW | 16 | 90 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251PWE4 | ACTIVE | TSSOP | PW | 16 | 90 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251PWG4 | ACTIVE | TSSOP | PW | 16 | 90 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251PWR | ACTIVE | TSSOP | PW | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251PWRE4 | ACTIVE | TSSOP | PW | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251PWRG4 | ACTIVE | TSSOP | PW | 16 | 2000 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |

| Orderable Device | Status ⁽¹⁾ | Package Type | Package Drawing | Pins | Package Qty | Eco Plan ⁽²⁾ | Lead/Ball Finish | MSL Peak Temp ⁽³⁾ |
|------------------|-----------------------|--------------|-----------------|------|-------------|-------------------------|------------------|------------------------------|
| no Sb/Br) | | | | | | | | |
| SN74HC251PWT | ACTIVE | TSSOP | PW | 16 | 250 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251PWTE4 | ACTIVE | TSSOP | PW | 16 | 250 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SN74HC251PWTG4 | ACTIVE | TSSOP | PW | 16 | 250 | Green (RoHS & no Sb/Br) | CU NIPDAU | Level-1-260C-UNLIM |
| SNJ54HC251FK | ACTIVE | LCCC | FK | 20 | 1 | TBD | POST-PLATE | N / A for Pkg Type |
| SNJ54HC251J | ACTIVE | CDIP | J | 16 | 1 | TBD | A42 | N / A for Pkg Type |

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBsolete: TI has discontinued the production of the device.

⁽²⁾ Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

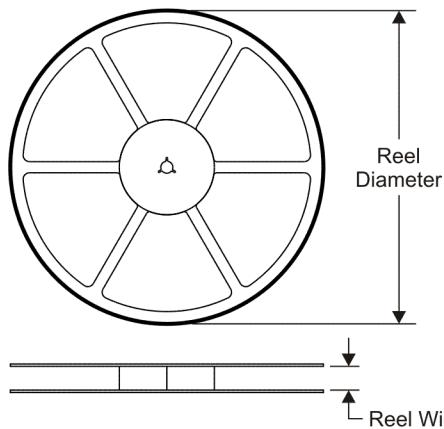
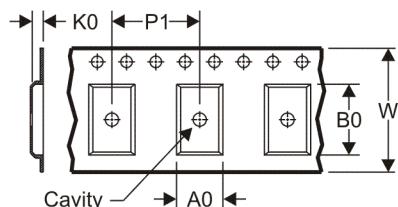
Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

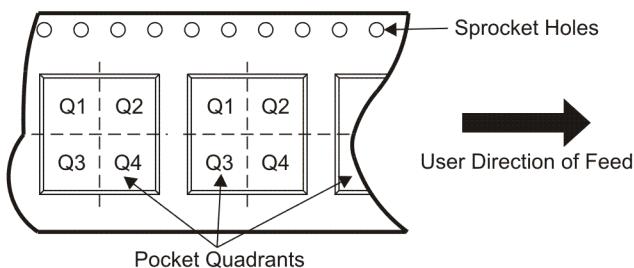
⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

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TAPE AND REEL INFORMATION
REEL DIMENSIONS

TAPE DIMENSIONS


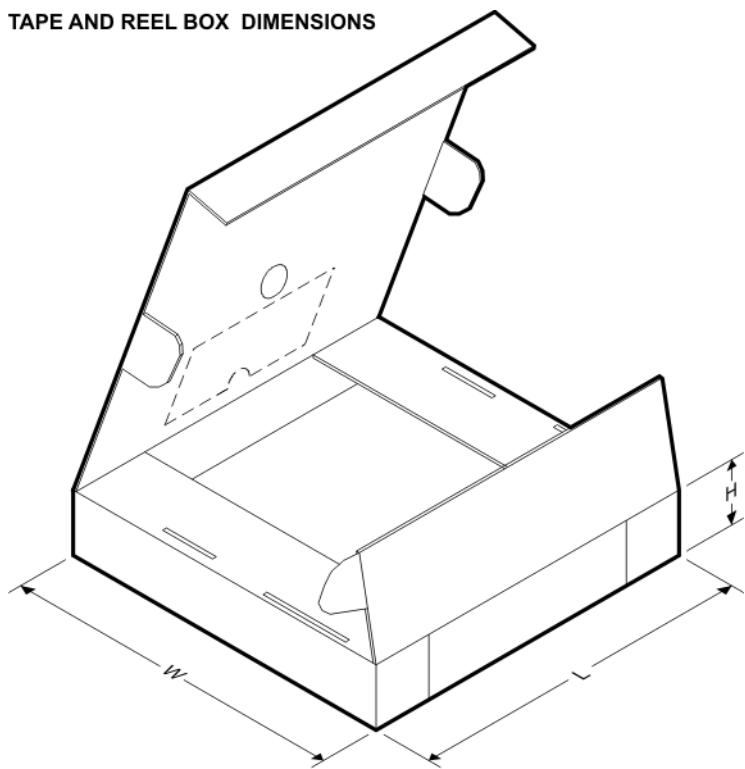
| | |
|----|---|
| A0 | Dimension designed to accommodate the component width |
| B0 | Dimension designed to accommodate the component length |
| K0 | Dimension designed to accommodate the component thickness |
| W | Overall width of the carrier tape |
| P1 | Pitch between successive cavity centers |

QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE


*All dimensions are nominal

| Device | Package Type | Package Drawing | Pins | SPQ | Reel Diameter (mm) | Reel Width W1 (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|--------------|--------------|-----------------|------|------|--------------------|--------------------|---------|---------|---------|---------|--------|---------------|
| SN74HC251DBR | SSOP | DB | 16 | 2000 | 330.0 | 16.4 | 8.2 | 6.6 | 2.5 | 12.0 | 16.0 | Q1 |
| SN74HC251DR | SOIC | D | 16 | 2500 | 330.0 | 16.4 | 6.5 | 10.3 | 2.1 | 8.0 | 16.0 | Q1 |
| SN74HC251NSR | SO | NS | 16 | 2000 | 330.0 | 16.4 | 8.2 | 10.5 | 2.5 | 12.0 | 16.0 | Q1 |
| SN74HC251PWR | TSSOP | PW | 16 | 2000 | 330.0 | 12.4 | 7.0 | 5.6 | 1.6 | 8.0 | 12.0 | Q1 |

TAPE AND REEL BOX DIMENSIONS



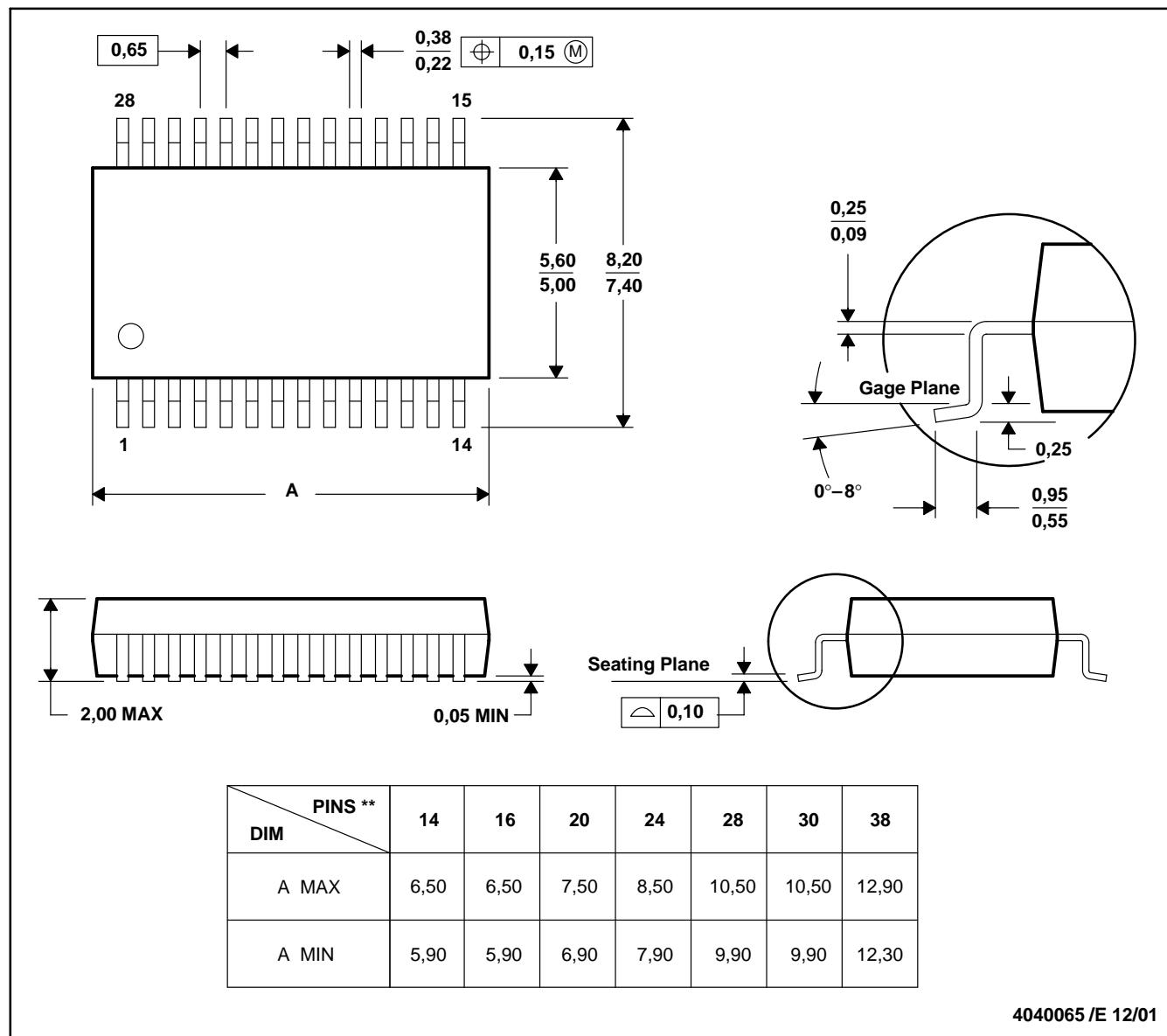
*All dimensions are nominal

| Device | Package Type | Package Drawing | Pins | SPQ | Length (mm) | Width (mm) | Height (mm) |
|--------------|--------------|-----------------|------|------|-------------|------------|-------------|
| SN74HC251DBR | SSOP | DB | 16 | 2000 | 346.0 | 346.0 | 33.0 |
| SN74HC251DR | SOIC | D | 16 | 2500 | 333.2 | 345.9 | 28.6 |
| SN74HC251NSR | SO | NS | 16 | 2000 | 346.0 | 346.0 | 33.0 |
| SN74HC251PWR | TSSOP | PW | 16 | 2000 | 346.0 | 346.0 | 29.0 |

DB (R-PDSO-G**)

PLASTIC SMALL-OUTLINE

28 PINS SHOWN

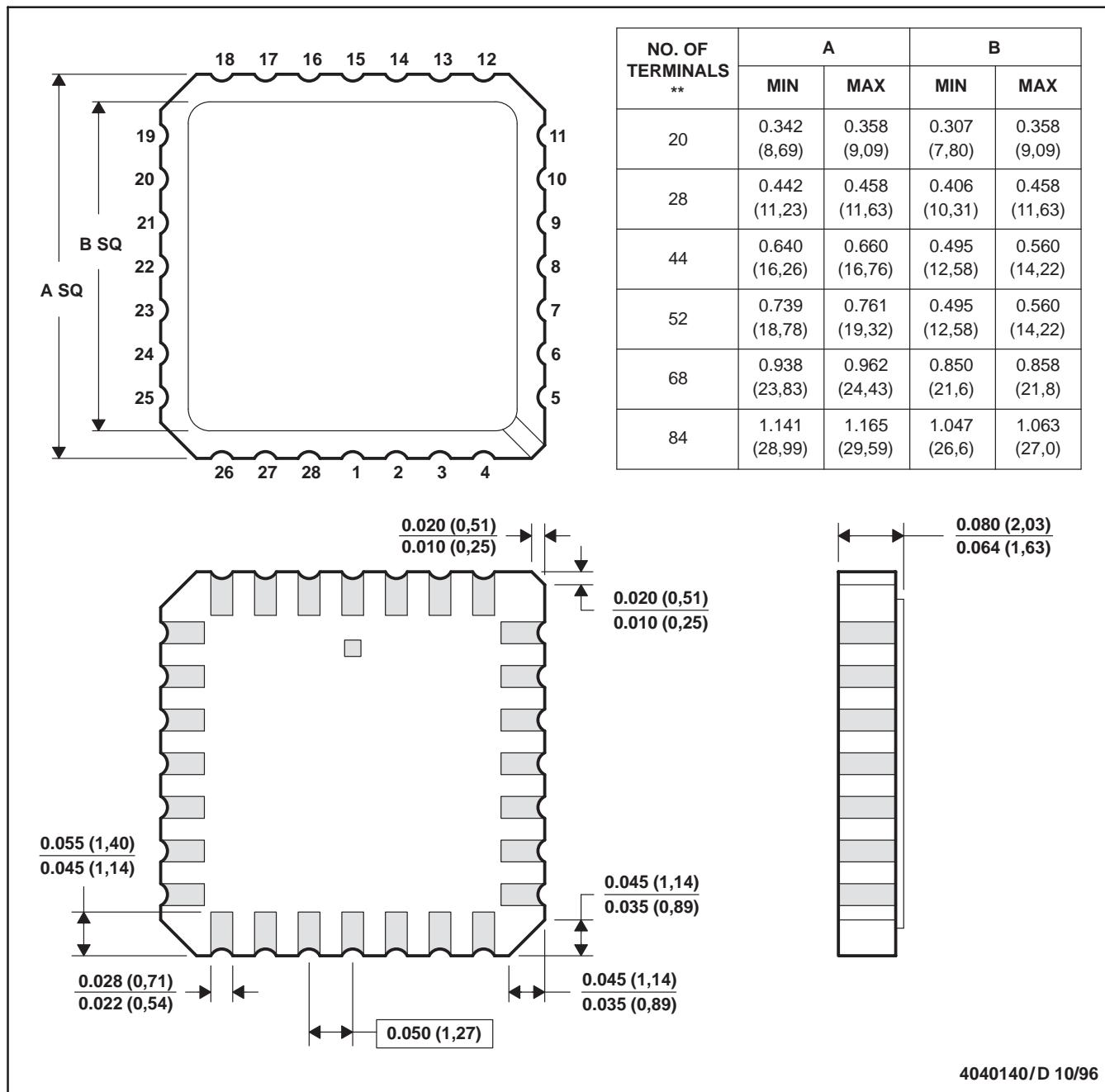


NOTES: A. All linear dimensions are in millimeters.
 B. This drawing is subject to change without notice.
 C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.
 D. Falls within JEDEC MO-150

FK (S-CQCC-N**)

LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



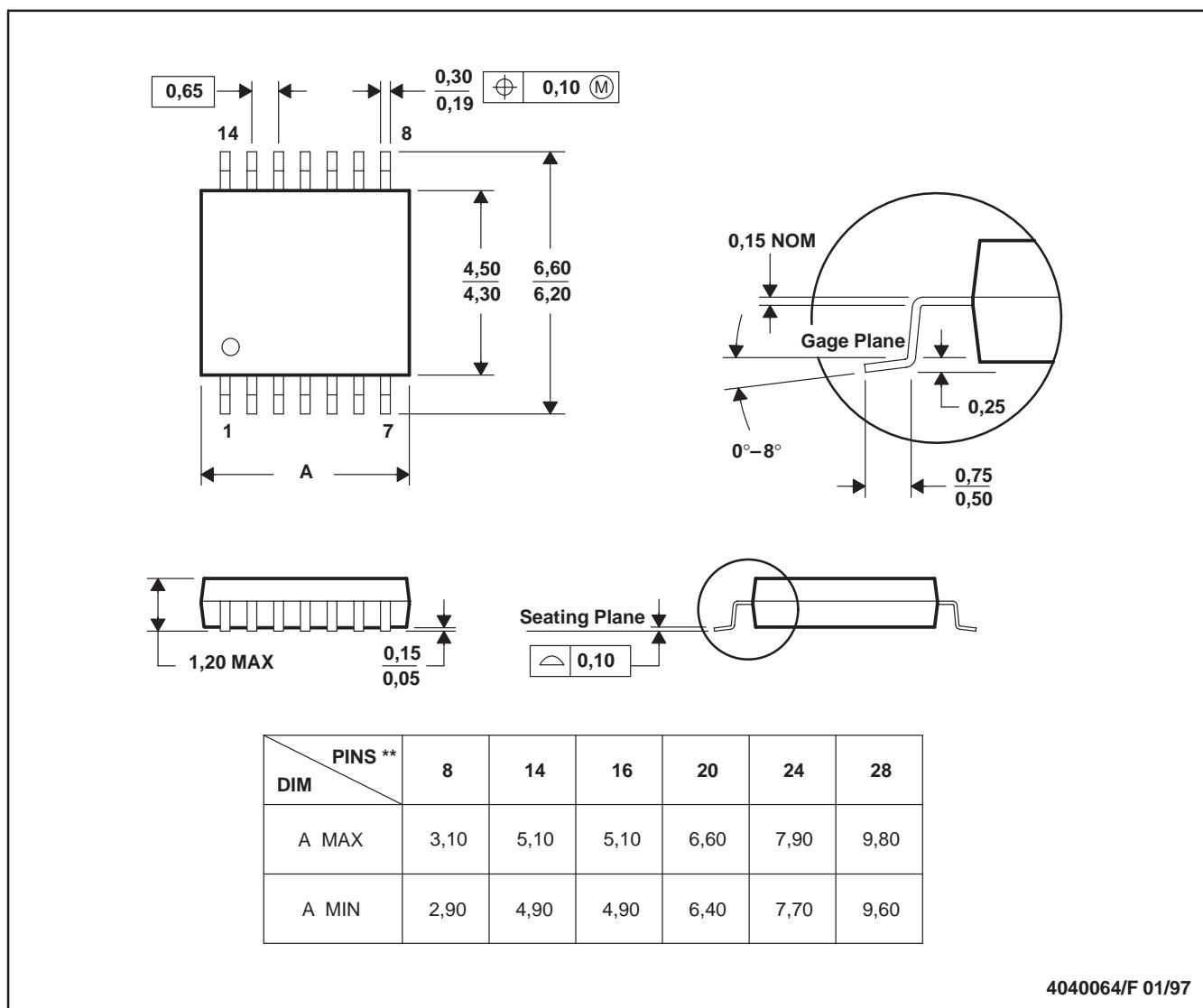
NOTES:

- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. The terminals are gold plated.
- E. Falls within JEDEC MS-004

PW (R-PDSO-G**)

PLASTIC SMALL-OUTLINE PACKAGE

14 PINS SHOWN



NOTES:

- All linear dimensions are in millimeters.
- This drawing is subject to change without notice.
- Body dimensions do not include mold flash or protrusion not to exceed 0,15.
- Falls within JEDEC MO-153

MECHANICAL DATA

NS (R-PDSO-G)**

PLASTIC SMALL-OUTLINE PACKAGE

14-PINS SHOWN



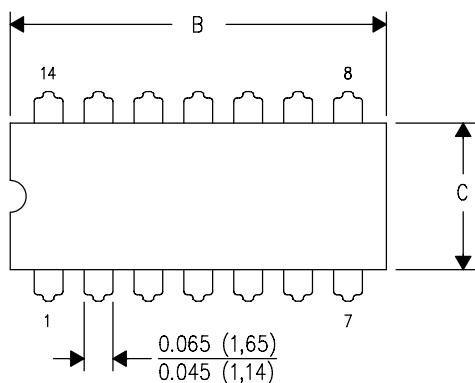
NOTES:

- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.

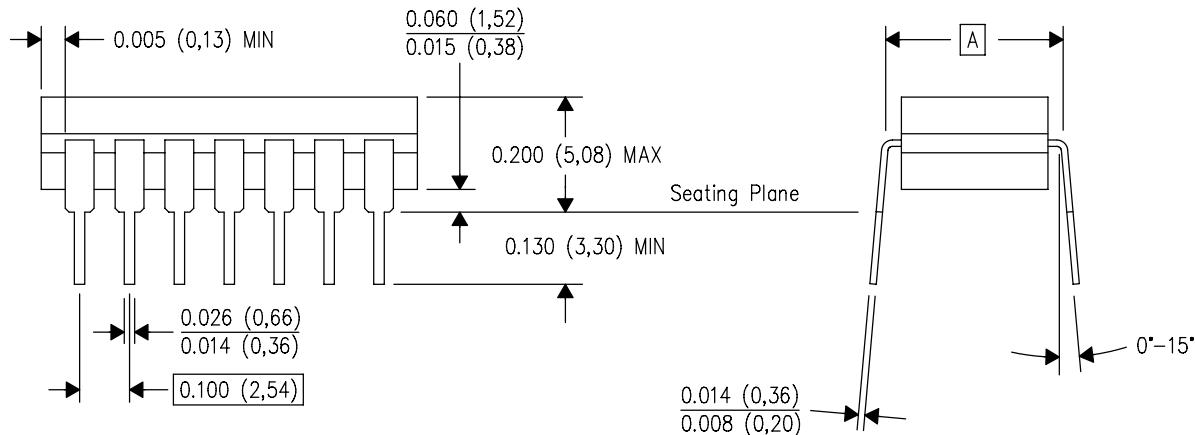
J (R-GDIP-T**)

14 LEADS SHOWN

CERAMIC DUAL IN-LINE PACKAGE



| PINS ** DIM | 14 | 16 | 18 | 20 |
|----------------|------------------------|------------------------|------------------------|------------------------|
| A | 0.300 (7,62) BSC | 0.300 (7,62) BSC | 0.300 (7,62) BSC | 0.300 (7,62) BSC |
| B MAX | 0.785 (19,94) | .840 (21,34) | 0.960 (24,38) | 1.060 (26,92) |
| B MIN | — | — | — | — |
| C MAX | 0.300 (7,62) | 0.300 (7,62) | 0.310 (7,87) | 0.300 (7,62) |
| C MIN | 0.245 (6,22) | 0.245 (6,22) | 0.220 (5,59) | 0.245 (6,22) |

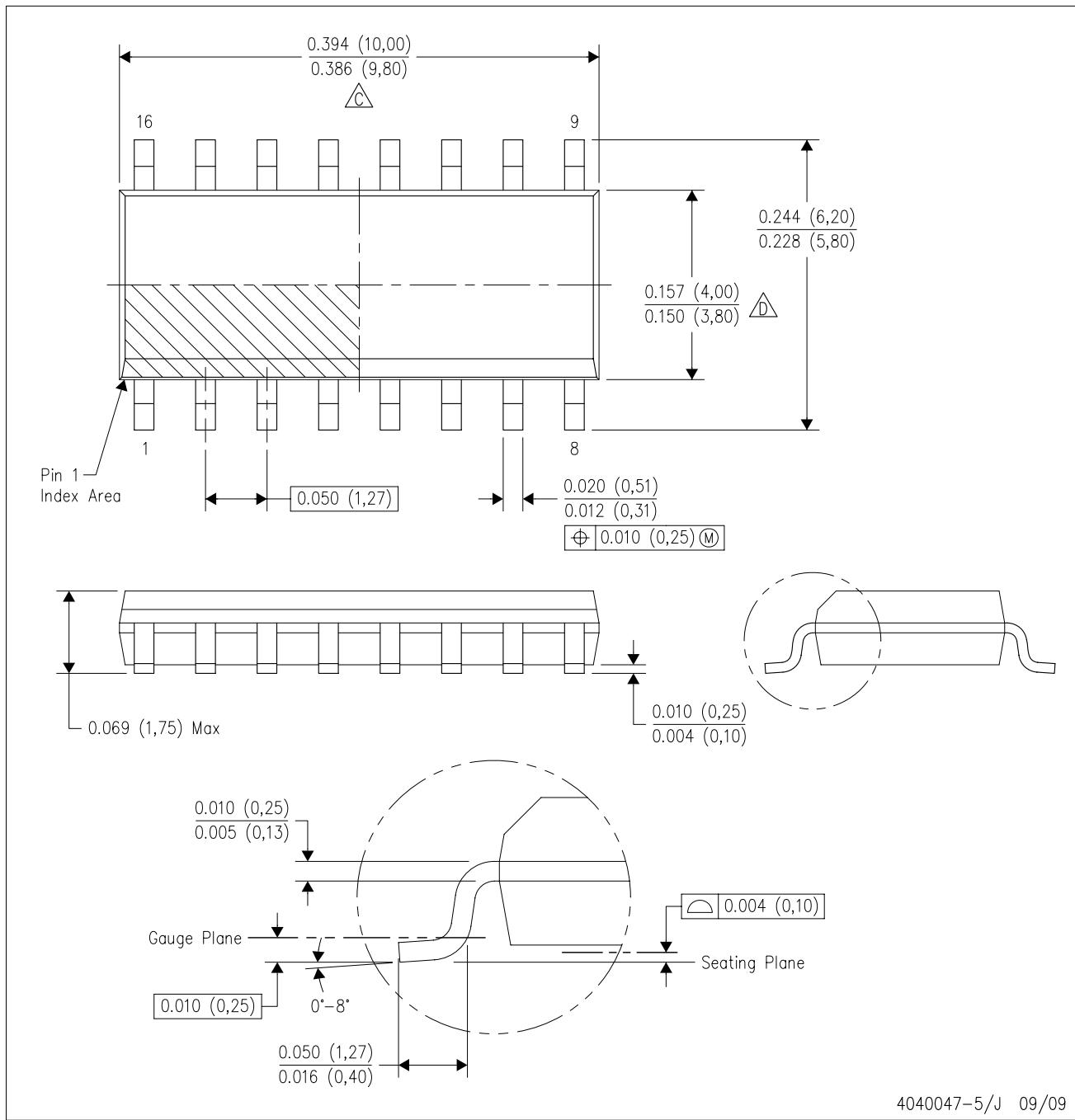


4040083/F 03/03

NOTES: A. All linear dimensions are in inches (millimeters).
B. This drawing is subject to change without notice.
C. This package is hermetically sealed with a ceramic lid using glass frit.
D. Index point is provided on cap for terminal identification only on press ceramic glass frit seal only.
E. Falls within MIL STD 1835 GDIP1-T14, GDIP1-T16, GDIP1-T18 and GDIP1-T20.

D (R-PDSO-G16)

PLASTIC SMALL-OUTLINE PACKAGE



4040047-5/J 09/09

NOTES: A. All linear dimensions are in inches (millimeters).

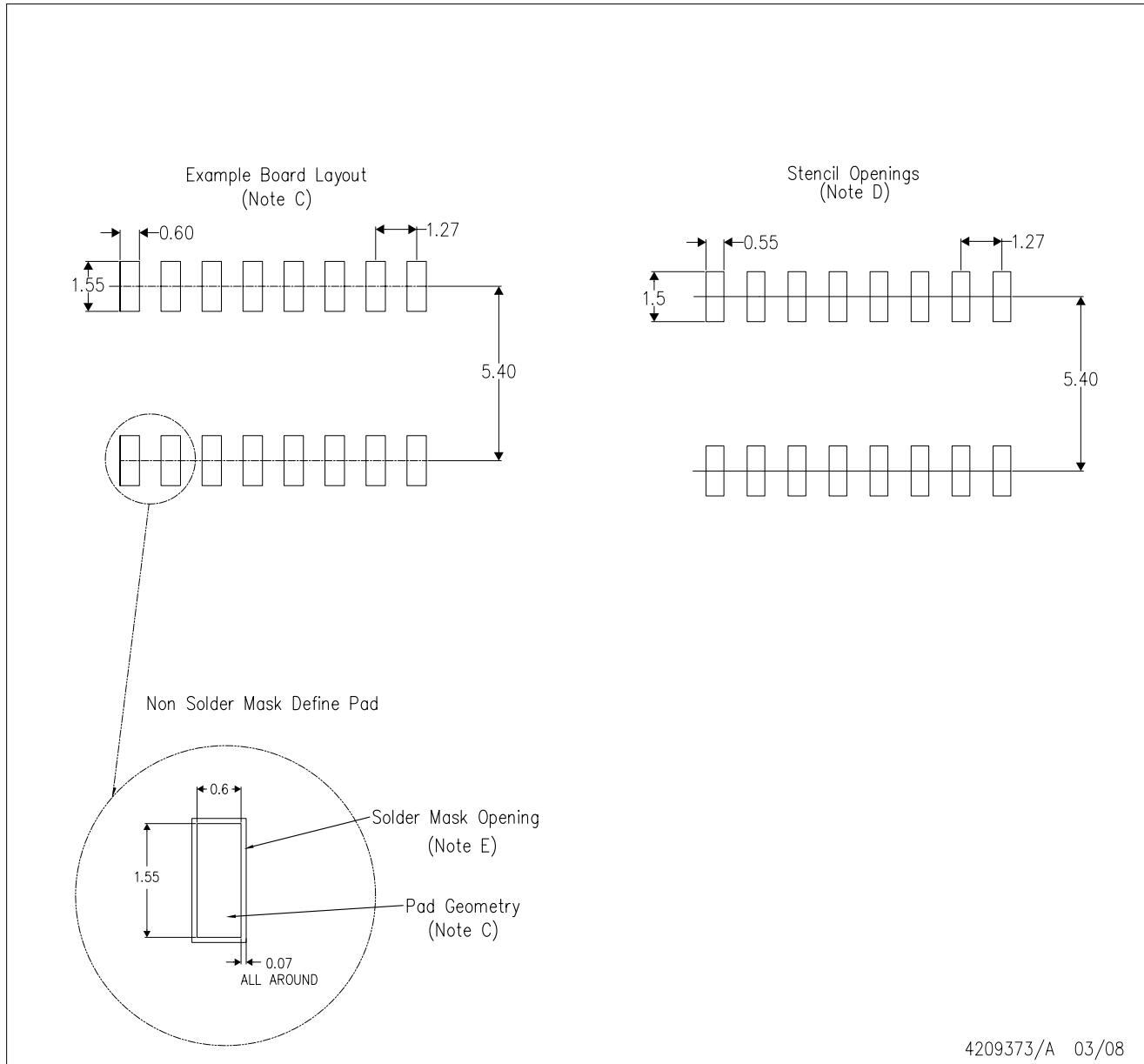
B. This drawing is subject to change without notice.

 Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed .006 (0,15) per end.

 Body width does not include interlead flash. Interlead flash shall not exceed .017 (0,43) per side.
E. Reference JEDEC MS-012 variation AC.

E. Reference JEDEC MS-012 Variation AC.

D(R-PDSO-G16)



NOTES:

- All linear dimensions are in millimeters.
- This drawing is subject to change without notice.
- Refer to IPC7351 for alternate board design.
- Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525
- Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.

N (R-PDIP-T**)

16 PINS SHOWN

PLASTIC DUAL-IN-LINE PACKAGE



NOTES: A. All linear dimensions are in inches (millimeters).
 B. This drawing is subject to change without notice.

△ Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).

△ The 20 pin end lead shoulder width is a vendor option, either half or full width.

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